

COS320 Types III

- hw3 due today
- hw4 released today, due April 1st
(typechecker and translator for Oct)

Oct v2:

- specified by fairly large type system

- Several features:

- * Memory safety
- * Mutable record types
- * Subtyping

non-null ref's are subtype of nullable refs ($\text{ref} <: \text{ref?}$)
with subtyping for record

Compiling w/ a type system

- take intrinsic view: types are syntactic part of program
→ program w/o types is not program.
- so only look at well-typed programs.

- Compiler translates programs in source language to target language.
 - * Explicit typechecking pass to throw out ill-typed programs, or
 - * translation pass to throw exceptions.
 - * Compiler owner ^{← lang checks if front-end produces} target program is well-typed, type-correct code.

Compilation - translation of derivation of judgements from src lang to proofs in target lang

Each kind of judgement has a different translation category

- Well-formed types in src become well-formed types in target
- Exprs in src become (operand, instr list) in target.

Each inference rule corresponds to a case within that category

example: Dat v1

- Implicit type system

$\vdash t$ "t is well formed type"

\vdash_{ref} "ref is well-formed ref-type"

\vdash_{rt} "rt is well-formed return type"

$T_{int} \frac{}{\vdash_{int}}$

$T_{bool} \frac{}{\vdash_{bool}}$

$T_{ref} \frac{\vdash_{ref}}{\vdash_{ref}}$

$RString \frac{}{\vdash_{string}}$

$RArray \frac{\vdash t}{\vdash_{rt}[t]}$

$RFun \frac{\vdash t_1 \dots \vdash t_n \quad \vdash_{rt}}{\vdash (t_1 \dots t_n) \rightarrow rt}$

$RTVoid \frac{}{\vdash_{rt} void}$

$RTTyp \frac{\vdash t}{\vdash_{rt} t}$

LLmdite type system

Judgements:

- $T \vdash t$ within named types T , t is well-formed type.
- $T \vdash_s t$ within named types T , t is well-formed simple type.
- $T \vdash_{ret} t$ within named types T , t is well-formed ret type.
- $T \vdash_{rt} t$ within named types T , t is w.f. return type.

$$\begin{array}{l} \text{LLBool} \frac{}{T \vdash_s !} \quad \text{LLint} \frac{}{T \vdash_s !} \quad \text{LLptr} \frac{T \vdash_{ret}}{T \vdash_s \text{ret}} \\ \text{LLTuple} \frac{T \vdash t_1 \dots T \vdash t_n}{T \vdash \{t_1, \dots, t_n\}} \quad \text{LLArray} \frac{T \vdash t}{T \vdash [t]} \quad \text{LLSimple} \frac{T \vdash t}{T \vdash t} \end{array}$$

$$\text{LLR+Void} \frac{}{} \quad \text{LLR+Simple} \frac{}{} \quad \text{LLR+Char} \frac{}{T \vdash_s !}$$

$$\text{LLR+type} \frac{T \vdash t}{T \vdash t} \quad \text{LLR+fun} \frac{}{} \quad \text{LLR+void} \frac{}{T \vdash \text{void}}$$

$$\text{LLNamed} \frac{}{T \vdash \text{void}} \quad \text{void} \in T$$

Just used void as type is well formed if $\text{void} \in T$.

Translating Well formed Types

Each well-formed nat type \longrightarrow well-formed Mmu type.

types \longrightarrow simple types

ref types \longrightarrow ref types

ret. types \longrightarrow ret. types

Use \rightsquigarrow to denote translation of derivations.

Axioms: $TInt \frac{}{t \text{ int}}$ $TBool \frac{}{t \text{ bool}}$ $TRef \frac{t_r \text{ ref}}{t \text{ ref}}$

$(TInt \frac{}{t \text{ int}}) \rightsquigarrow (LLInt \frac{}{t_s \text{ i64}})$

$(TBool \frac{}{t \text{ bool}}) \rightsquigarrow (LLBool \frac{}{t_s \text{ i8}})$

$\star (TRef \frac{t_r \text{ ref}}{t \text{ ref}}) \rightsquigarrow (LRef \frac{t_r t}{t \text{ t*}})$

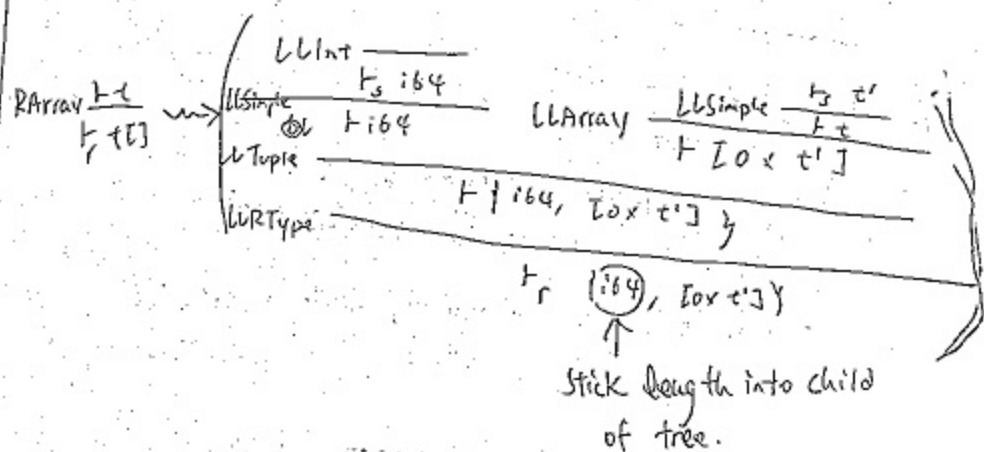
where $(t_r \text{ ref}) \rightsquigarrow (t_r t)$
extension of type judgement tree.

In Out v2, array accesses are checked at runtime.

Recall: (can implement runtime array access ^{checking} by allocating additional field storing the length.

\rightarrow we do exactly that:

Array access:



Tedious to write out trees \rightarrow leave trees implicit.

Succinct notation: $\llbracket t \rrbracket = J'$

derivation w/ root J translates to
derivation w/ root J'

$$- \llbracket t \text{ int} \rrbracket = t_3 \text{ i64}$$

$$- \llbracket t \text{ bool} \rrbracket = t_3 \text{ i1}$$

$$- \llbracket t \text{ ref} \rrbracket = t_3 \text{ t*}, \text{ where } t_r \ t = \llbracket t \text{ ref} \rrbracket$$

...

Well-formed Coexpressions

Judgements take the form

$$\textcircled{1} \quad \Gamma \vdash s \Rightarrow \Gamma'$$

Under typeenv Γ , coexpression s is well formed ... results in typeenv Γ'

$$\textcircled{2} \quad \Gamma \vdash \text{opn} : t \quad \text{"under typeenv } \Gamma, \text{ operand } \text{opn} \text{ has type } t\text{"}$$

$$\text{Id} \frac{}{\Gamma \vdash \text{id} : \Gamma(\text{id})}$$

$$\text{Num} \frac{}{\Gamma \vdash n : \text{int} \quad n \in \mathbb{Z}}$$

$$\text{Add} \frac{\Gamma \vdash \text{opn}_1 : \text{int} \quad \Gamma \vdash \text{opn}_2 : \text{int}}{\Gamma \vdash \% \text{int} = \text{add } \text{int } \text{opn}_1, \text{opn}_2 \Rightarrow \Gamma' \vdash \% \text{int} = \text{add } \text{int}} \quad \% \text{int} \notin \text{dom}(\Gamma)$$

$\hat{\Gamma}$: Modify the type environment.
"Interactive typing" property

side condition:
int is not in domain of Γ ;
Enforce SSA property

$$\text{Seq} \frac{\Gamma \vdash s_1 \Rightarrow \Gamma' \quad \Gamma' \vdash s_2 \Rightarrow \Gamma''}{\Gamma \vdash s_1, s_2 \Rightarrow \Gamma''}$$

order is important for SSA property.

Well-typed expressions

- translates a type judgement $\Gamma \vdash e : \tau$ to
 codestream judgement $\Gamma_{\mu} \vdash s \Rightarrow \Gamma_{\mu}$
 operand judgement $\Gamma_{\mu} \vdash \text{opn} : t_{\mu}$
 if, execute the codestream, then operand has value that
 the expression would take.

Goal: well-typed exprs in $\text{out} \rightarrow$ ^{derivation of} codestreams, operand judgements.

①:

Var $\frac{}{\Gamma \vdash x : \tau}$

How to translate $\Gamma \vdash x : \tau$ (Var):

* New symbol table $\text{ctxt} : \text{Out identifiers} \rightarrow \{\{\text{LUNA type/operand judgements}\}\}$

(Operand associated w/ variable x is

pointer to memory location associated w/ x).

* to compute $\llbracket \Gamma \vdash x : \tau \rrbracket (\text{ctxt})$, first

first let $(id, t\#) = \text{ctxt}(x)$. then

- define $\llbracket \text{ctxt} \rrbracket$ to be the LUNA type/operand associated w/ ctxt .

$\nabla \llbracket \epsilon \rrbracket = \epsilon$ (empty context \rightarrow empty out)

* $\llbracket \text{ctxt}, x \mapsto (id, t\#) \rrbracket = \Gamma_{\mu}, id \mapsto t$
 where $\llbracket \text{ctxt} \rrbracket = \Gamma_{\mu}$.

- Codestream: $\llbracket \text{ctxt} \rrbracket \vdash \% \text{vid} = \text{load } \tau, t\# id \Rightarrow \llbracket \text{ctxt} \rrbracket \{ \% \text{vid} \rightarrow t \}$.

- Operand: $\llbracket \text{ctxt} \rrbracket \{ \% \text{vid} \rightarrow t \} \vdash \% \text{vid} : t$.

Q translate $\Gamma \vdash e_1 + e_2 : \text{int}$ (Add).

- let $(\llbracket \text{ctx} \rrbracket \vdash S_1 \Rightarrow \Gamma_2, \Gamma_1 \vdash \text{opn}_1 : i64) = \llbracket e_1 \rrbracket(\text{ctx})$.

- let $(\llbracket \text{ctx} \rrbracket \vdash S_2 \Rightarrow \Gamma_2, \Gamma_2 \vdash \text{opn}_2 : i64) = \llbracket e_2 \rrbracket(\text{ctx})$.

- codestream: $\Gamma_1 + \Gamma_2 \vdash S_1, S_2, \%oid = \text{add } i64 \text{ opn}_1, \text{opn}_2$.

$\Rightarrow (\Gamma_1 + \Gamma_2) \vdash \%oid \rightarrow i64$.

- Operand: $(\Gamma_1 + \Gamma_2) \vdash \%oid \rightarrow i64 \vdash \%oid : i64$.

Tedious — but this is what a compiler does.

Summary:

- Semantic analysis — takes AST as input

- Constructs symbol table

- performs well-formedness checks

- well-formedness derivations impact compilation.

ex: D x. field gets compiled differently
depending on type of x

② might have to emit bitcasts for subsumption.

(When type system does not have subtyping).

Summary, cont

- Tedious: Compiler translates derivations of well-formedness judgements in source language to derivations of well-formedness judgements in target language.

→ In an implementation, this viewpoint is implicit.

- Don't need to do all the bookkeeping involved in manipulating derivations.
- But it is helpful for understanding how