

COS320 - Loop transforms

Essential Question:

What is a graph-theoretic def of a loop?
(CFG)

This def has to be syntax-invariant, (nonsensitive to choice of loop e.g. goto, while, for, etc)

First attempt: Strongly Connected Components & But this isn't ^{enough} — Nested loops have only one SCC (but we have multiple loops nested) we want to transform separate loops. So this is too general — Not enough information! (in the sense that not a lot of local info within each SCC.

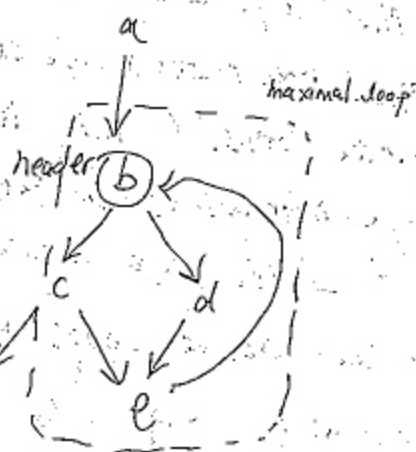
We want to at least capture loops that result from structured programming (e.g. w/o goto's).

What is a loop?

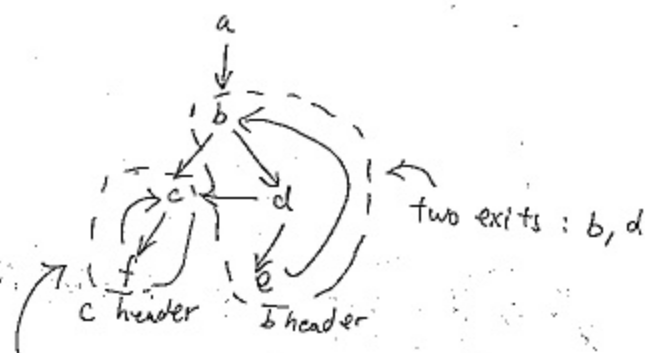
A loop of set of nodes S :

- ① S is SCC
 - ② header node h that dominates \checkmark other nodes in S
 - ③ No edge from any node outside S to any node inside S , except for h .
- (Single entry, multiple exits)

ex



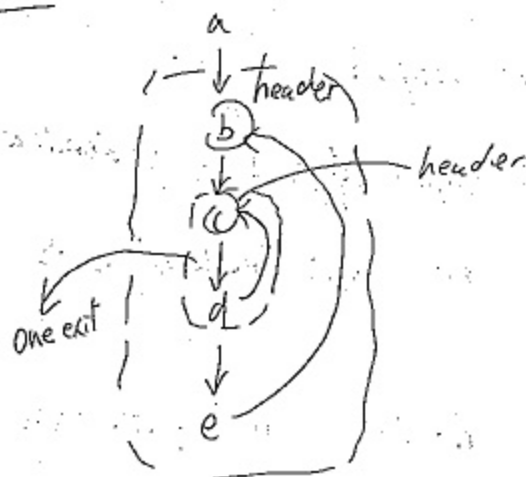
ex 2 Loop with 2 nodes



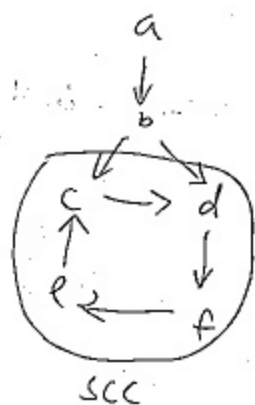
no exits

both loop have one entry

ex 3



ex 1 (counterexample)



but two headers (entries)
c, e!

So not a loop per our
definition.

Identification

Only works for natural loops:

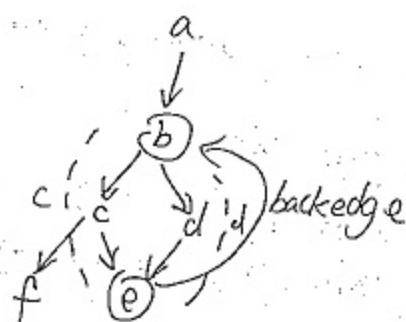
1) back edge is (u, v) edge s.t. v dominates u .

2) the nat loop of (u, v) back edge is

set of nodes

$\{n; v \text{ dominates } n \wedge \text{path from } n \text{ to } u \text{ w/o } v\}$

- Can do DFS at src of back edge to compute set of nodes for nat. loop:



$$\text{natural loop} = \{c, d\} \cup \{b, e\}$$

Proposition Every nat loop is a loop

We have 3 defs for loops:

① Strongly connected def for loop

- by DFS construction every node has path to u that doesn't pass thru v
- Every node has path from v (path from entry to node to u must incl. v).

But nat. loop obeys this definition.

② Header (v) dominates the loop

Pf by contradiction:

Suppose from entry \rightarrow $\hat{\text{path}}$ \rightarrow \neq nat loop that
doesn't pass thru v .

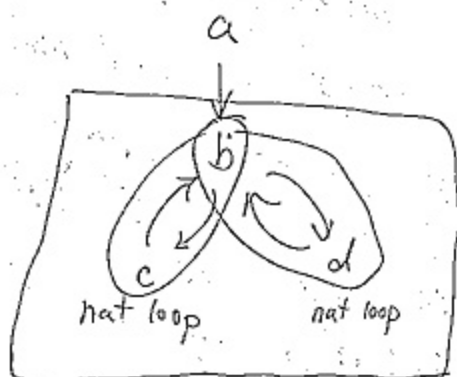
$\Rightarrow \exists$ path from entry $\rightarrow v$ that doesn't
pass thru v , contradiction.

③ Single entry

Pf by dfs construction, all predecessors
of any node except v belongs to
loop.


Q: Is every loop natural?

A: No: example:



Not nat loop

(but single entry SCC)

→ So  also loop

— but no back edge that encloses this.

but we are happy for this conservative definition.

Nested loops and intersecting loops

Def loop B nested within A if $B \subseteq A$

A node can be header of more than one ~~natural~~ ^{natural} loop (intersecting loops that aren't nested) \longrightarrow but we will merge

nat loops w/ same header (



No longer natural
but is a loop.

Key property every loops are either disjoint or nested

- form a forest
- leaves of trees are most deeply nested loops.

* SCC constructed as intersections of natural loops

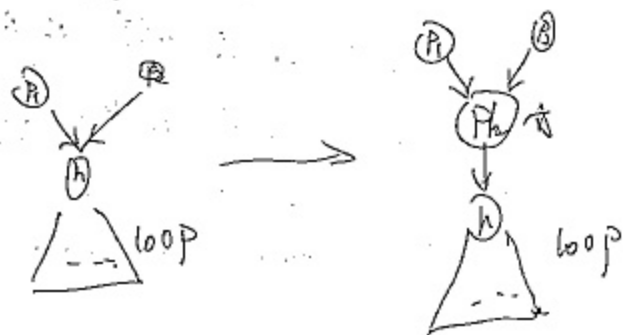
Apply loop transformational "inside-out",

Start w/ leaf nodes (innermost loops) first.

Loop Preheaders

- Some optimizations (e.g. loop-invariant code motion) require inserting statements immediately before code executes.

- A loop preheader is basic block that is inserted immediately before loop header to serve as a place to store these statements.



loop-invariant code motion (LICM)

* Saves cost of recomputing expressions that are lhs-invariant (do not change) inside the loop.

- Such computations can be moved into preheader as long as they're not side-effecting.

SSA-based LICM

operand is invariant in loop L if

① it is const

② it is giv

③ it is giv whose def is not in L

③ holds based on SSA property

— it is eval'd in some node that dominates loop preheader.

Now for each computation

$$\%x = op_1 \text{ op } op_2$$

if $op_1, \wedge op_2$ are invariant.

→ move $\%x = op_1 \text{ op } op_2$
to loop pre-header.

This moves def of $\%x$ outside of the
loop so $\%x$ is now invariant.

Induction Variables

“等差数列”

Def: Induction var is var $\%x$ such
~~that differ~~
the diff. between successive vals in
loop is const.

ex: for($i=0$; $i < n$; $i++$) ← Counter var

— Use $\%x(k)$ to denote x in k th iter.

$$\%x(k+1) = \%x(k) + \Delta(\%x) \quad \Delta$$

Useful for several optimizations...

strength reduction

loop unrolling

induction variable elimination

parallelization

array bounds-check

basic:

A variable $\%X$ is basic induction variable

for loop L if increased/decreased by
fixed loop ~~invariant~~ invariant quantity in any
iteration of loop: $\Delta(\%X) = C$

$$\%X(i+1) = \%X(i) + C$$

this is a "synthetic" sequence of assignments.

Derived:

Y is a derived induction variable for
loop L if it is an affine function of a
basic induction variable:

$$Y(i) = a \cdot X(i) + b \rightarrow \Delta(Y) = a \cdot C$$

depends
on
basic
induct.
var X

Finding Induction Variables in SSA form

— look for basic induction vars:

look for ϕ statements in loop header

$$\%X = \phi(\%X_1, \dots, \%X_n)$$

— if value of some ^{basic} var is changed in loop then it appears in a ϕ -statement.

— Only look for X_i s.t. each X_i ^{position} corresponds to the same vid at the back edge of a loop.

Next find chain of $\%X_k$ leading back to $\%X$ such that each either adds or subtracts an invariant quantity.

Detecting derived induction variables

- Choose basic induction variable $\%x$
- find assignments of form $\%y = opn, op\ opn_1$

Where $op \in \{+, -\}$

opn_1, opn_2 are either $\frac{\%x}{\text{derived induction variables of } \%x}$
or loop invariant quantities.

example of application: Strength Reduction

Idea : Replace expensive operation w/ cheaper ones (e.g. replace multiplication with addition).

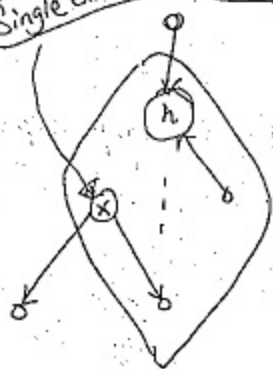
Loop Unrolling

- Some loops are so small that significant time is spent for branch instructions
- Unrolling helps (by trading code size for space)

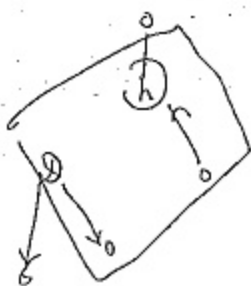
Graphical Illustration

Single exit: $b \neq t$, in, out / $t \geq 0$ is induction variable with $\Delta(t) = -1 \geq 0$

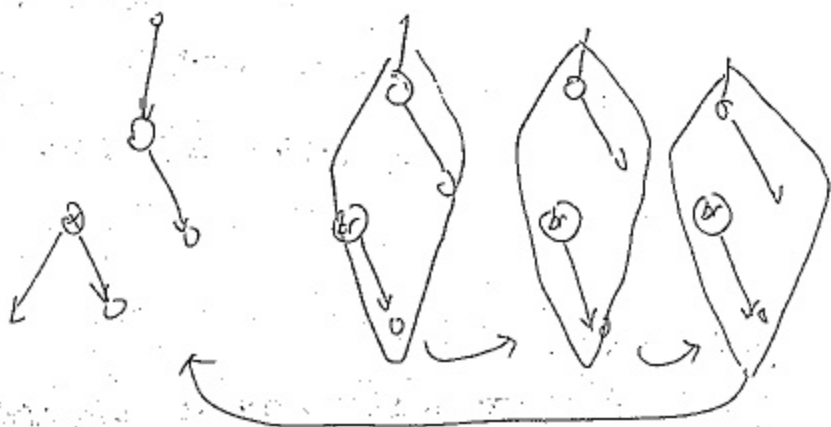
i.e. t is monotonically decreasing ... successive t_i are equidistant.



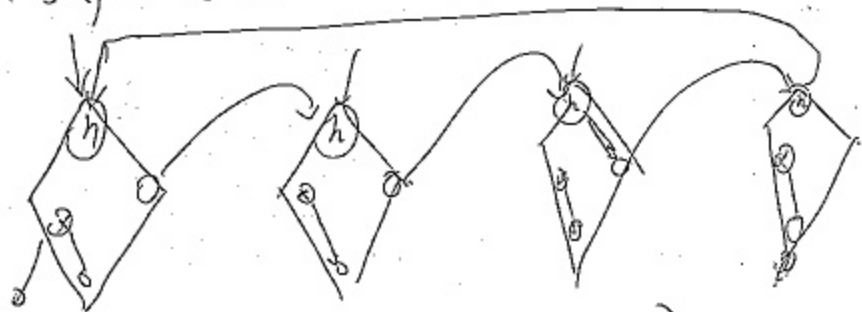
first step: copy loop k times



Next step: Alter conditionals to unconditional branch.



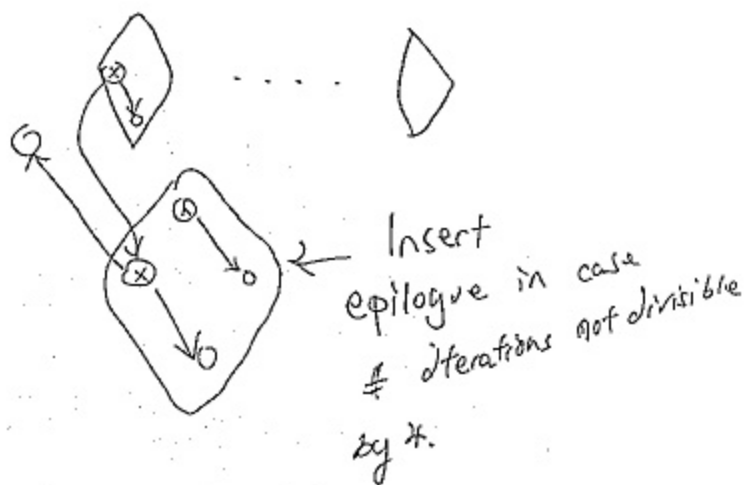
Next step: redirect back edges to next loop copy



— Doesn't quite work — why?

A: Still have to "clean up" at the end.

Final step:



Epilogue: wrap-up

Recap:

Optimizer is series of IR \rightarrow IR transformations
these transformations are typically supported by

Some analysis that proves the transformation
is safe.

Each transformation is simple —
When come in series they are mutually
beneficial!