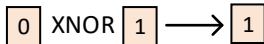
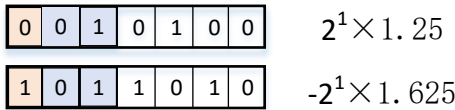
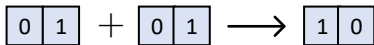


(a) Matrix Multiplication

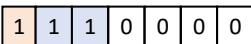


Sign bits XNOR

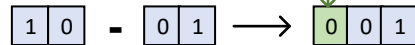
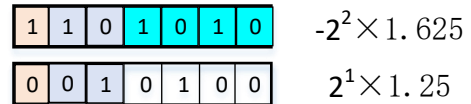


Exponent bits Fixed-Point Addition

1.0100  $\times$  1.1010 = 10.0000  
Mantissa bits Fixed-point multiplication

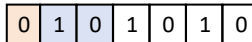


Result normalization

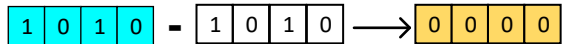


Subtract the exponent bits

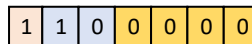
Determine the sign bit



Align the exponent bits



Subtract the mantissa bits



Result normalization

(b) Floating Point Multiplication

(c) Floating Point Addition