

Front Panel Connections

- Target Activity
- Target Clock
- Armed
- Capture in Progress
- USB Activity
- System Status

Status LED may be OFF until
bitstream loaded from PC into FPGA.



"Listen to your Inner Hardware"

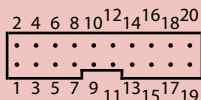
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Target Connector Pinout



View looking into rear connector

| | |
|---------------|--------------------------|
| 1. +5V (USB) | 2. GND |
| 3. +3.3V | 4. FPGA-HS1 (Clock In) |
| 5. PROG-RESET | 6. FPGA-HS2 (Clock Out) |
| 7. PROG-MISO | 8. VTarget (Input) |
| 9. PROG-MOSI | 10. FPGA-TARG1 (TX) |
| 11. PROG-SCK | 12. FPGA-TARG2 (RX) |
| 13. PROG-PDIC | 14. FPGA-TARG3 (SCARD) |
| 15. PROG-PDID | 16. FPGA-TARG4 (TRIGGER) |
| 17. GND | 18. +3.3V |
| 19. GND | 20. +5V (USB) |

Rear USB-A: Programmer Interface, External Power In

Side USB-Mini: High-Speed FPGA Connection

USB have VBUS & GND connected internally. See manual for details.