

8255 Adresleme		
A_1	A_0	Port
0	0	PortA
0	1	PortB
1	0	PortC, Status
1	1	Kontrol

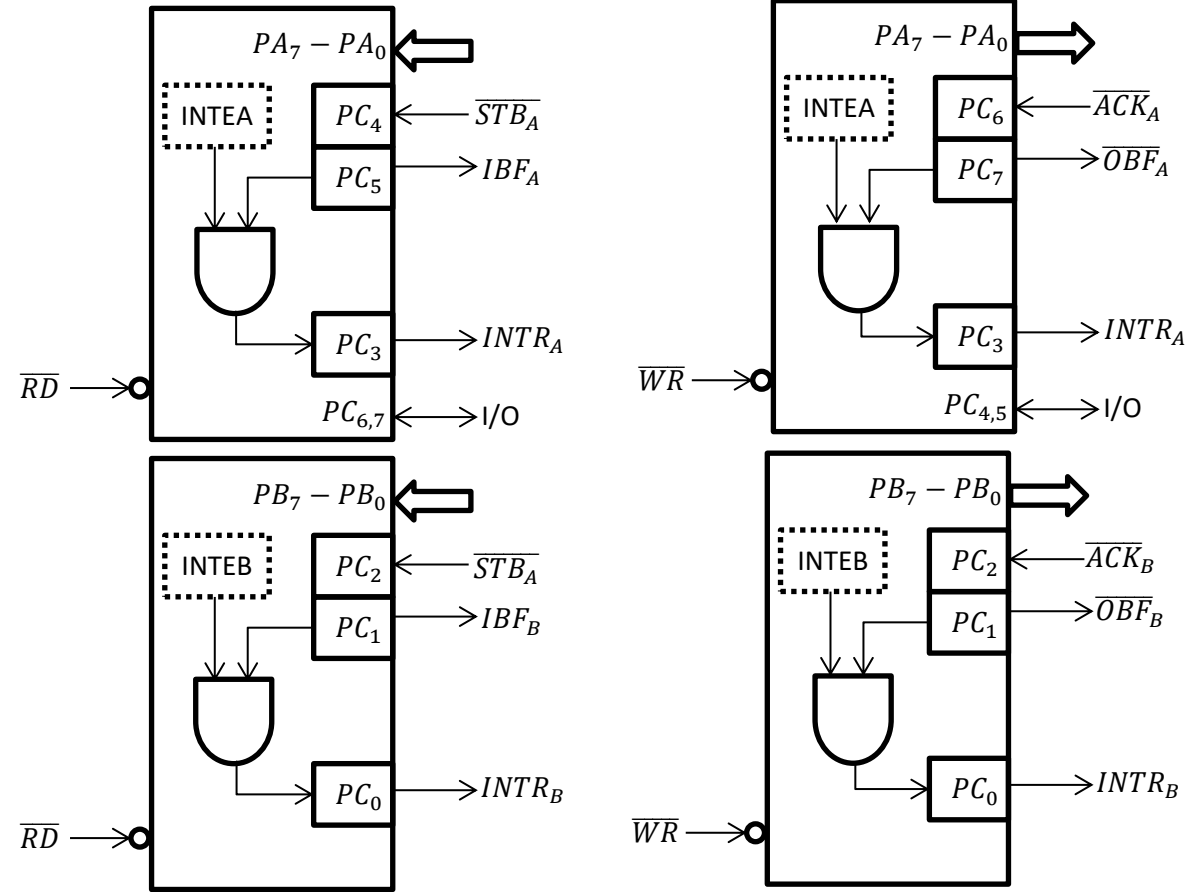
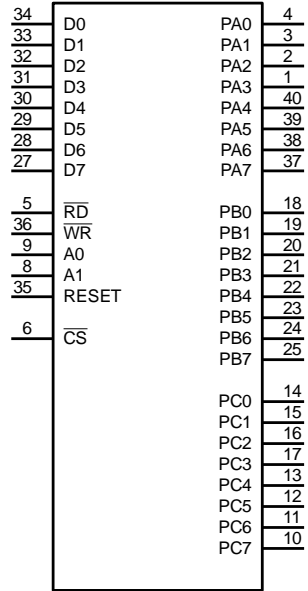
8255 Mod 1 (Grup A) Kontrol Yazmacı							
D_7	D_6	D_5	D_4	D_3	D_2	D_1	D_0
1	0	1	PA	$PC_{6,7}$	X	X	X
PA	$PC_{6,7}$	Port Yönü					
0	0	Çıkış					
1	1	Giriş					

8255 Mod 1 (Grup B) Kontrol Yazmacı							
D_7	D_6	D_5	D_4	D_3	D_2	D_1	D_0
1	X	X	X	X	1	PB	X
PB	Port Yönü						
0	Çıkış						
1	Giriş						

8255 Bit Set Reset Mod Kontrol Yazmacı							
D_7	D_6	D_5	D_4	D_3	D_2	D_1	D_0
0	X	X	X	B_2	B_1	B_0	S/R

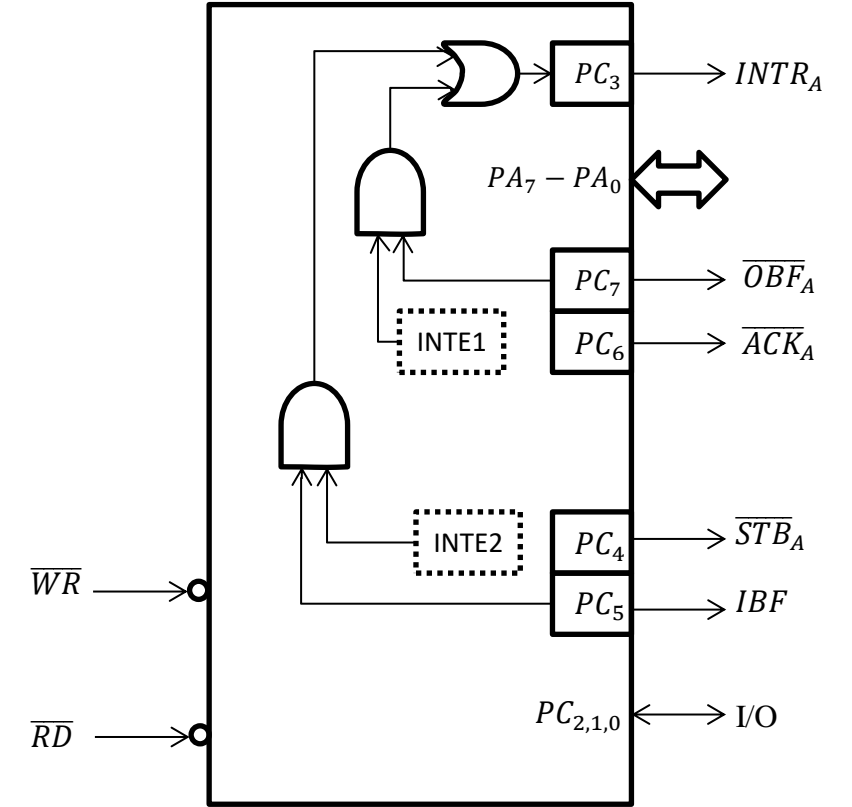
Port C'nin ($B_2B_1B_0$)₂ numaralı pinini seç.
 S/R : Seçilen pinde Set (1) veya Reset (0) oluşturun.

8255 Basit I/O (Mod 0) Kontrol Yazmacı							
D_7	D_6	D_5	D_4	D_3	D_2	D_1	D_0
1	0	0	PA	PC_U	0	PB	PC_L
PA	PB	PC_U	PC_L	Port Yönü			
0	0	0	0	Çıkış			
1	1	1	1	Giriş			



8255 Mod 1 Input Status							
D_7	D_6	D_5	D_4	D_3	D_2	D_1	D_0
I/O	I/O	IBF_A	$INTE_A$	$INTR_A$	$INTE_B$	IBF_B	$INTR_B$

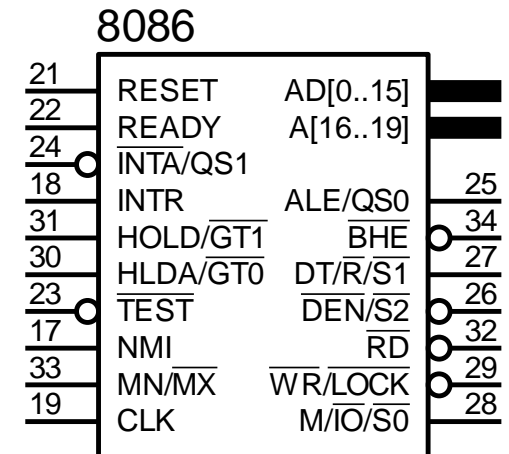
8255 Mod 1 Output Status							
D_7	D_6	D_5	D_4	D_3	D_2	D_1	D_0
\overline{OBF}_A	$INTE_A$	I/O	I/O	$INTR_A$	$INTE_B$	\overline{OBF}_B	$INTR_B$



8255 Mod 2 (Grup A) Kontrol Yazmacı							
D_7	D_6	D_5	D_4	D_3	D_2	D_1	D_0
1	1	X	X	X	X	X	$PC_{2,1,0}$
$PC_{2,1,0}$	Port Yönü						
0	Çıkış						
1	Giriş						

8255 Mod 2 Status							
D_7	D_6	D_5	D_4	D_3	D_2	D_1	D_0
\overline{OBF}_A	$INTE_1$	IBF_A	$INTE_2$	$INTR_A$	X	X	X

8086 Flags															
D_{15}	D_{14}	D_{13}	D_{12}	D_{11}	D_{10}	D_9	D_8	D_7	D_6	D_5	D_4	D_3	D_2	D_1	D_0
X	X	X	X	O	D	I	T	S	Z	X	A	X	P	X	C



8251 Adresleme			
C/\overline{D}	\overline{RD}	\overline{WR}	Yazmaç
0	0	1	Data $\rightarrow \mu P$
0	1	0	$\mu P \rightarrow$ Data
1	0	1	Status $\rightarrow \mu P$
1	1	0	$\mu P \rightarrow$ Mode, Control, Sync

8251 Mod Yazmacı (Senkron)							
D_7	D_6	D_5	D_4	D_3	D_2	D_1	D_0
SCS	ESD	EP	PEN	L_2	L_1	0	0

SCS: Sync karakter sayısı. 0: 2 sync, 1: 1 sync

ESD: External sync detect. 0: SYNDET output, 1: SYNDET input.

8251 Kontrol Yazmacı							
D_7	D_6	D_5	D_4	D_3	D_2	D_1	D_0
EH	IR	RTS	ER	$SBRK$	RxE	DTR	TxE

IR: Internal reset. ER: Clear error bits. SBRK: Break

transmit, forcing TxD low.

8251 Status Yazmacı							
D_7	D_6	D_5	D_4	D_3	D_2	D_1	D_0
DSR	$SYNDET$	FE	OE	PE	TxE	$RxRDY$	$TxRDY$

FE: Framing error. OE: Overrun error. PE: Parity error.

8251 Mod Yazmacı (Asenkron)							
D_7	D_6	D_5	D_4	D_3	D_2	D_1	D_0
S_2	S_1	EP	PEN	L_2	L_1	B_2	B_1

S_2	S_1	Stop biti sayısı
0	0	Invalid
0	1	1 stop biti
1	0	1.5 stop biti
1	1	2 stop biti

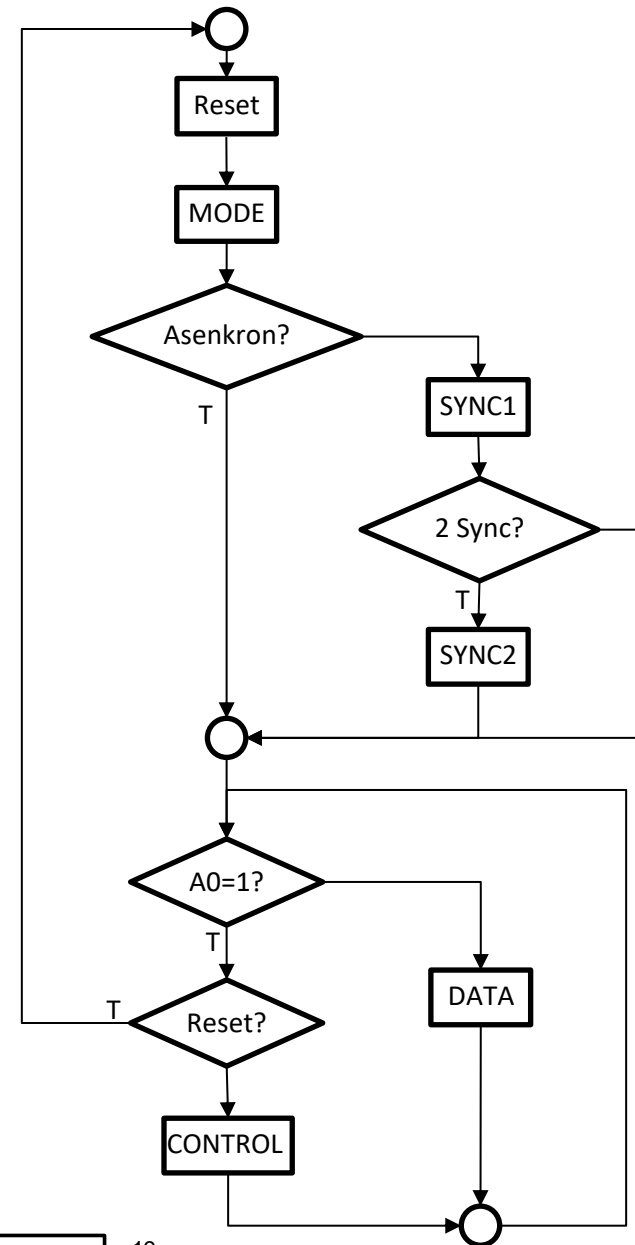
EP	Parity
0	Odd parity
1	Even parity

PEN	Parity enable
0	Parity yok
1	Parity var

L_2	L_1	Data bit sayısı
0	0	5
0	1	6
1	0	7
1	1	8

B_2	B_1	Baud rate factor
0	0	Senkron mod
0	1	1
1	0	16
1	1	64

8254 Status							
D_7	D_6	D_5	D_4	D_3	D_2	D_1	D_0
$OUTPUT$	Null Count	RW_1	RW_0	M_2	M_1	M_0	BCD



8254 Adresleme		
A_1	A_0	Yazmaç
0	0	Counter0, Status0
0	1	Counter1, Status1
1	0	Counter2, Status2
1	1	Control

8254 Kontrol Yazmacı							
D_7	D_6	D_5	D_4	D_3	D_2	D_1	D_0
SC_1	SC_0	RW_1	RW_0	M_2	M_1	M_0	BCD

SC_1	SC_0	SC – Select Counter
0	0	Counter0
0	1	Counter1
1	0	Counter2
1	1	Read Back Command

M_2	M_1	M_0	M – Mod
0	0	0	Mod 0
0	0	1	Mod 1
X	1	0	Mod 2
X	1	1	Mod 3
1	0	0	Mod 4
1	0	1	Mod 5

RW_1	RW_0	RW – Read/Write
0	0	Counter Latch Command
0	1	LSb
1	0	MSb
1	1	Önce LSb, sonra MSb

BCD	Sayma
0	Binary
1	Binary Coded Decimal

8254 Read Back Command							
D_7	D_6	D_5	D_4	D_3	D_2	D_1	D_0
1	1	$COUNT$	$STATUS$	CNT_2	CNT_1	CNT_0	0

$COUNT = 0$: Sayma değeri tut

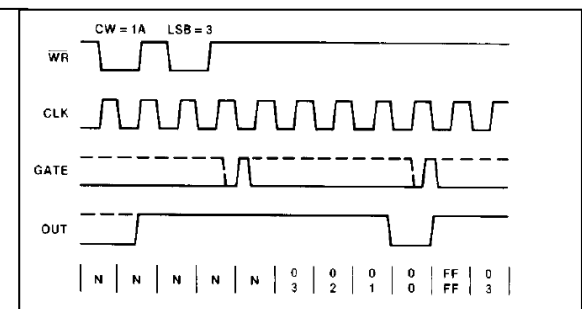
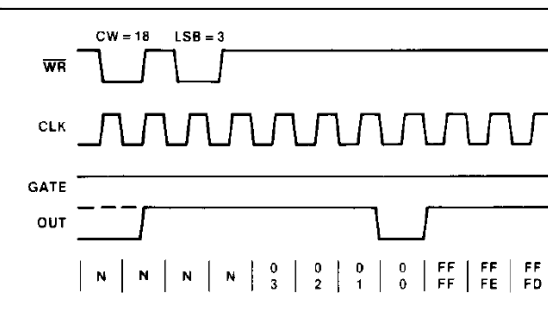
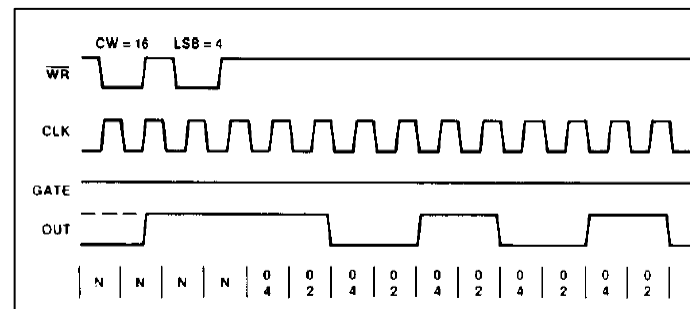
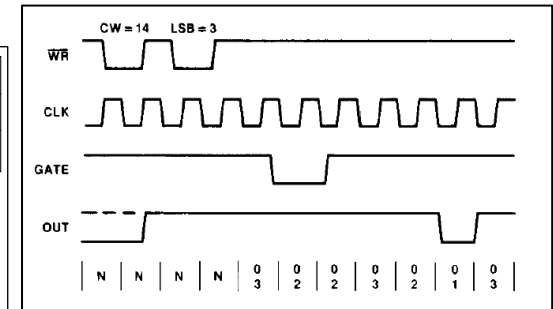
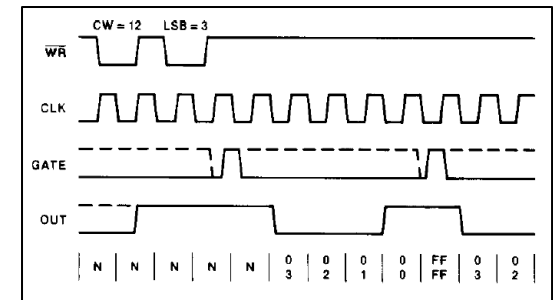
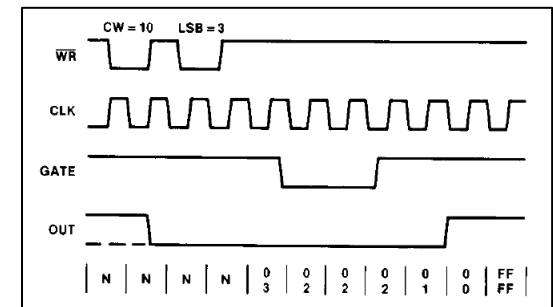
$STATUS = 0$: Durum tut

$CNT2 = 1$: Sayıcı 2 için tut

$CNT1 = 1$: Sayıcı 1 için tut

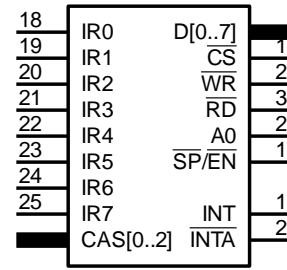
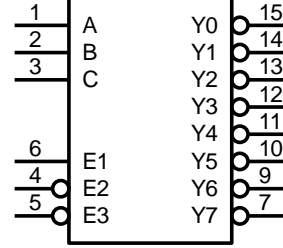
$CNT0 = 1$: Sayıcı0 için tut

8	D0	CLK0	9
7	D1	GATE0	11
6	D2	OUT0	10
5	D3		
4	D4	CLK1	15
3	D5	GATE1	14
2	D6	OUT1	13
1	D7		
22	\overline{RD}	CLK2	18
23	\overline{WR}	GATE2	16
		OUT2	17
19	A0		
20	A1		
21	\overline{CS}		



21	D[0..7]	TxD	19
20	RESET		
12	CLK	TxRDY	15
10	C/\overline{D}	TxEMPTY	18
13	\overline{WR}	TxC	9
	\overline{RD}		
11	\overline{CS}	RxD	3
24	DTR	RxRDY	14
22	DSR	RxC	25
23	RTS		
17	CTS	SYNDET	16

74138 3x8 Dekoder Fonksiyon Tablosu														
INPUTS						OUTPUTS								SELECTED OUTPUT
ENABLE			SELECT											
E1	E2	E3	C	B	A	Y0	Y1	Y2	Y3	Y4	Y5	Y6	Y7	
L	X	X	X	X	X	H	H	H	H	H	H	H	H	NONE
X	X	H	X	X	X	H	H	H	H	H	H	H	H	NONE
X	H	X	X	X	X	H	H	H	H	H	H	H	H	NONE
H	L	L	L	L	L	L	H	H	H	H	H	H	H	Y0
H	L	L	L	L	H	H	L	H	H	H	H	H	H	Y1
H	L	L	L	H	L	H	H	L	H	H	H	H	H	Y2
H	L	L	L	H	H	H	H	H	L	H	H	H	H	Y3
H	L	L	H	L	L	H	H	H	H	L	H	H	H	Y4
H	L	L	H	L	H	H	H	H	H	H	L	H	H	Y5
H	L	L	H	H	L	H	H	H	H	H	H	L	H	Y6
H	L	L	H	H	H	H	H	H	H	H	H	H	L	Y7
X : Don't Care, L : Low, H : High														



8259 ICW ₁								
A ₀	D ₇	D ₆	D ₅	D ₄	D ₃	D ₂	D ₁	D ₀
0	X	X	X	1	LTIM	0	SNGL	IC ₄
LTIM	Açıklama							
0	Kenar tetikleleme							
1	Seviye tetikleleme							
SNGL	Açıklama							
0	Kaskat bağlı 8259'lar							
1	Tek 8259							
IC ₄	Açıklama							
0	IC ₄ kullanılmayacak							
1	IC ₄ kullanılacak							

8259 ICW ₂								
A ₀	D ₇	D ₆	D ₅	D ₄	D ₃	D ₂	D ₁	D ₀
1	A ₇	A ₆	A ₅	A ₄	A ₃	X	X	X

(A₇A₆A₅A₄A₃000)₂ IR0 için kesme isteği adresi

8259 ICW ₃ SGNL=0 ise (Master)								
A ₀	D ₇	D ₆	D ₅	D ₄	D ₃	D ₂	D ₁	D ₀
1	S ₇	S ₆	S ₅	S ₄	S ₃	S ₂	S ₁	S ₀
S _i	Açıklama							
0	IR _i 'ye slave bağlı değil							
1	IR _i 'ye slave bağlı							

8259 ICW ₃ SGNL=0 ise (Slave)								
A ₀	D ₇	D ₆	D ₅	D ₄	D ₃	D ₂	D ₁	D ₀
1	0	0	0	0	0	ID ₂	ID ₁	ID ₀

(ID₂ID₁ID₀)₂ Slave ID

8259 ICW ₄								
A ₀	D ₇	D ₆	D ₅	D ₄	D ₃	D ₂	D ₁	D ₀
1	0	0	0	SFNM	BUF	M/S	AEOI	μP
BUF	M/S	Buffered – Master/Slave						
0	X	Non-buffered						
1	0	Buffered slave						
1	1	Buffered master						

AEOI=1 otomatik kesme sonlandırma

μP=1 8086 için

SFNM=0, BUF=0, M/S=0 kullanılacak

8259 OCW ₁								
A ₀	D ₇	D ₆	D ₅	D ₄	D ₃	D ₂	D ₁	D ₀
1	M ₇	M ₆	M ₅	M ₄	M ₃	M ₂	M ₁	M ₀
M _i	Açıklama							
0	Mask reset							
1	Mask set							

8259 OCW ₂								
A ₀	D ₇	D ₆	D ₅	D ₄	D ₃	D ₂	D ₁	D ₀
0	R	SL	EOI	0	0	L ₂	L ₁	L ₀

8259 OCW ₃								
A ₀	D ₇	D ₆	D ₅	D ₄	D ₃	D ₂	D ₁	D ₀
0	0	ESMM	SMM	0	1	P	RR	RIS

