# Notes

* All unused space has been filled in with GND.
* Have used a small +5V plane near USB-C (shown in layer 3) and a small +1.1V plane near left of MCU (also shown in layer 3).
* All vias are specifically layered so that they only go from the top layer to their respective layer (excluding GND – these vias pass all the way through). For example, a 3.3V via will go from layer 1 – 2 but not drill through 3 or 4.

# Layer 1 – Front Layer

A computer screen shot of a circuit board

Description automatically generated

# Layer 2 – 3.3V

A screenshot of a video game

Description automatically generated

# Layer 3 – Inner Signals

A screenshot of a video game

Description automatically generated

# Layer 4 - GND

A screenshot of a video game with Cyclades in the background

Description automatically generated