Figure 4. Memory map based on IDAU mapping for STM32U575/585
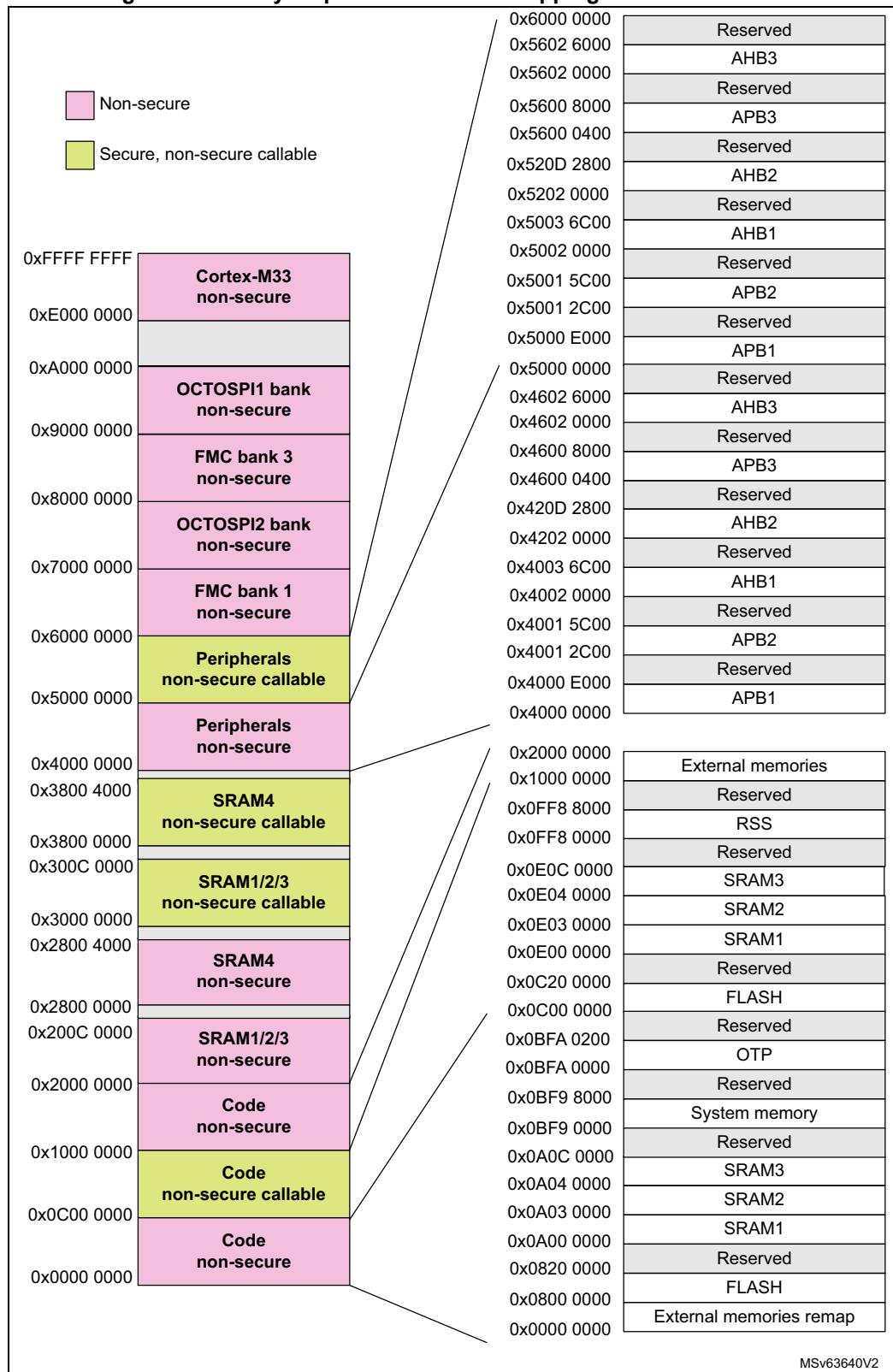

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