

Top Layer

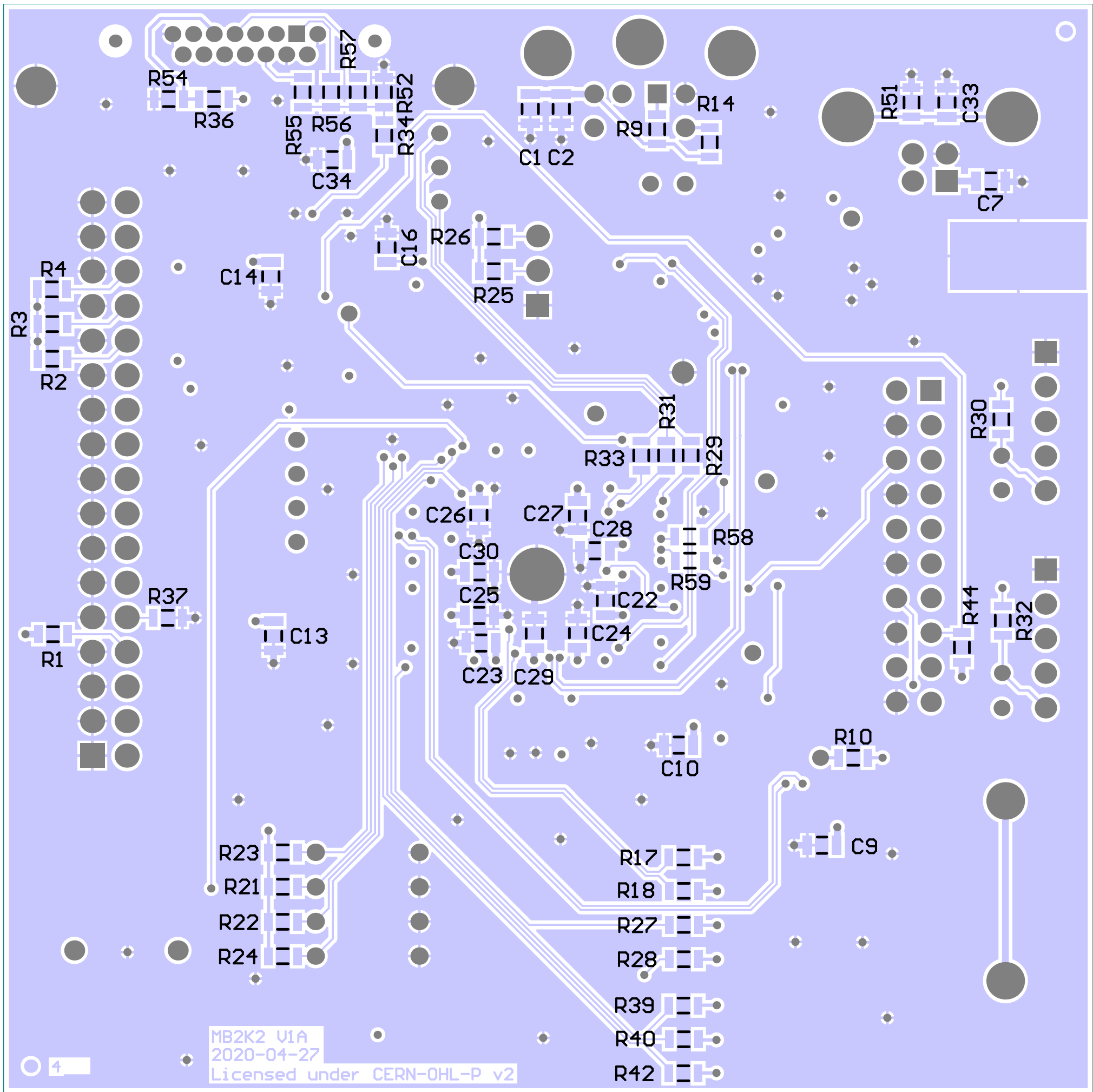
<i>AVED</i>	ENGINEER: DAR	TITLE: MB2K2 V1A	
	PCB DESIGNER: DAR		
	DATE: 5/6/2020	PART NO.:	REV: V1
	FILE NAME: MB2K2_V1.PcbDoc	DWG NO:	SCALE:

A

B

C

D



Bottom Layer

AVED

ENGINEER: DAR	TITLE: MB2K2 V1A		
	DATE: 5/6/2020		REV: V1
	FILE NAME: MB2K2_V1.PcbDoc		SCALE: