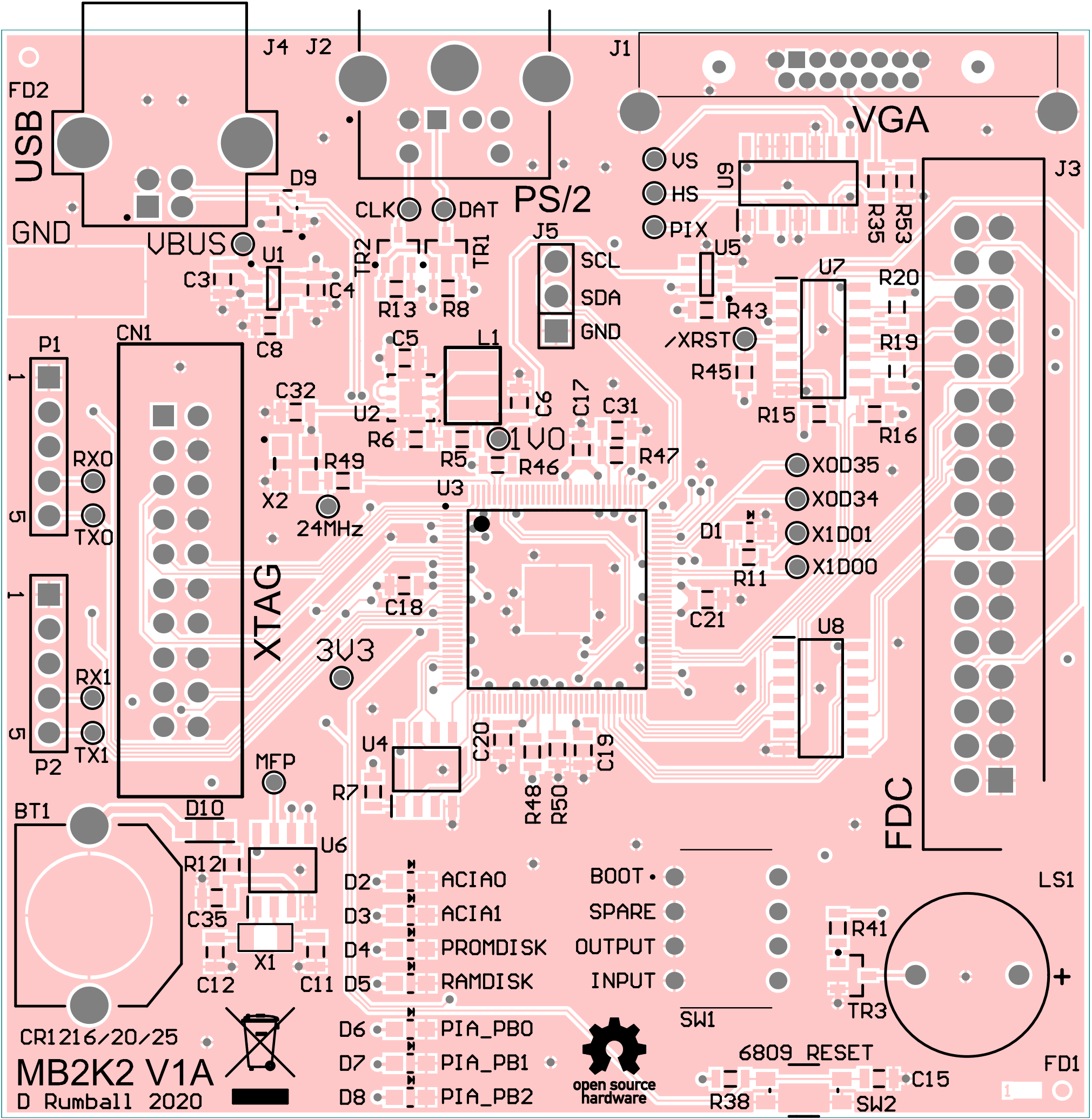


A

B

C

D



Top Layer

<div>AVED</div>	ENGINEER:	DAR	TITLE:		MB2K2 V1A	
	PCB DESIGNER:	DAR				
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	FILE NAME:	MB2K2_V1.PcbDoc	DWG NO:			SCALE:

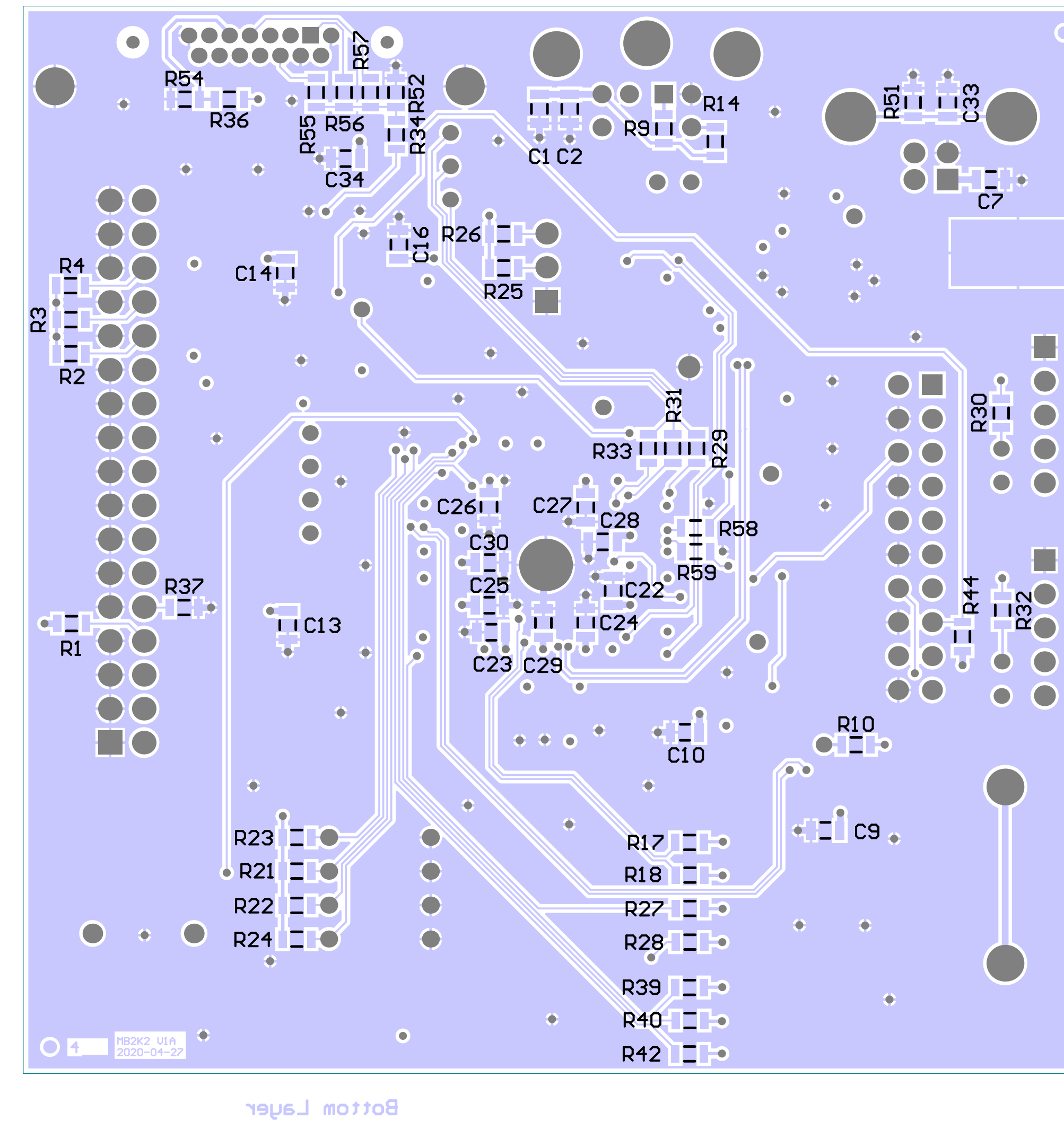
D


A

B

C

D



	ENGINEER: DAR	TITLE: MB2K2 V1A	
	PCB DESIGNER: DAR		
	DATE: 4/27/2020	PART NO.:	REV: V1
	FILE NAME: MB2K2_V1.PcbDoc	DWG NO:	SCALE:

ENGINEER: DAR	TITLE: MB2K2 V1A	
PCB DESIGNER: DAR		
DATE: 4/27/2020	PART NO.:	REV: V1
FILE NAME: MB2K2_V1.PcbDoc	DWG NO:	SCALE: