

GATE SEQUENCER

5 6

4 7

3 8

2 9

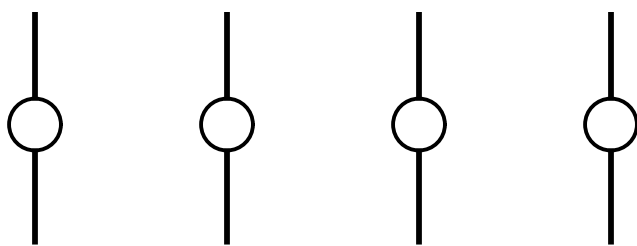
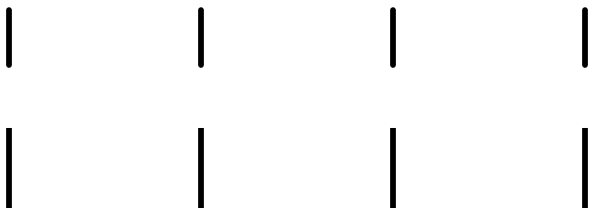
1 10

CLOCK RESET

HOLD RESET



4 STAGE PROGRAMMER-SEQUENCER



1 2 3 4

A

B

C

D

COMMON

ALL EVENTS

STEP RIGHT

STEP LEFT

SNR

A

B

C

D

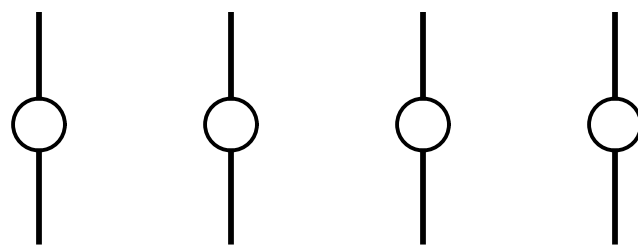
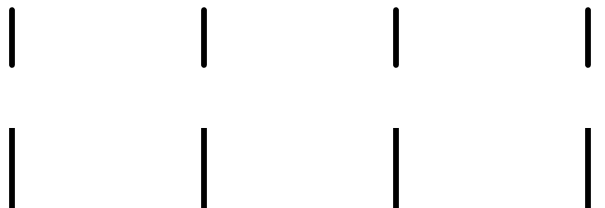
OUT

STEP



RESET

4 STAGE PROGRAMMER-SEQUENCER



1 2 3 4

A

B

C

D

COMMON

ALL EVENTS

STEP RIGHT

STEP LEFT

SNR

A

B

C

D

OUT

STEP



RESET

BI-DIRECTIONAL ROUTER

A1

B

A1 \neg A2

A2

A1

B

A1 \neg A2

A2

A1

B

A1 \neg A2

A2