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**Objective statement**

Senior ( 4th Year ) student at the faculty of Engineering Alexandria University Communications Department .

Looking for internship opportunity at Swift Act .

**Work experience :**

**Still an undergraduate , I graduate this year 2020.**

**Courses attended:**

* Attended and finished a two- week course provided by ITI on PCB design using Mentor graphics tools ( Expedition ) .
* Attended and finished a two-week course provided by ITI on Embedded System Design ( STM32F4 Controller Kit ) .

**Education**

Senior Student ( 4th year ) at The Faculty of Engineering Alexandria University ( Communications Department ) .

**Projects and Skills :**

**Languages:**

**1- C Programming language experience , both embedded and general programming like win32 api and so on .**

**2- C++ Programming Language experience , ( done some projects with QT ( user interface for graduation project ) as well as it is the preffered language to use on codeforces for competitions .**

**3- Background with Python / Verilog /VHDL .**

**Projects :**

* AVR microcontroller experience with AVR-GCC tool chain.
* (wide array of projects and drivers using various AVR Micros like the ATTIN2313 , atiny85 , atmega32,atmega16 .
* Graduation project worked on firmware design for the ATXMEGA32a4 .
* Took a student council provided ARM course based on the TIVA C Board .
* Some Experience with Make file scripts .
* ARM M3 Experience with the STM32F103 .
* ARM4 M4 Experience with STM32F429 Microcontroller.
* Some ARM Projects like :

1-Audio application with I2S bus , on the STM32F429 Disco Board with PCM5102 and PCM1808 Audio Codecs , application involved sampling audio from PC doing DSP filtering on the Micro controller and then playing back the filtered samples .

2-Drivers for STM32 SAI and I2S audio buses .

3-Auto baud rate detection UART .

4-USB device driver for the STM32F103C8T6 .

5- Smart home system with ST TouchGFX TFT Driver for the touch screen interface .

* Some FPGA Programming Experience , in particular the Cyclone II family from ALTERA .
* Background with VERILOG and VHDL .
* DSP Background and some knowledge in designing digital filters.
* Universal EEPROM programmer with interfacing software written in win32 API .
* Closed loop feedback controller for an induction heater.
* 8085 microprocessor based digital multi-meter.
* 8086 based microprocessor system ( College project ) .
* Worked on ROV systems with robotics team .
* 16 MHz ASK modulator/demodulator driven by microcontroller’s Oscillator .
* 8 - bit micro processor using 40xx chips .
* Able to communicate effectively with team members.
* Excellent Command of the English language .
* Research and Experimenting skills .
* Able to write 70 words/min .