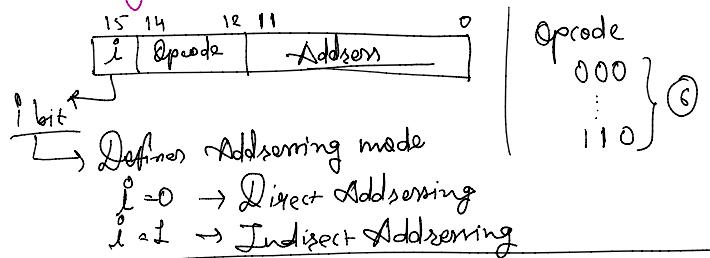


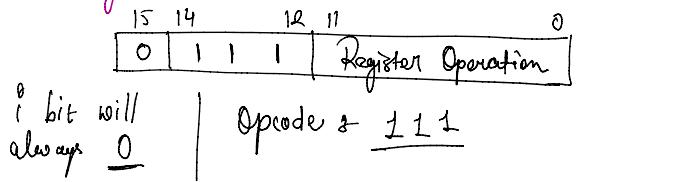
## Computer Instruction

### \* Format.

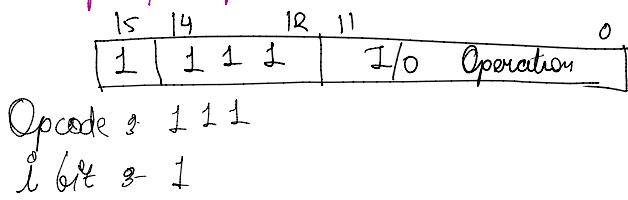
#### ① Memory Reference Instruction



#### ② Register Reference Instruction

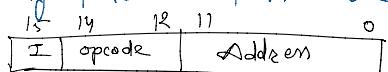


#### ③ Input / Output Instruction



## Instruction Example

#### ① Memory Reference Instruction

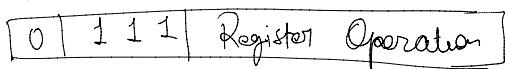


Symbol	Format	Description
<u>I=0, I=1</u>		

AND	0 XXX	8 XXX	AND Memory Word to Acc
ADD	1 XXX	9 XXX	ADD Memory Word to Acc
LDA	2 XXX	A XXX	Load Acc From Memory
STA	3 XXX	B XXX	Store Content of Acc to Mem
BUN	4 XXX	C XXX	Branch Unconditionally
BSA	5 XXX	D XXX	Branch & Save Return Address
ISZ	6 XXX	F XXX	Increment & Skip if Zero

⇒ All above are Memory reference instruction

#### ② Register Reference Instruction



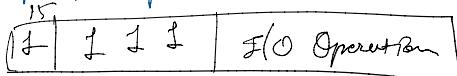
## Symbol

CLA	7800	clear Acc
CLE	7400	clear E
---	7000	---



C LA	7800	clear Acc
C LE	7400	clear E
C MA	7200	Complement acc
C ME	7100	Complement G
C IR	7080	circulate right Acc and G
C IL	7040	circulate left Acc & E
I NC	7020	Inrement ACC
S PA	7010	Skip next Instruction if positive
S NA	7008	Skip next Instr. if negative Acc
S ZA	7004	Skip next Instr. if ACC is zero
S ZE	7002	Skip next Instr. if E is zero
HLT	7001	Halt Computer

## ③ Input / Output Instruction



### Symbo

INP	F800	Input character to Acc
OUT	F400	Output character from Acc
SKI	F200	Skip on Input flag
SKO	F100	Skip on Output flag
ION	F080	Interrupt ON
IOP	F040	Interrupt OFF

