

## PBR VISVODAYA INSTITUTE OF TECHNOLOGY & SCIENCE

KAVALI – 524 201, S.P.S.R Nellore Dist (A.P)

Admn. No. 20731A04I5

Date: 01-11-2024

### STUDY & CONDUCT CERTIFICATE

This is certify that <u>CHITTETI RUSHENDRA</u>	
S/o / D/o CHITTETI CHENCHU KRISHNAIAH	
Is /was a student of this college during the academic year(s)	2020-2024
In the B.TECH, ECE	Course.
His/Her conduct and character is/wasGOOD	10000 Willey



Principal
PARVATHAREDDY BABUL REDDY
VISVODAYA INSTITUTE OF TECHNOLOGY & SCIENCE
(AUPRINOMPAS)
KAVALI - 524201 S PS R Nellore Dist. A P

#### JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR ANANTHAPURAMU - 515 002, ANDHRA PRADESH, INDIA

Adm.no. 20731A0415

Aadhaar.no. XXXXXXXX7649



SI. No. PC 0415860



#### PROVISIONAL CERTIFICATE

This is to certify that Mr. CHITTETI RUSHENDRA Son of Sri CHITTETI CHENCHU KRISHNAIAH & Smt CHITTETI CHENCHU KUMARI, Fassed the

\*\*\*ELECTRONICS & COMMUNICATION ENGINEERING\*\*\*

Examination of this University held in the month of May 2024 and that he was placed in

\*\*\* First Class with Distinction \*\*\*

He has satisfied all the requirements for the award of B. TECH Degree of the

Jawaharlal Nehru Technological University anantapur, Ananthapuramu.

Medium of Instruction: English

Thursday, 18 July 2024

E. W.

DIRECTOR OF EVALUATION Anantapur - A.P.

Angland site

REGISTRAR



# PBR VISVODAYA INSTITUTE OF TECHNOLGOY AND SCIENCE,



KAVALI - 524201 NELLORE DT. ANDHRA PRADESH

S.NO: 9004

R.NO. 20731A04I5

## TRANSFER CERTIFICATE

01. Name of the Student in full : CHITTETI RUSHENDRA

02. Name of the Parent/Guardian : CHITTETI CHENCHU KRISHNAIA

03. Date of Birth : 24/05/2002

04. a) Nationality and Religion : INDIAN-HINDU

b) Caste : BC-D MUTHARASI

05. Date of admission : 08.01.2021

06. Class and year in which the

student was first admitted : FIRST YEAR B.TECH ECE

07. Class in which the student was

studying at the time of leaving : FINAL YEAR B.TECH

08. Whether qualified for promotion: YES

09. Whether the student has paid all dues to the college : YES

an dues to the conego

10. Data on which the student actually left the Institute : 15.05.2024

11. Date on which application for T.C. was made by the student : 01.11.2024

12. Reasons for applying for TC : COURSE COMPLETED

KAVALI S.P.S.R. Mellore Dt.

PBRVITS, Kavali

PARVATHAREDDY BABUL REDDY
VISVODAYA INSTITUTE OF TECHNOLOGY & SCIENCE
(AUTONOMOUS)

KAVALI - 524201 SPSR Nellore Dist. AP



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR ANANTAPURAMU - 515 002, ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO/GRADE SHEET/CREDIT SHEET

CMM. No. JAC 34 3341 Serial No.

Name CHITTETI RUSHENDRA

S. No.	SUBJECT TITLE	INT. MARKS	EXT. MARKS	TOTAL	GRADE	CREDITS	S. No.	SUBJECT TITLE	INT. MARKS	EXT. MARKS	TOTAL.	Grade	CREDITS
	I SEMESTE				-	IY	EA	R II SEMESTER		2	F		0
T,	NEAR ALGEBRA AND CALCULUS		1	1	-	1	_		444	144			2
- 1	PPLIED PHYSICS	29	45	74	8	3	1	Differential Equations and Vector Calculus Chemistry	36	4	74	5	
1	OMMUNICATIVE ENGLISH	28	41	69	C	3	ĺ,	C-Programming & Data Structures	27	35 25	62	0	
	INDAMENTALS OF ELECTRICAL CIRCUITS	30	40	70	В	3	4	Electronic Devices & Circuits	27	28	49	E 0	
	NGINEERING DRAWING	30	34	64	c	2	5	Engineering Workshop	30	79	100	9	,
	IGINEERING GRAPHICS LAB	30	67	97	5	1	5	IT Workshop	28	69	95	5	
- 1	PPLIED PHYSICS LAB	30	70	100	S	1.5	7	C-Programming & Data Structures Lab	28	65	34	s	
4	DMMUNICATIVE ENGLISH LAB	27	60	87	A	1.5	8	Chemistry Lab	36	55	85	A	-
-	INDAMENTALS OF ELECTRICAL CIRCUITS LAB	28	70	88	S	1.5	10	Electronic Devices & Circuits Lab  Environmental Science	36 24	56	95 24	S	
S	SPA 8.23						S	GPA 7.77					
	I SEMESTE	R			1	IY	EA	R II SEMESTER					
1	omplex variables and Transforms	30	25	55	D	3	1	Probability Theory & Stochastic Processes	23	40	58	c	
1	gnals & Systems	30	34	64	c	3	2	Digital Logic Design	30	37	57	c	
	ectrical Engineering	29	35	64	C	3	3	EM Waves and Transmission Lines	28	40	58	C	
4	nalog Circuits	29	44	73	В	3	4	Communication Systems	29	50	79	8	
10	anagerial Economics & Financial Analysis mulation Lab	28	44	72	В	3	5	Linear and Digital IC Applications	29	57	36		
1	ectrical Engineering Lab	26 29	65	91 98	5	1.5	6 7	Digital Logic Design Lab  Communication Systems Lab	30	87	97	5	
1	nalog Circuits Lab	25	65	91	7000	1.5	2	Linear and Digital IC Applications Lab	29	66 E9	95	5	
1	OC-I: Application Development with Python	27	63	90	5	2	9	SOC-II: Soft Skills	28	67	95	5	
1	niversal Human Values	29	0	29	Y			Design Thinking for Innovation	29		23	Y	
	SS/NCC/NSO Activities	30	•	30	Y								
SC	PA 8.05						S	GPA 8.33					
	I SEMESTE	R			1	II Y	EA	II SEMESTER					
1	ontral Systems Engineering	26	45	71	8	3	1	Antennas & Microwave Engineering	24	48	72	8	
	gital Signal Processing	26	31	57	D	3	2	VLSI Design	21	49	76	8	
1	croprocessors and Microcontrollers	20	25	45	E	3	3	Communication Networks	23	43	66	c	
1	omputer Architecture & Organization	27	31	56	D	3	4	Embedded System Design	28	60	88	1	
1	ectrical Vehicle gital Signal Processing Lab	29 27	52 66	81 93	A	3	5	Introduction to Robotics Antennas & Microwave Engineering Lab	28 29	54	82 98	A	
1	croprocessors and Microcontrollers Lab	24	63	87	1	1.5	7	VLSI Design Lab	30	66	96	S	
1	OC III-PCB Design and Prototype Development	28	67	95	5	2	8	Communication Networks Lab	30	65	95	s	
10	ommunity Service Project	98		98	8	1.5	9	SOC-IV:RF System Design	27	50	87	A	
-							10	Intellectual Property Rights & Patents	30		30	٧	
	PA 7.7						The state of the s	GPA 8.65				Ш	
	I SEMESTE	R			1	VY	EA	II SEMESTER					
8	sellite Communications	24	42	66	c	3	1	Full Internship & Project Work	57	139	196	S	
100	oud Computing	24	43	67	C	3				1000			
	Itular & Mobile Communications	26	49	75	8	3							
	inagement Science	28 29	33	61	C	3				1		-	
1	eb Technologies newable Energy Systemes	29	45	74	В	3			Vi Breel	1000			
	ic-V:Industrial IOT & Automation	28	63	91	8	2				1000	14 18		
п	aluation of Industry Internship		96	96	5	3							
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Audit Course Y-Satisfactory N-Unsatisfactory

**AGGREGATE MARKS SECURED: 5119** 

PERCENTAGE OF MARKS: 79.98 %

Number of Credits registered for : 160 Total Credits Acquired : 160 Month of Declaration of Results : June 2024

