



KITRA 530

Data sheet

05/10/2017





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.1 REVISION HISTORY

Date	Revision	Description
11/08/2017	0.1	Draft version
05/10/2017	0.2	Expansion connector image modified



.2 KITRA 530 IMAGES

TOP VIEW



BOTTOM VIEW





.3 INTRODUCTION

This data sheet provides the description of the KITRA 530 board.

KITRA is a family of boards and includes a set of carrier boards for Samsung ARTIK modules, KITRA 530 is a carrier board of the Samsung ARTIK 530 module.

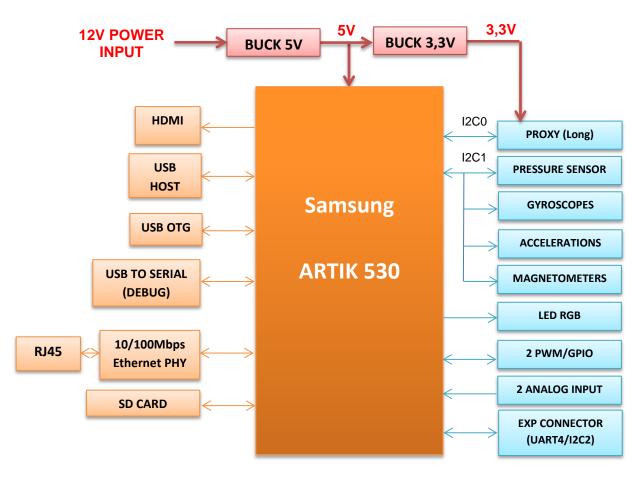
As RushUp electronics platform, KITRA 530 is a product accelerator and can be used from makers, developers, high mix low volume products and from all who want the benefit of an off the shelf industrialized board and doen't have time and/or money to invest in a custom solution.

For details about RushUp and ARTIK, please visit:

www.rushup.tech www.artik.io

.4 DESCRIPTION

KITRA 530 is the product accelerator for IoT Industry 4.0, factory automation, smart home gateway, multimedia applications, video cameras and home appliances and includes multicore processing, memories, crypto, sensors, communication interfaces and GPIO, as well as the power supply circuits.



.5 HARDWARE & COMPONENTS DETAIL

.5.1 Samsung ARTIK 530 module

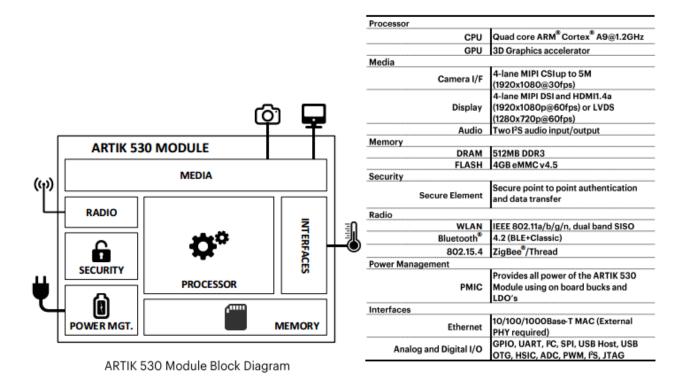
Samsung ARTIK 530 is the module of the KITRA 530 board and it embeds four specific functions:

- Processing:
- Memory;
- · Wireless;
- Data security.

Samsung's ARTIK[™] 530 Module is a highly-integrated System-in module that utilizes a quad core ARM® Cortex® -A9 processor packaged DRAM and Flash memories, a Secure Element and a wide range of wireless communication options such as 802.11a/b/g/n/ac, Bluetooth® 4.1, Bluetooth Low Energy (BLE), and 802.15.4/ZigBee® communications all into a 30x25mm footprint. The many standard digital control interfaces support external sensors and higher performance peripherals to expand the module capabilities. With the



combination of Wi-Fi, Bluetooth and ZigBee the ARTIK 530 Module is the perfect choice for home automation and home hub devices, while also supporting a rich UI/UX capability with the camera and display options. The hardware based Secure Element works with the ARM TrustZone® and Trustonic's Trusted Execution Environment (TEE) to provide "bank level" security end-to-end.



Please refer to www.artik.io website for more details on Samsung ARTIK 530 module.

.5.2 Accelerometers - Magnetometers Sensors

The LSM303AGR is an ultra-low-power high-performance system-in-package featuring a 3D digital linear acceleration sensor and a 3D digital magnetic sensor.

The LSM303AGR has linear acceleration full scales of ±2g/±4g/±8g/±16g and a magnetic field dynamic range of ±50 gauss.

The LSM303AGR includes an I2C serial bus interface that supports standard, fast mode, fast mode plus, and high-speed (100 kHz, 400 kHz, 1 MHz, and 3.4 MHz) and an SPI serial standard interface.

The system can be configured to generate an interrupt signal for free-fall, motion detection and magnetic field detection.

The magnetic and accelerometer blocks can be enabled or put into power-down mode separately.

The LSM303AGR is available in a plastic land grid array package (LGA) and is guaranteed to operate over an extended temperature range from -40 °C to +85 °C.

Features

- 3 magnetic field channels and 3 acceleration channels
- ±50 gauss magnetic dynamic range
- Hard, soft ironing for external magnetic sensor corrections
- ±2/±4/±8/±16 g selectable acceleration full scales
- 16-bit data output
- Analog supply voltage 1.71 V to 3.6 V
- Selectable power mode/resolution for accelerometer and magnetometer
- Single measurement mode for magnetometer
- Programmable interrupt generators for free-fall, motion detection and magnetic field detection
- Embedded self-test
- Embedded temperature sensor



- Embedded FIFO
- ECOPACK®, RoHS and "Green" compliant

For any specific information and for the firmware commands and procedures, please refer to the data sheet of the components.

.5.2.1 Accelerometers - Gyroscopes Sensors

The LSM6DSL is a system-in-package featuring a 3D digital accelerometer and a 3D digital gyroscope performing at 0.65 mA in high-performance mode and enabling always-on low-power features for an optimal motion experience for the consumer.

The LSM6DSL supports main OS requirements, offering real, virtual and batch sensors with 4 kbyte for dynamic data batching.

ST's family of MEMS sensor modules leverages the robust and mature manufacturing processes already used for the production of micromachined accelerometers and gyroscopes.

The various sensing elements are manufactured using specialized micromachining processes, while the IC interfaces are developed using CMOS technology that allows the design of a dedicated circuit which is trimmed to better match the characteristics of the sensing element.

The LSM6DSL has a full-scale acceleration range of $\pm 2/\pm 4/\pm 8/\pm 16$ g and an angular rate range of $\pm 125/\pm 245/\pm 500/\pm 1000/\pm 2000$ dps.

High robustness to mechanical shock makes the LSM6DSL the preferred choice of system designers for the creation and manufacturing of reliable products.

The LSM6DSL is available in a plastic land grid array (LGA) package.

Features

- Power consumption: 0.4 mA in combo normal mode and 0.65 mA in combo high-performance mode
- "Always-on" experience with low power consumption for both accelerometer and gyroscope
- Smart FIFO up to 4 kbyte based on features set
- Android M compliant
- Hard, soft ironing for external magnetic sensor corrections
- ±2/±4/±8/±16 g full scale
- ±125/±245/±500/±1000/±2000 dps full scale
- Analog supply voltage: 1.71 V to 3.6 V
- Independent IOs supply (1.62 V)
- Compact footprint, 2.5 mm x 3 mm x 0.83 mm
- SPI & I² C serial interface with main processor data synchronization feature
- Pedometer, step detector and step counter
- Significant motion and tilt function
- Standard interrupts: free-fall, wakeup, 6D/4D orientation, click and double-click
- Embedded temperature sensor
- ECOPACK®, RoHS and "Green" compliant

For any specific information and for the firmware commands and procedures, please refer to the data sheet of the components.

.5.2.2 Humidity and temperature sensors

The HTS221 is an ultra-compact sensor for relative humidity and temperature. It includes a sensing element and a mixed signal ASIC to provide the measurement information through digital serial interfaces.

The sensing element consists of a polymer dielectric planar capacitor structure capable of detecting relative humidity variations and is manufactured using a dedicated ST process.

The HTS221 is available in a small top-holed cap land grid array (HLGA) package guaranteed to operate over a temperature range from -40 °C to +120 °C.

Features

- 0 to 100% relative humidity range
- Supply voltage: 1.7 to 3.6 V



- Low power consumption: 2 μA @ 1 Hz ODR
- Selectable ODR from 1 Hz to 12.5 Hz
- High rH sensitivity: 0.004% rH/LSB
- Humidity accuracy: ± 3.5% rH, 20 to +80% rH
- Temperature accuracy: ± 0.5 °C,15 to +40 °C
- Embedded 16-bit ADC
- 16-bit humidity and temperature output data
- SPI and I²C interfaces
- Factory calibrated
- Tiny 2 x 2 x 0.9 mm package
- ECOPACK® compliant

For any specific information and for the firmware commands and procedures, please refer to the data sheet of the components.

.5.3 Barometer/Pressure sensor

The LPS22HB is an ultra-compact piezo resistive absolute pressure sensor which functions as a digital output barometer.

The device comprises a sensing element and an IC interface which communicates through I2C or SPI from the sensing element to the application.

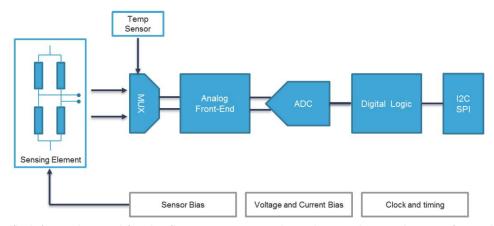
The sensing element, which detects absolute pressure, consists of a suspended membrane manufactured using a dedicated process developed by ST.

The LPS22HB is available in a full-mold, holed LGA package (HLGA). It is guaranteed to operate over a temperature range extending from -40 °C to +85 °C. The package is holed to allow external pressure to reach the sensing element.

Features

- 260 to 1260 hPa absolute pressure range
- Current consumption down to 3 µA
- High overpressure capability: 20x full-scale
- Embedded temperature compensation
- 24-bit pressure data output 16-bit temperature data output
- ODR from 1 Hz to 75 Hz
- SPI and I2C interfaces
- Embedded FIFO
- Interrupt functions: Data Ready, FIFO flags, pressure thresholds
- Supply voltage: 1.7 to 3.6 V
- High shock survivability: 22,000 g
- Small and thin package ECOPACK® lead-free compliant

Block diagram



For any specific information and for the firmware commands and procedures, please refer to the data sheet of the components.



.5.4 Proximity sensor

The VL53L0X is a new generation Time-of-Flight (ToF) laser-ranging module housed in the smallest package on the market today, providing accurate distance measurement whatever the target reflectance unlike conventional technologies. It can measure absolute distances up to 2m, setting a new benchmark in ranging performance levels, opening the door to various new applications.

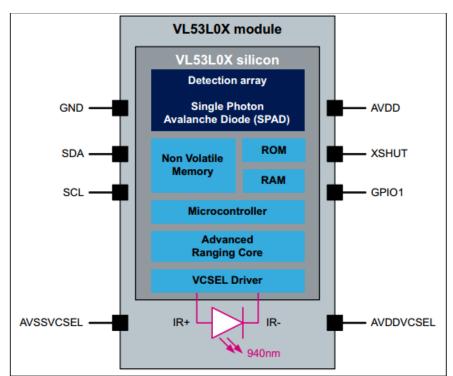
The VL53L0X integrates a leading-edge SPAD array (Single Photon Avalanche Diodes) and embeds ST's second generation FlightSense patented technology.

The VL53L0X's 940nm VCSEL emitter (Vertical Cavity Surface-Emitting Laser), is totally invisible to the human eye, coupled with internal physical infrared filters, it enables longer ranging distance, higher immunity to ambient light and better robustness to cover-glass optical cross-talk.

Features

- Fully integrated miniature module
 - 940nm Laser VCSEL
 - VCSEL driver
 - Ranging sensor with advanced embedded micro controller
 - 4.4 x 2.4 x 1.0mm
- · Fast, accurate distance ranging
 - Measures absolute range up to 2m
 - Reported range is independent of the target reflectance
 - Operates in high infrared ambient light levels
 - Advanced embedded optical cross-talk compensation to simplify cover glass selection
- Eye safe
 - Class 1 laser device compliant with latest standard IEC 60825-1:2014 3rd edition
- Easy integration
 - Single reflowable component
 - No additional optics
 - Single power supply
 - I2C interface for device control and data transfer
 - Xshutdown (Reset) and interrupt GPIO
 - Programmable I2C address

Block Diagram



For any specific information and for the firmware commands and procedures, please refer to the data sheet of the components.



.5.5 ETHERNET

The KSZ9031RNX is a completely integrated triple speed (10Base-T/100Base-TX/1000Base-T) Ethernet Physical Layer Transceiver for transmission and reception of data over standard CAT-5 unshielded twisted pair (UTP) cable. The KSZ9031RNX reduces board cost and simplifies board layout by using on-chip termination resistors for the four differential pairs and by integrating a LDO controller to drive a low cost MOSFET to supply the 1.2V core. On the copper media interface, the KSZ9031RNX can automatically detect and correct for differential pair misplacements and polarity reversals, and correct propagation delays and re-sync timing between the four differential pairs, as specified in the IEEE 802.3 standard for 1000Base-T operation. The KSZ9031RNX provides the Reduced Gigabit Media Independent Interface (RGMII) for direct connection to RGMII MACs in Gigabit Ethernet Processors and Switches for data transfer at 10/100/1000 Mbps speed. The KSZ9031RNX Evaluation Board (KSZ9031RNX-EVAL) provides a comprehensive platform to evaluate the KSZ9031RNX features. All KSZ9031RNX configuration pins are accessible either by jumpers, test points or interface connectors.

For any specific information and for the firmware commands and procedures, please refer to the data sheet of the components.

IMPORTANT FUNCTIONAL NOTE:

In KITRA 530 board the KSZ9031RNX is used in 10/100 Mbps with RGMII interface.

.5.6 HDMI

The board has one HDMI 1.4a connector (Micro D-Type).

The following video formats are supported:

- 480p/480i @59.94Hz/60Hz, 576p/576i@50Hz
- 720p/720i @50Hz/59.94Hz/60Hz
- 1080p/1080i @50Hz/59.94Hz/60Hz

.5.7 USB HOST 2.0

The board has one USB HOST 2.0.

.5.8 USB OTG

KITRA 530 has one USB OTG.

.5.9 USB to serial (UART) converter

The platform has an embedded USB to UART converter that is connected directly to the debug serial port of the ARTIK 530 module that is useful during the development.

See the chapter 10 for the development details and how this serial port can be used.

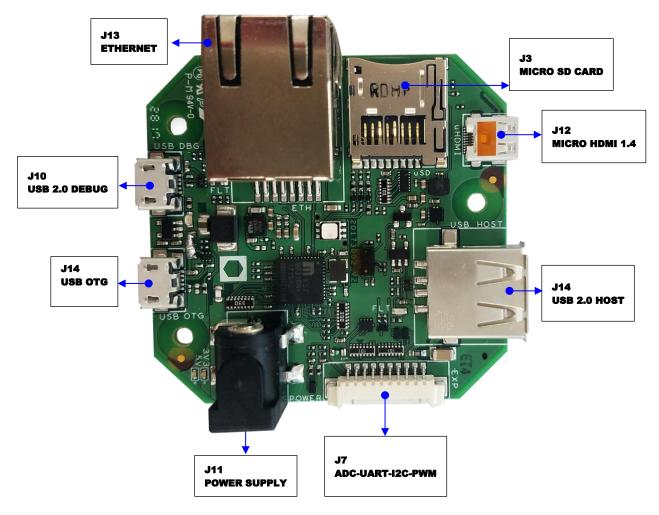
.5.10 SD CARD

The Platform board has one SD-CARD interface supporting SD3.0.



.6 BOARD CONNECTORS

MICROUSB-B (USB OTG)	J14	HDMI TYPE-D	J12
MICROUSB-B (USB DEBUG)	J10	ETHERNET RJ45	J13
MICRO SD CARD CONNECTOR	J3	12V POWER CONNECTOR	J11
EXPANSION CONNECTOR	J7	USB TYPE A (USB HOST)	J8



.6.1 POWER SUPPLY CONNECTOR

Input voltage type: DC Typical input voltage: 12V Power Input Max: 15W

Connector type mounted on KITRA 530: Cui PJ-002AH-SMT-TR Mate with: Power Jack DI 2.1mm DE 5.5mm (Example Cui PP3-002A)

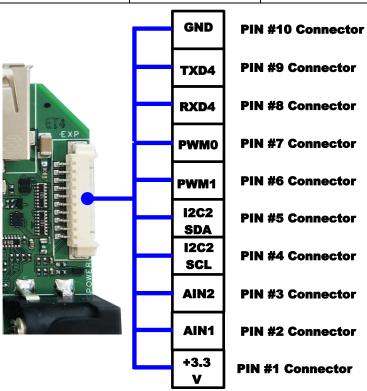
.6.2 EXPANSIONS CONNECTOR

FUNCTIONAL NOTES ON EXPANSION CONNECTOR LINES

- Lines PWM0 and PWM1 have a couple of 100ohm internal resistor so maximum current per output is 15mA.
- Lines ADC_AIN1 and ADC_AIN2 have an internal voltage divider that scale down the external voltage to a factor equal to 0,56. Maximum voltage at the ARTIK ADC pin is 1,8V and then externally the maximum voltage is 3,3V.
- I2C1 SDA/I2C1 SCL have 2,49kohm internal pull up to 3,3V.



PIN NAME	KITRA 530 PIN CONNECTOR	ARTIK 530 PIN	SPECIFICATIONS
+3V3	J7-1		Voltage source that can be used
			to power external sensors. Max-
			imum current 100mA, protected
			by a resettable fuse.
ADC_AIN1	J7-2	AP_ADC0	12 bit resolution.
			3,3V absolute maximum range of
			analog voltage input. 780ohm
			series and 1kohm pull down.
ADC_AIN2	J7-3	AP_ADC1	12 bit resolution.
			3,3V absolute maximum range of
			analog voltage input. 780ohm
			series and 1kohm pull down.
I2C2_SCL	J7-4	AP_GPD6_SCL	2,49kohm pull up to internal
			3,3V.
I2C2_SDA	J7-5	AP_GPC7_SDA	2,49kohm pull up to internal
			3,3V.
PWM1	J7-6	AP_GPC14_PWM2	3,3V voltage level of the digital
			output.
			200ohm series resistor.
PWM0	J7-7	AP_GPD1_PWM0	3,3V voltage level of the digital
			output.
			200ohm series resistor.
UART4_RX		AP_UARTRX4	3,3V INPUT
UART4-TX	J7-9	AP_UARTTX4	3,3V OUTPUT
GND	J7-10		



Connector type mounted on KITRA 530: Molex Picoblade 53398-1071 Mate with: Molex Picoblade 51021-1000



.6.3 ANTENNA CONNECTORS

ARTIK 530 module embeds all the wireless functionalities related to Wi-Fi, Bluetooth, Zigbee and Thread and only external antenna must be attached. To do this the module has a couple of uFL connectors as indicated in the picture below.



Two antennas are required to use the full set of radio communication links on the ARTIK 710 Module. One supports the combination of Wi-Fi and BT, and the other is dedicated to 802.15.4 for ZigBee or Thread.

Caution: Do not apply power (enable) the radio chips before connecting antennas or damage to the chip may result.

The U.FL-R-SMT Hirose connector is used for both the BT/Wi-Fi and the 802.15.4 for ZigBee or Thread antenna connectors on the ARTIK 530 Module.

The mechanical size of the connector (receptacle) is described here in after, for suggestions on mating plug and more details on the connector, please contact Hirose Electric Co., LTD.

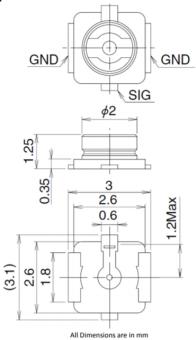


Figure 7. RF Connector for BT/Wi-Fi and ZigBee/Thread



.7 LEDS

KITRA 530 provides one RGB LED connected to 3 output lines connected directly to ARTIK 530 that can be managed by application software.



The board embeds also a couple of green LED useful for diagnostic that are directly connected to the power supplies output (5V and 3,3V).



.8 BUTTONS

RESET KEY	PB3-PB4		
POWER KEY	PB1-PB2		



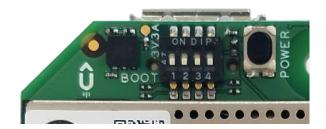
POWER push button is used to power up KITRA 530 board by pressing for about one second and RESET can be used to reboot the system.

Power ON will be indicated by the RGB LED that turn on in RED color and then in GREEN.

.9 BOOTING SEQUENCE

This section describes the various boot modes that are supported on the KITRA 530 board. Next table and figure show how to manipulate dipswitch located on the board to set the various booting options that are available.

Dipswitch position	eMMC 1st Boot	SD Card 1st Boot	USB 1st Boot
1	OFF	OFF	ON
2	OFF	OFF	ON
3	Χ	X	Χ
4	OFF	ON	Х



When 'eMMC 1st Boot' is selected as a booting option, the system will first try to boot from eMMc, if this fails the system will search for an SD Card to boot from. If booting from the SD-Card also fails the system tries to boot from USB. When choosing the SD-Card booting option, the system starts with booting from SD, and if



this fails will continue to try a USB boot. When USB is selected as the booting mechanism of choice, only a USB boot will be attempted.

.10 KITRA 530 DEVELOPMENT BOARD BOOTING

This section will describe how to start working with your KITRA 530 Development Environment by setting up a serial connection on your development PC and booting up the ARTIK 530 Development Environment.

SERIAL PORT CONNECTION

As a first step we will select a serial console to communicate with the ARTIK 530 Module that is located on the ARTIK 530 Development Environment.

You can use a typical Linux® serial console as depicted in the next Figure, using the USB 2.0 DEBUG connector.

To use the serial USB cable you need to install the associated device driver..

```
mskim@dage2-PowerEdge-T620: ~
                                                                                                                   Execute a single command in a subshell on the local system. If command is omitted, subshell is invoked.
                    Get help. With no arguments, telnet prints a help summary. If a command is specifi print the help information for just that command.
ENVIRONMENT
      Telnet uses at least the HOME, SHELL, DISPLAY, and TERM environment variables. Other environme propagated to the other side via the TELNET ENVIRON option.
      ~/.telnetrc
                         user customized telnet startup values
HISTORY
      The Telnet command appeared in 4.2BSD.
NOTES
      On some remote systems, echo has to be turned off manually when in "old line by line" mode.
      In "old line by line" mode or LINEMODE the terminal's eof character is only recognized (and sentem) when it is the first character on a line.
      The source code is not comprehensible.
                                                                    August 15, 1999
mskim@dage2-PowerEdge-T620:~$
mskim@dage2-PowerEdge-T620:~$
mskim@dage2-PowerEdge-T620:~$
```

Setting up a connection with the ARTIK 530 Module can be done in a wired or wireless manner. Here we choose to install PuTTY a free serial console.

The software can be downloaded from HTTP://WWW.PUTTY.ORG/.

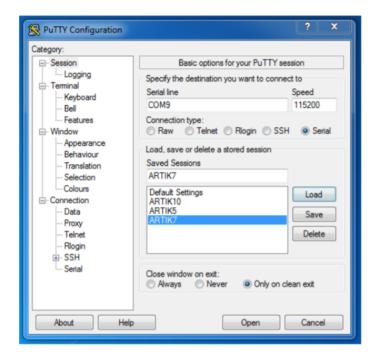
Once downloaded go through the following steps:

- 1) Open the device manager on the control panel.
- When using a PC install the USB to Serial driver. The driver can be found at the following location: (http://www.ftdichip.com/Drivers/CDM/CDM21218_Setup.zip). For other drivers please visit (http://www.ftdichip.com/Drivers/D2XX.htm).
- 3) Check the COM port number on your PC when you connect the USB serial cable. In our case the COM port allocated is COM9.



- 4) Set the PuTTY configuration as follows:
 - a) Set the "Serial line" as the COM port number found in step 3.
 - b) Set the COM speed to "115200".
 - c) Set the connection type to "Serial".
 - d) Save the session under KITRA-530.
- 5) Select your saved session and click the "Open" button.





POWER ON THE KITRA 530

To power up the KITRA 530 you first have to connect the power adapter and the Platform Board as shown following the requirements indicated in chapter 6.1.

Once the jack connector of the power supply is plugged into the KITRA 530 power port, push the power button (indicate in chapter 8) for about one second. Once released the booting process will start and you should see booting messages from your console, using the serial connection that you previously established

.11 RF ELECTRICAL SPECIFICATIONS

All performance numbers related to 802.11/Bluetooth® and 802.15.4 mentioned in this section are preliminary and likely to change once module characterization has taken place.

802.11, 2,4GHz Receive RF specifications

Parameter	Conditions	Min	Тур.	Max	Unit
Frequency Range	-	2400	-	2500	MHz
Mi	nimum receiver sensitivity	in 802.11b n	node (2.4GH	z)	
1Mbps		-	- 97	-	dBm
2Mbps	PER < 8%,	-	- 95	-	dBm
5.5Mbps	Packet size = 1024 bytes	-	- 94	-	dBm
11Mbps		-	-90	-	dBm
Mi	nimum receiver sensitivity	in 802.11g n	node (2.4GH	z)	
6Mbps		-	-91	-	dBm
9Mbps	PER < 10%, Packet size= 1024 bytes	-	- 90	-	dBm
12Mbps		-	-89	-	dBm
18Mbps		-	-87	-	dBm
24Mbps		-	-84	-	dBm
36Mbps		-	- 81	-	dBm
48Mbps		-	- 76	-	dBm
54Mbps		-	-75	-	dBm
Mi	nimum receiver sensitivity	in 802.11n n	node (2.4GH	z)	
MCS 0		-	-89	-	dBm
MCS 1		-	-88	-	dBm
MCS 2		-	-86	-	dBm
MCS 3	PER<10%,	-	-83	-	dBm
MCS 4	Packet size= 4096 bytes, GF, 800ns GI, Non-STBC	-	-79	-	dBm
MCS 5		-	-75	-	dBm
MCS 6		-	-73	-	dBm
MCS 7]	-	-72	-	dBm



802.11, 2,4GHz Transmit RF specifications

Parameter	Conditions	Min	Тур.	Max	Unit
Linear output power					
Maximum output power in 802.11b mode	A 10 11 -	-	15	-	dBm
Maximum output power in 802.11g mode	As specified in IEEE802.11	-	15	-	dBm
Maximum output power in 802.11n mode	TEEE002.11	-	13	-	dBm
Transmit spectrum mask					
Margin to 802.11b spectrum mask		0	-	-	dBr
Margin to 802.11g spectrum mask	Maximum output power	0	-	-	dBr
Margin to 802.11n spectrum mask	output power	0	-	-	dBr
Transmit modulation accuracy in 802.11b mod	le				
1Mbps		-	-	35	%
2Mbps	As specified in	-	-	35	%
5.5Mbps	IEEE 802.11b	-	-	35	%
11Mbps]	-	-	35	%
Transmit modulation accuracy in 802.11g mod	le				
6Mbps		-	-	-5	dB
9Mbps		-	-	-8	dB
12Mbps]	-	-	-10	dB
18Mbps	As specified in	-	-	-13	dB
24Mbps	IEEE 802.11g	-	-	-16	dB
36Mbps]	-	-	-19	dB
48Mbps]	-	-	-22	dB
54Mbps	1	-	-	-25	dB
Transmit modulation accuracy in 802.11n mod	le				
MCS7	As specified in IEEE 802.11n	-	-	-27	dB
Transmit power-on and power-down ramp tim	ne in 802.11b mode				
Transmit power-on ramp time from 10% to 90% output power	-	-	-	2	μs
Transmit power-down ramp time from 90% to 10% output power	-	-	-	2	μs

802.11, 5GHz Receive RF specifications

Parameter	Conditions	Min	Typ.	Max	Unit
Frequency Range	-	4900	-	5845	MHz
	Minimum receiver sensit	ivity in 802.	11a mode		
6Mbps		-	-90	-	dBm
9Mbps		-	-89	-	dBm
12Mbps		-	-88	-	dBm
18Mbps	PER < 10%	-	-87	-	dBm
24Mbps	PER < 10%	-	-84	-	dBm
36Mbps		-	-80	-	dBm
48Mbps	num receiver sensitivity ir	-	-76	-	dBm
54Mbps		-	- 75	-	dBm
Min	imum receiver sensitivity	in 802.11n	(HT-20) mode	•	
MCS 0		-	-89	-	dBm
MCS 1	PER < 10%	-	-88	-	dBm
MCS 2		-	-85	-	dBm
MCS 3		-	-82	-	dBm
MCS 4	PER < 10%	-	- 79	-	dBm
MCS 5		-	-75	-	dBm
MCS 6		-	-72	-	dBm
MCS 7		-	-71	-	dBm
Min	imum receiver sensitivity	in 802.11n	(HT-40) mode	9	
MCS 0		-	-86	-	dBm
MCS 1		-	-85	-	dBm
MCS 2		-	-83	-	dBm
MCS 3	DED < 1006	-	-80	-	dBm
MCS 4	PER < 10%	-	-77	-	dBm
MCS 5		-	-73	-	dBm
MCS 6		-	-71	-	dBm
MCS 7		-	- 69	-	dBm



802.11, 5GHz Transmit RF specifications

Parameter	Conditions	Min	Тур.	Max	Unit		
Frequency Range	-	4900		5845	MHz		
	Linear output power						
Maximum output power in 802.11a mode	54M, UNII-2e	-	13	-	dBm		
Maximum output power in 802.11n mode	HT20, MCS7, UNII-2e	-	12	-	dBm		
	HT40, MCS7, UNII-2e	-	11	-	dBm		
	Transmit spectrum mask						
Margin to 802.11a spectrum mask	Maximum autaut nawar	0	-	-	dBr		
Margin to 802.11n spectrum mask	Maximum output power	0	-	-	dBr		
Transı	mit constellation error in 802	.11a mode					
54Mbps	As specified in IEEE 802.11n	-	-	-25	dB		
Transmit constellation error in 802.11n (HT-20, HT-40) mode							
MCS 7	As specified in IEEE 802.11n	-	-	-27	dB		

Bluetooth receive RF specifications

Parameter	Conditions	Min	Тур	Max	Unit
Frequency Range	-	2402	-	2480	MHz
	GPSK, BER ≤0.1%	-	- 92	-	dBm
Sensitivity (BER)	π/4-DQPSK, BER ≤ 0.1%	-	- 92	-	dBm
	BER ≤ 0.1%, 8DPSK	-	-89	-	dBm
	GPSK, BER ≤0.1%	-20	-	-	dBm
Maximum Input Level	π/4-DQPSK, BER ≤ 0.1%	-20	-	-	dBm
	BER ≤ 0.1%, 8 DPSK	-20	-	-	dBm
	BDR				
Intermodulation Performance	-	-	-	0.1	%
	1DH1	-	-	0.1	%
Rx C/I Performance	1DH3	-	-	0.1	%
	1DH5	-	-	0.1	%
	EDR				
	2DH1	-	-	0.1	%
	2DH3	-	-	0.1	%
Rx C/I Performance	2DH5	-	-	0.1	%
RX C/1 Performance	3DH1	-	-	0.1	%
	3DH3	-	-	0.1	%
	3DH5	-	-	0.1	%
Rx BER Floor Performance	BER ≤ 0.001%	-	-	-70	dBm

Bluetooth transmit RF specifications

Parameter	Conditions	Min	Тур	Max	Unit	
Frequency Range	-	2402	-	2480	MHz	
Output Power (Average)	Output Power (Average)					
BDR (QPSK)	2440 MHz	-	6	-	dBm	
EDR (π/4-DQPSK)	2440 MHz	-	2	-	dBm	
EDR (8DPSK)	2440 MHz	-	2	-	dBm	

BLE RF specifications

Parameter	Conditions	Min	Тур	Max	Unit
Frequency Range	-	2402	-	2480	MHz
Rx Receiver Sensitivity PER	At -70dBm	-	-	30.8	%
Rx C/I and Receiver Selectivity Performance PER	-	-	1	30.8	%
Tx Power	-	-	6	-	dBm

802.15. RF specifications

The Typ numbers indicated in the next tables are one standard deviation below the mean, measured at room temperature 25°C. The Min and Max numbers were measured over process corners at room temperature.



802.15.4 receive RF specifications

Parameter	Test Condition	Min	Тур	Max	Unit
Operating Frequency Range	-	2400	-	2483.5	MHz
Receiver Sensitivity PER	At -95dBm	-	-	1	%
Receiver Sensitivity Search	At PER 1%	-	- 95		dBm
Receiver Interference Rejection PER	At -2 Channel, Alternate Channel, 30dB	-	-	1	%
Receiver Interference Rejection PER	At -1 Channel, Adjacent Channel, 0dB	-	-	1	%
Receiver Interference Rejection PER	At +1 Channel, Adjacent Channel, 0dB	-	-	1	%
Receiver Interference Rejection PER	At +2 Channel, Alternate Channel, 30dB	-	-	1	%
Error Vector Magnitude - RMS (EVM)	At Target Power	-	-	30	%
Error Vector Magnitude - Offset (EVM)	At Target Power	-	-	10	%
Receiver Maximum Input Level of Desired Signal	At -20dBm Input	-	-	1	%

802.15.4 transmit RF specifications

Parameter	Test Condition	Min	Тур	Max	Unit
Maximum output power	At highest normal mode power setting		6/16*	-	dBm
Minimum output power	At lowest power setting	-	-27	-	dBm
Error vector magnitude (Offset-EVM)	As defined by IEEE 802.15.4-2003, which sets a 35% maximum	-	-	10	%
Carrier frequency error	-	-40	-	+40	ppm
PSD mask relative	3.5 MHz away (Normal)	-20	-	-	dBm
PSD mask absolute	100 KHz BW	-30	-	-	dBm

^{*}The ARTIK 530 Module default setting is 6dBm for CE. You can change the setting to 16dBm for FCC testing.

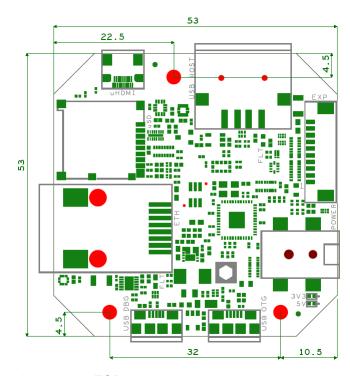
.12 SPECIFICATIONS

Parameter	Minimum	Typical	Maximum	Conditions
External power supply	6V	12V	14V	Standard 5.5mm jack
Power rating			15W	
Operation temperature	0	-	40°C	Without heatsink
Operation temperature	0	-	70°C	With heatsink or cooling
Storage temperature	-25	-	70°C	Electronics board

Wireless standard	Frequency band low	Frequency band high	Transmitted power
Bluetooth	2402 MHz	2480 MHz	9,8 dBm
Wi-Fi	2412 MHz	2472 MHz	17,6 dBm
Wi-Fi	5150 MHz	5350 MHz	17,1 dBm
Wi-Fi	5470 MHz	5725 MHz	16,1 dBm
Wi-Fi	5725 MHz	5850 MHz	11,6 dBm
Zigbee	2405 MHz	2480 MHz	7,8 dBm



.13 MECHANICAL SPECIFICATIONS



The measures are in mm and the view is TOP side.

.14 HOUSING PRESCRIPTION

KITRA 530 is PCBA (Printed Circuit Board Assembly) system and so it is not a final product, it's necessary to close the board in a specific housing or mechanical box.

The housing, in addition to achieve the desired functionality, musts be fireproof (in case of unexpected failure of the electronic board component that produce a little spark, a fire will be blocked).

.15 PACKAGING (STARTER KIT)

Standard package of the KITRA 530 bundle kit is made up with the next items:

- KITRA 530 electronics board:
- 2.4GHz uFL antennas.







.16 DECLARATION OF CONFORMITY

Pending

.17 INDOOR USE ONLY FOR 5GHZ BAND

In the frequency band 5150-5350 MHz, the use of the KITRA 530 with Wi-Fi wireless communications is restricted to indoor use only.

.18 OPERATING ENVIRONMENT

The operating environment excludes special environments (extreme temperatures, dust, humidity, vibrations, flammable gases, corrosive or explosive atmosphere, etc.).

.19 DICLAIMERS

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