

Updated Metadata

Repository

Specification

Interface Design

Specifications

Report Design

Specifications

Submit to Stage

Gate

ETL Design

Specifications

Deliverable Outputs

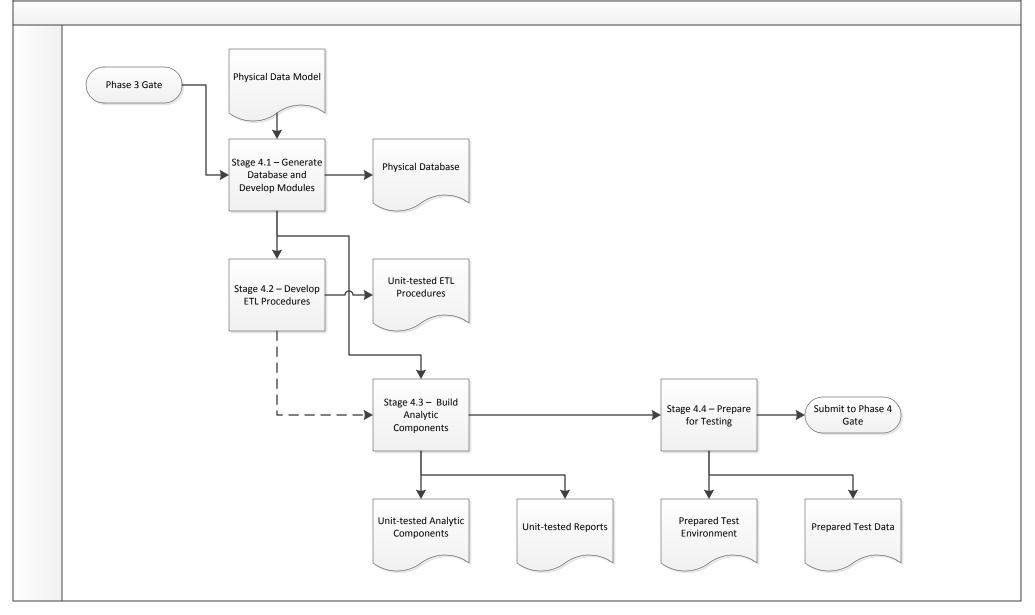
Physical Data Model

Source to Target

Mapping (physical-

level)





Extended Source

System Modules

Final Capacity Plan

Extended Common

Component

Modules

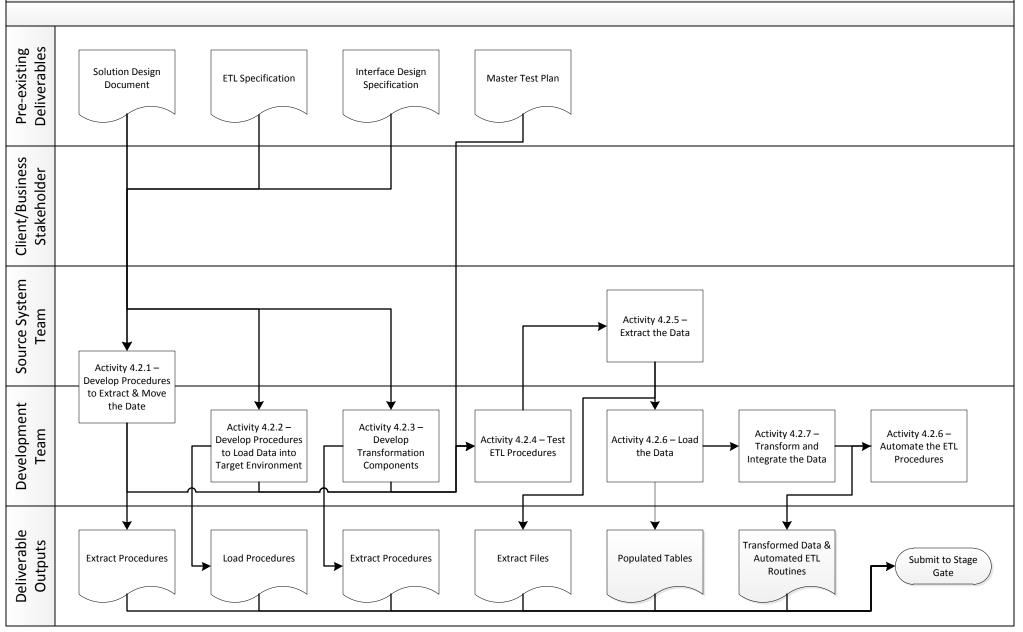
Submit to Stage

Gate

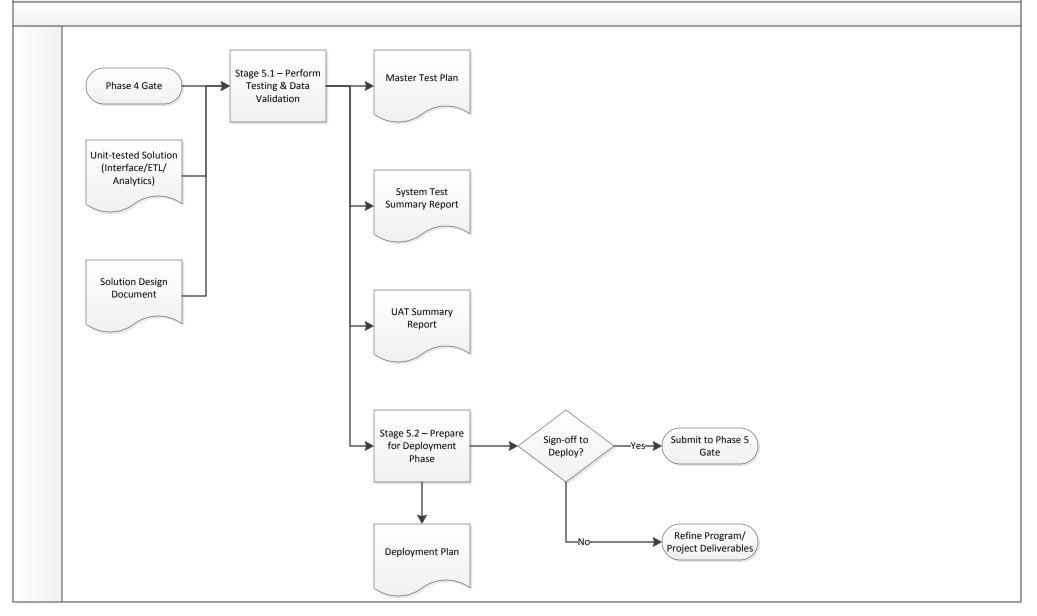
Deliverable Outputs

Built Databases

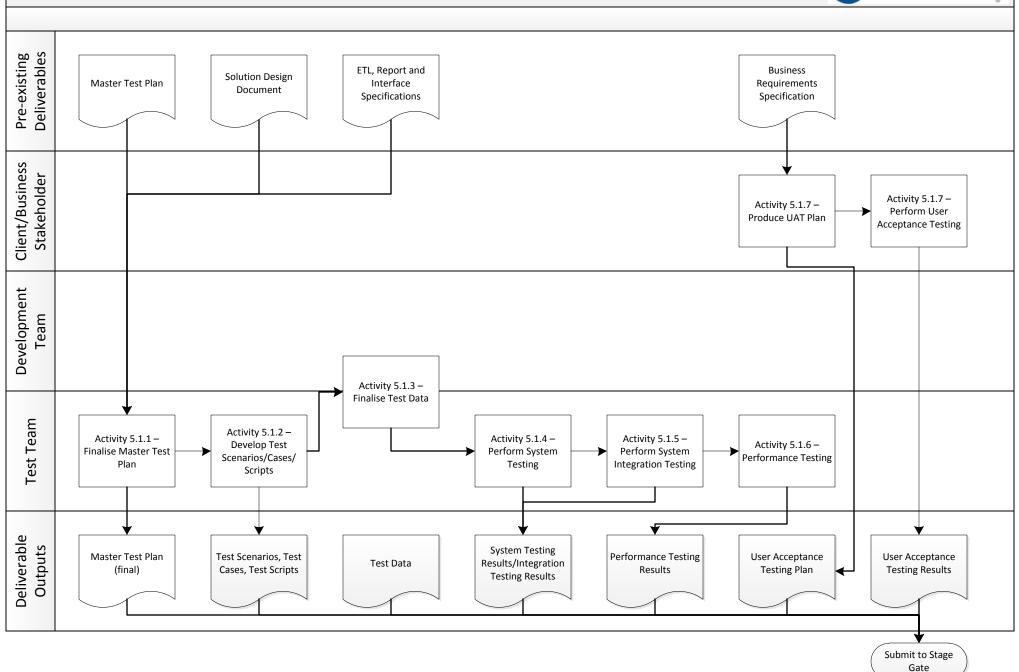












Gate



