Scheduling Algorithm optimization

-> Max CPU utilization

-) Max throughput

TAT = FT-AT

-> Min turnaround time

WT = TAT -BT

-> Min waiting time

-) Min response time

First-come, First-served (FCFS) scheduling

Process	Burst time
Pi	24
Pz	3
P3	3

FCFS: process arrive in order: P1 P2 P3
Gant+ chart for schedule:

waiting time for P1 = 6 P2 = 24

Average Waiting time = (0+24+27) = 17

convoy effect :- short process behind long process.

snortest - Job - First (SJF) scheduling

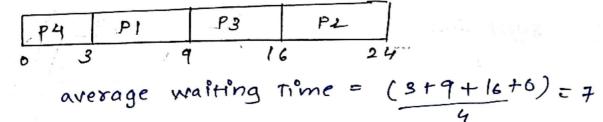
Associate with each process the length of its

SJF is optimal - gives minimum average waiting time for a given set of processes

Example of SJF

process	Busst	Time
Pt	6	7. 17.1
P2	8	TE THE SERVE
P3	7	
P4	3	

SJF scheduling charf



Explaining snortest - Remaining - Time - First or SJF (preemitive)

b socess	Arrival Time Bu	18st - Time
PI	, a de la O ndre de la companya de	8 > 7 7
P2	1	4-13-122>1.76
P3		91 +
P4	3	5.

Process	Arrival	BURST	completion time	TUBN-ground	Waifing Time
PI	0	8	17	17-	# 9
Pz	- 1	4	5	4	0
P3	2	9	26	24	15
P4	3	5	10	7	2
P4	3	5		,	

$$\frac{9+15+2+0}{9} = \frac{26}{4} = 6.5 \text{ msec}$$

Priority-scheduling

A priority number (integer) is associated with

The cpu is allocated to the process with nighest Priority

SJF is priority ocheduling where priority is the inverse of predicted next cru Burst time

problem = starration solution = Aging

Example of Priority-scheduling

baocess	BUOST Time	priority	completion
PI	10	3 ^	
P2	1	10	
P3	2	4	
P4	1	5	- 1
P5	5	2 -	i e

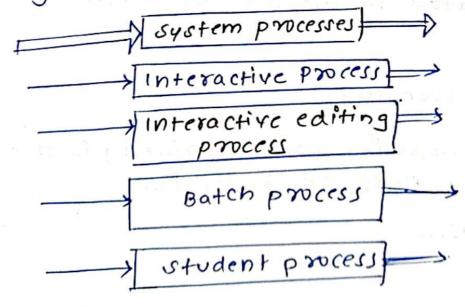
priority scheduling gant-chart

	P2	P 5	PI	وم ا	3	P4	_
0	• 1		6	16	18	19	

Average =
$$\frac{0+1+6+16+18}{5} = 8.2 \text{ m/ec}$$

Multilevel aveve scheduling

nighest priority



briosità

chp.7 Deadlocks

system Model

system consist of resources

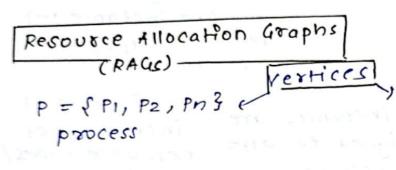
Resources types R1, R2 ... Rm

Each Resource Type has Ri has Wi Instances

Deadlock Arras if four cond hold simultaneously

Mutual Hold Ho

exclusion Sit preemption



RESOUTCE Types

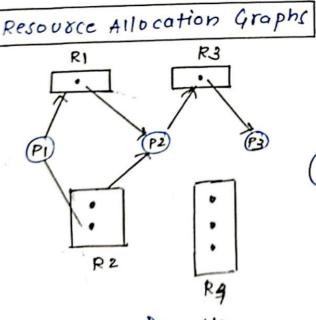
Request :- Directed edge Pi → Rj

Assignment: - Directed edge Rj -> Pi edge

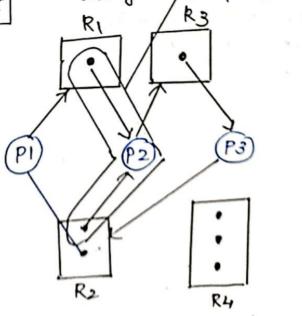
with two resources
assigned to process pz

Ri

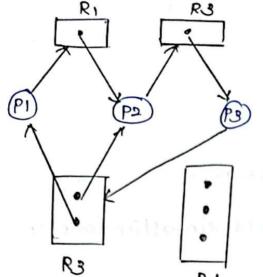
Ri



Graph with Ho Deadlock



graph with a deadlock



Resource Allocation Graph with A Deadlock

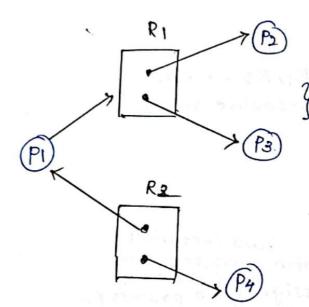
3 All instances are Assigned to one process.

NOME

· If Graph contains
No cycle -> No deadlock

PF graph contains cycle

→ only one instance per resource type, then aeadlock



Graph with cycle But No Deadlock

All Instances are instances per assigned to one resource types/
process each. possibility of deadlock

Avoidance Algorithms

single Instance Multiple Instance
over

Resource Type

Resource Type

Resource Allocation

Graph

Algorithm

Bankers Algorithm

he no. of process me number of resources

Available available[j] = K there are K instances of Resource type Rj available

MAX nxm if max[i,j]=k then process pi may request maximum k instances of resource type Rj

Allocation nxm matrix. If allocation [i,j]=k then Pi currently allocated kinstances of kj

Need! Need[ij]=k then Pi may need k more instance of Rj to complete its tork

Need [i,i] = max [i,i] - allocation [i,i]

Safety - Algorithm

1. Work and Finish dength m and n

work = Available

Finish[i] = false for i=0,1,

- 2. find an i such that
- a) Finish [i] = false
 - 6) Need; < Work
 if no such i exist goto 4)
- 3) WORK = WORK + Allocation Finish[i] = true go to step2)
- 4) Finish[i] == true for all i system in a safe state

Resource - Request Algorithm for process pi

- 1. Requesti ≤ Needi
- 2. Requesti & Avallable

Available = Available - Requesti;

Allocation = Allocation; + Request;

Need = Need; + - Request;

if safe > The resources are availallocated to A'
if unafe > pi must wait, and the old - resource allocation state is restored

Example of Banker's Algorithm

5 process Po +nrough P4;

3 recource types

A (10 instances), B (5 instances) c (7 instances)

	Allocation	Max	Avallable		
	ABC	48 C	ABC		
PO	0 10	753	332		
PI	200	3 2 2			
P2	302	902			
P3	2 1)	222	(A start		
P4	0 0 2	433			

Need = MAX - Allocation

Need P A B C P6 7 4 3 P1 1 2 2 P2 6 0 0 P3 0 1 1 P4 4 3 1

The system is safe state until since the sequence <PI P3 P4 P2 P0> satisfies safety chiteria.

Deadlock Avoidance

simplest and most useful model requires that each process declare the maximum number of resources of each type that it may need.

The deadlock-avoidance algorithm Dynamically examine the resource allocation state to ensure that there never can be a circular-wait condition.

Resource - allocation state is defined by number of available and allocated resources and meximum demands of process

safe state

system is in safe state if there exists a sequence (PI, P2, Pn> of All resource pe to the process in the systems such that for each Pi, the resources that Pi can still request can be resources that Pi can still request can be satisfied by currently available resources t satisfied by all the Pj with j () resources held by all the Pj with j

tnatis

Pi resource needs are not immediately available then pi can wait until all pj nave finished.

when Pi is finished pi can obj botain needed resources, execute, return allocated resources and terminate

when pi terminates pits can obtain its need ful resources and so on.

If a system is in safe state -> NO Deadlocks

If a system is in unsafe state -> possibility of

Deadlock

Avoidance >> Ensage that a system will never enter an unsafe state.

process syncronization

producer and concomer problem! -

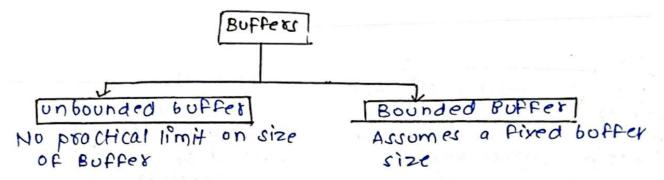
1) A produces process produce information that is consumed by

2) a consumer process.

one solution to producer and consumer is shared memors

3) A Buffer of items that can be filled by the producer and emptied by consumer

Note: The process of producer and consumer should be syncronized, so consumer does not try to consume a item that has not been produced



A situation like this, where several processes access and manipulate the same data concurrently and the outcome of execution depends on particular order on which the access takes place, is caued a race condition.

clearly, we want the resulting changes not to interfere with one another hence, we need process syncronization.

critical-section Problem

caved a critical section, in which process may be changing common variables, updating a table, writing a file and so on

when one process is executing in its critical section, no other process is to be autowed to execute in its critical section

No two process are executing in Ptheir critical sections at the same time!

The criffcal-section problem is to design a protocol can use to cooperate. bascerrer that the

General structure of code of process do entry section critical section exit section remainder section 3 while (TRUE)

2. progess Solution to critical process not section problem in remainder section can must satisfy force Req. participate in cnitical section

1. Mutual Exclusion only one process should be in critical section

Bounded waiting There exist a bound or limit that other process are allowed enter critical section problem

Peterson's solution

peterson's solution is restricted to two process that alternate execution between their critical section and remainder section, (Pi) and (Pi

Let's call the process

int torn > indicates who ese town is it to enter critical section

Boolean Flag [2] y used to indicate if a process is ready to enter its critical section.

```
Structure of process pi in
                              structure of process pi in
                               peterson's solution
   peterson's solution
   do &
                                       f199[j] = +80e;
    flag [i] = true;
                                        toon=i;
     turn = ii
                                       while (flag[i] ss
     While (flag [i] ss turn==[i]);
                                         +080 == (i]);
       critical section
                                        critical section
    Flag [i] = false;
                                     flog [j = False]
       remainder section
                                     remainder section
     3 while (TRUE)
                                    3 while (TRUE);
  Test and Set Lock
 snared lock variable which can esther take two values
  1800
 Before entering into its contical section, a process inquires
  about lock
  if Pts locked it keeps walting till free
 if not locked, it takes the lock and executes critical
 section
     boolean Testandset (boolean *target) &
        booleon TV = #target
           # target = TRUE
           return tv;
        3
     do (
     while (Test And SEt (& lock));
        1/ do notwing
     11 critical rection
       10CK = FALSE
```

semaphores

3 WHIE (TRUE);

semaphores & simply a variable which is non-negative and shared between threads. Used to solve exitical section problem and to achieve process syncronization

A semaphores is an Poteger variable is accessed toward two standard atomic operations

wait() signal()

wait () -> p "to test"

signal() -> v " to increment"

Defination of signal()

P(Semaphores) & while (s <=0); s--;

if SC=0 it will

tell the process to

wait as rome other

process is in critical

condition.

Defination of signar ()

V (semaphores) f S++;

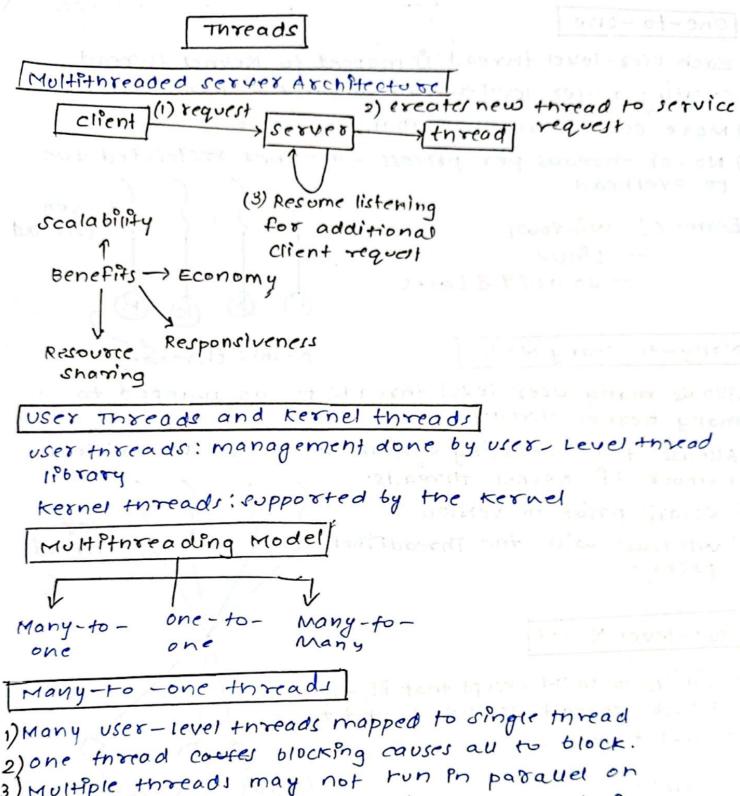
Types of semaphores

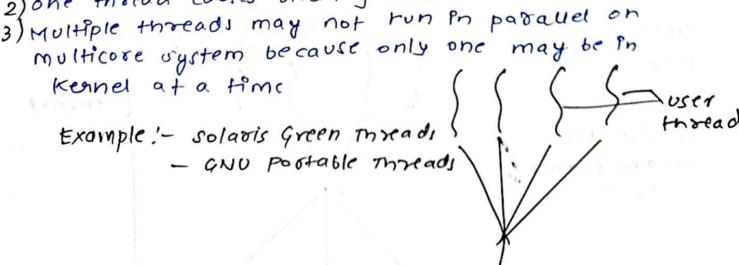
Binary Semaphore

The value of a binary semaphore can range only between 0 and 1. They are known or <u>mutex Locks</u>. Locks that posside mutual exclusion

(Counting semaphore)

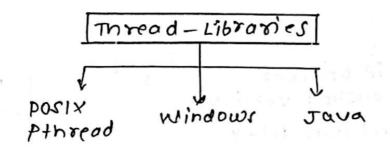
Its value can change over an unswithicted domain . It is used to control access to a resource that has multiple instances





 $k) \leftarrow kernel thread$

one-to-one 1) Each user-level thread is mapped to keene thread 2) creating, a user-level thread creates a kernel thread 3) More concurrency than many -to-one 4) No. of threads per process sometimes restricted due to overhead Example: Windows Linux - sola VIS 9 & Later Many-to-Many Model Kernel thread 1) Allows many user level threads to be mapped to many kernel threads. ?) Allows the operating system to create a sufficient Kernel threads. 3) solaris prior to version 9 4) windows with the Threadfiber package Two-level Model 1) similar to M:M except that it allows a user thread to be bound to Kennel thread Keinel Example thread IRIX HP -UX Tru64 UNX Solaris 8 and carlies ceanel threa



Posix [Portable operating system Interface) Ps a set of standard operating system Paterfaces based on the unix operating system.

The Windows thread library is a kernel-Level isbrary avallable on windows system.

monaged directly in Java programs