

ECE 483: Analog IC Design
Final Report
Low Dropout Regulator

Sayankar, Rutvik
rutviks2@illinois.edu

Patel, Dev
dpatel2@illinois.edu

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Chapter 1

Overall Design Approach

1.1 Design choices

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1.2 System level trade-offs

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Chapter 2

Schematic & Parameters

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Chapter 3

Performance

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3.1 Simulation Results

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3.1.1 Loop-gain AC response

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3.1.2 DC load and line regulation respons

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3.1.3 Power supply rejection (PSR)

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3.1.4 Output noise versus frequency plot

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