MAS WES - 268

Project5 BNN

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I tried to code the BNN but couldn't get a pass on the testbench. I think the TB is very sensitive to how the coding happens and only works with a preset coding style any deviation from that doesn't work.

This is a hypothesis, I still dint get time to debug it further or try alternative approaches.

Also I plan to try and updating the weighs but that again I couldn't achieve as yet.

I tried reversing the binarizing logic, updating ignoring 16 MSB and 16 LSBs but I always got the TB failing absolutely. I think a deeper understanding is required for me to truly be able to debug this. May be once I try to update the weights I will understand depths of this NN.

## Each Layer explained:

First layer: 784->128 neurons

- Takes binary input (packed in 25 32-bit words)
- Performs XNOR operation between inputs and weights
- Uses popcount to count matching bits
- Applies threshold activation function

Second layer: 128->64

- Packs previous layer's outputs into bits
- Performs XNOR with weights
- Uses popcount for bit counting
- Applies same threshold activation

## Third Layer 64->10

- Similar bit packing and XNOR operations as perevious layers.
- No activation function in final layer
- Outputs raw scores for each class

## **Code:**

```
xnor_result = ~(input_bits ^ w2[out_n * 4 + word]); // XNOR
         sum[out_n] += __builtin_popcount(xnor_result);
     sum[out_n] = sum[out_n] * 2 - 128;
layer2_out[out_n] = sum[out_n] > 0 ? 1 : -1;
 for(int i = 0; i < 64; i++) {
    //if(i < 5) printf("Layer2_out[%d] = %d\n", i, layer2_out[i]);
    printf("Layer2_out[%d] = %d\n", i, layer2_out[i]);</pre>
xnor_result = ~(input_bits ^ w3[out_n * 2 + word]); // XNOR
sum[out_n] += __builtin_popcount(xnor_result);
     sum[out_n] = sum[out_n] * 2 - 64;
// No activation function in the output layer
```

```
/erifying the sample: Sample 1
rong output: Expected: -2 Obtained: -6
rong output: Expected: 4 Obtained: 16
Vrong output: Expected: -8 Obtained: -4
Vrong output: Expected: 14 Obtained: -6
rong output: Expected: 0 Obtained: 4
Vrong output: Expected: -42 Obtained: -10
vrong output: Expected: 48 Obtained: 0
/rong output: Expected: -4 Obtained: -8
vrong output: Expected: 2 Obtained: 14
Sample: Sample 1 FAILED
/erifying the sample: Sample 1
Vrong output: Expected: 2 Obtained: -2
Vrong output: Expected: 0 Obtained: 4
vrong output: Expected: 48 Obtained: 4
rong output: Expected: 10 Obtained: -2
rong output: Expected: -20 Obtained: -8
Vrong output: Expected: 4 Obtained: -8
rong output: Expected: 2 Obtained: -14
rong output: Expected: -16 Obtained: -4
vrong output: Expected: 4 Obtained: 0
rong output: Expected: -14 Obtained: 6
Sample: Sample 1 FAILED
/erifying the sample: Sample 1
Vrong output: Expected: -16 Obtained: 4
Vrong output: Expected: 42 Obtained: 6
Vrong output: Expected: 2 Obtained: 6
vrong output: Expected: 4 Obtained: 0
rong output: Expected: -14 Obtained: -2
Vrong output: Expected: 8 Obtained: -12
rong output: Expected: 2 Obtained: -6
vrong output: Expected: 14 Obtained: 6
vrong output: Expected: -8 Obtained: 0
Sample: Sample 1 FAILED
INFO: [SIM 1] CSim done with 0 errors.
```