

P2K-MARGHERA Monday, January 10, 2000 Date.

Document Number

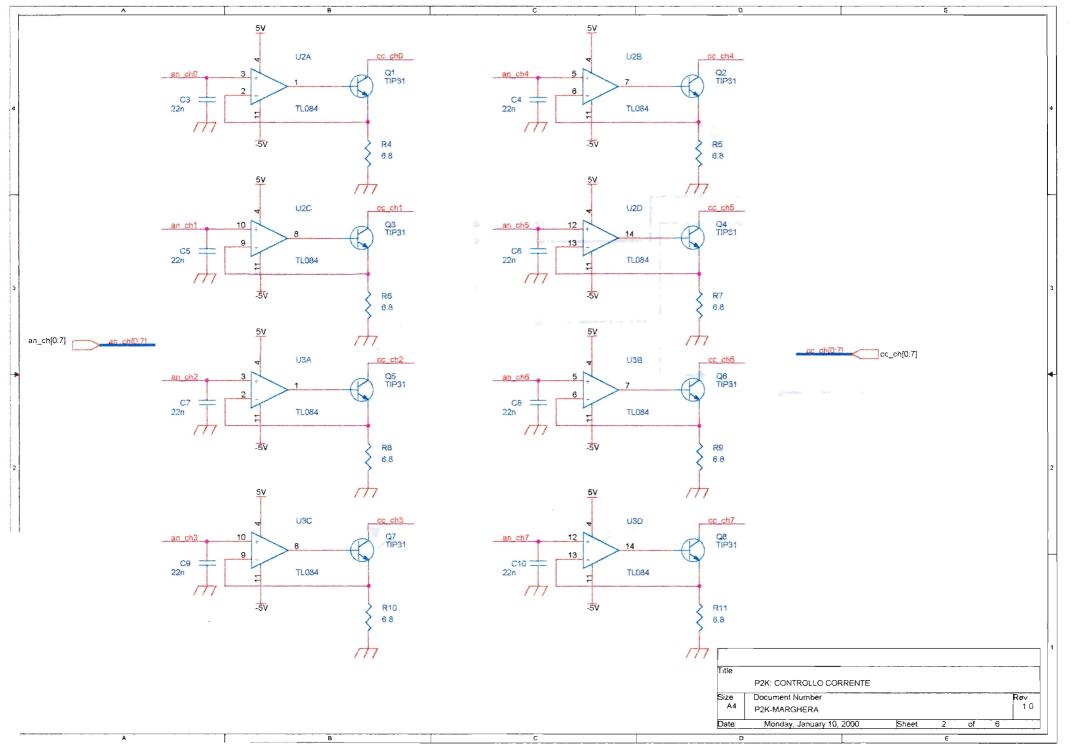
P2K: RADICE

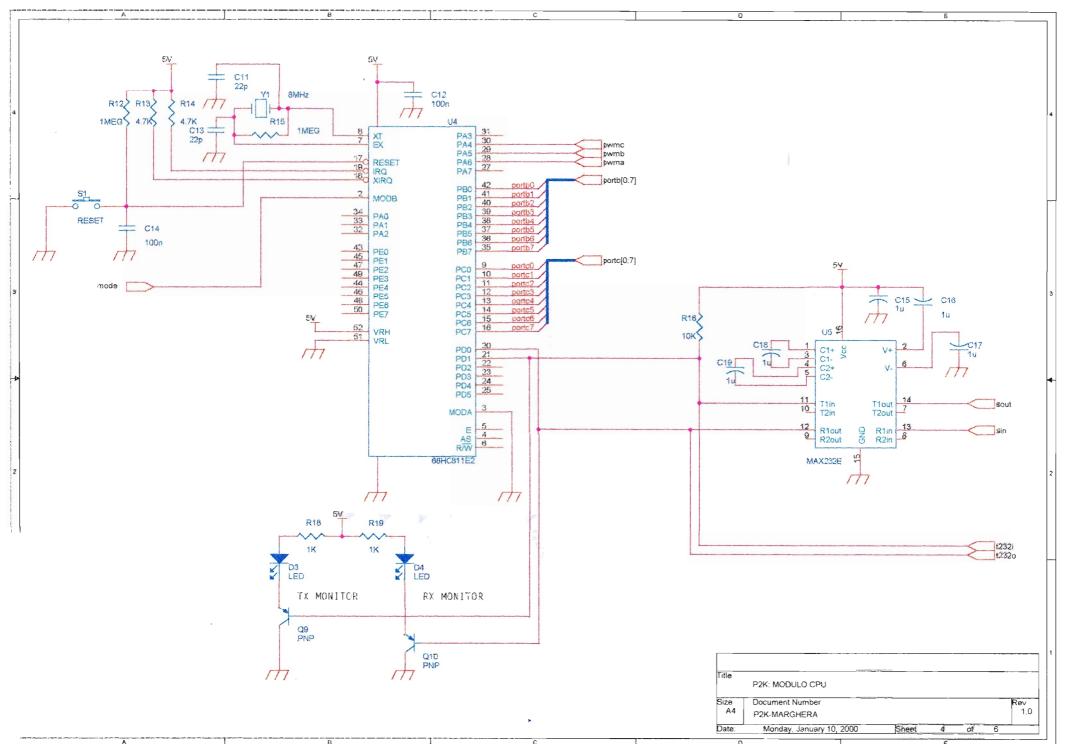
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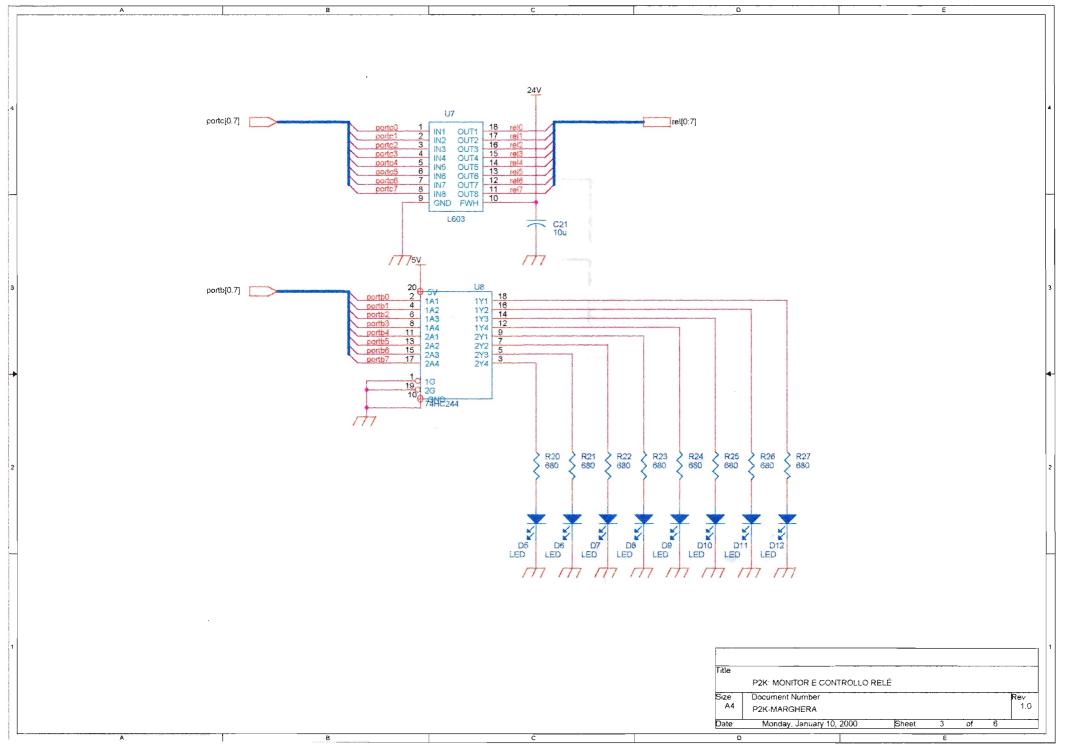
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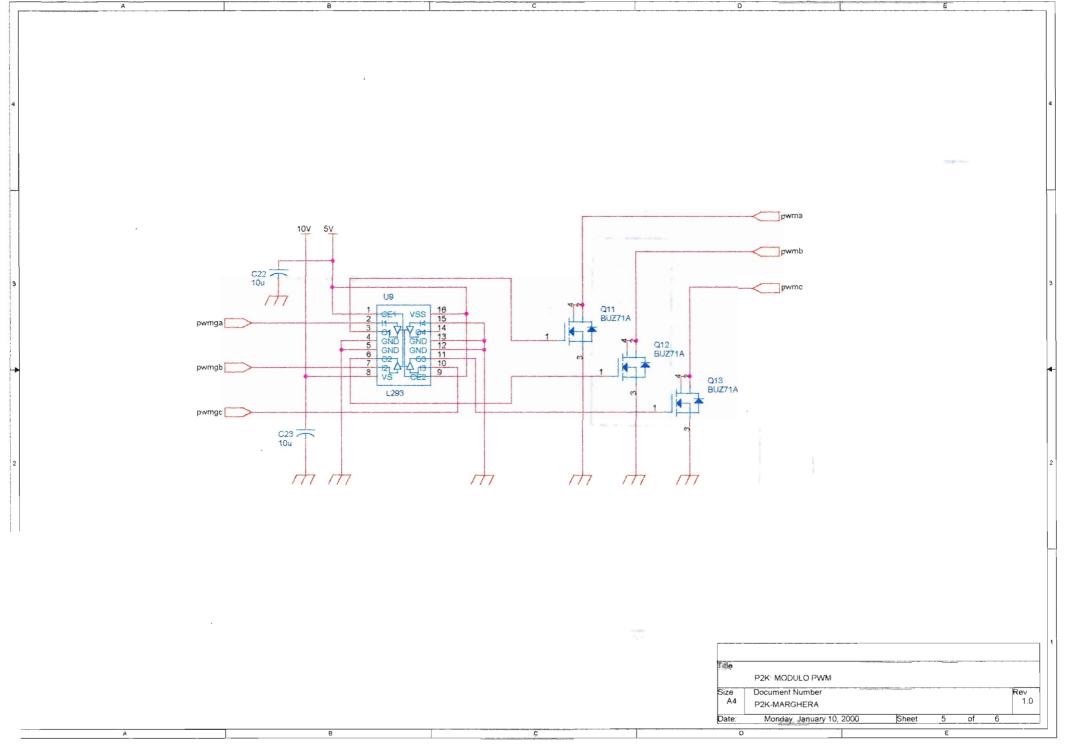
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PORT-B: 0- clock DA 1- dati seriali per il DA 2- linea di caricamento del DA 3- INHIBIT del multiplexer analogico 7,6,5,4 - indirizzo del canale analogico portb[0:7] U10 an\_buff 4067 R28 C24 100K 1u an\_ch[0:7] INJOUT an ch1 an ch2 IN/OUT: an châ cc\_ch[0:7] IN/OUT: portb4 portb5 portb6 portb7 10 11 14 13 an ch4 IN/OUT4 an chi IN/OUT5 an ch6 IN/OUT8 an\_buff IN/OUT N/OUT8 23 22 21 20 19 18 17 16 Ednog IN/OUT1 IN/OUT11 IN/OUT12 IN/OUT13 **OUT/IN** IN/OUT14 IN/OUT15 R29 C25 U11 GND 100K 1u LTC1257 DIN DIN OOA VOUT TUOD portb1 GND R30 100K U12 CAP+ OSC GND CAP-OUT ADM680 C27 10u Title P2K: MODULO ANALOGICO Document Number Size Rev P2K-MARGHERA Date: Monday, January 10, 2000 Sheet of