

Athena Design Rev. A1 Schematic

RVR Lab, Rice University, Houston TX

The first RISC–V development board at Rice University, featuring the SiFive FE310 microprocessor, Athena is a prototyping and experimental platform for education and research at Rice. It is intended to be used in undergraduate lab courses and in VIP projects, therefore the design focus is on debug capability and I/O access.

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Sheet	Description
1	Cover Page
2	Power: USB, Charging, Voltage Regulators
3	Debug Control: MK22 Processor
4	Main Processor: SiFive FE310, the RISC–V MCU
5	Peripherals: Oscillators, Flash Memory
6	I/O Headers: mikroBus–Compatible, GPIO, and Power; Mechanical

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Designed in EAGLE 9.6.2.