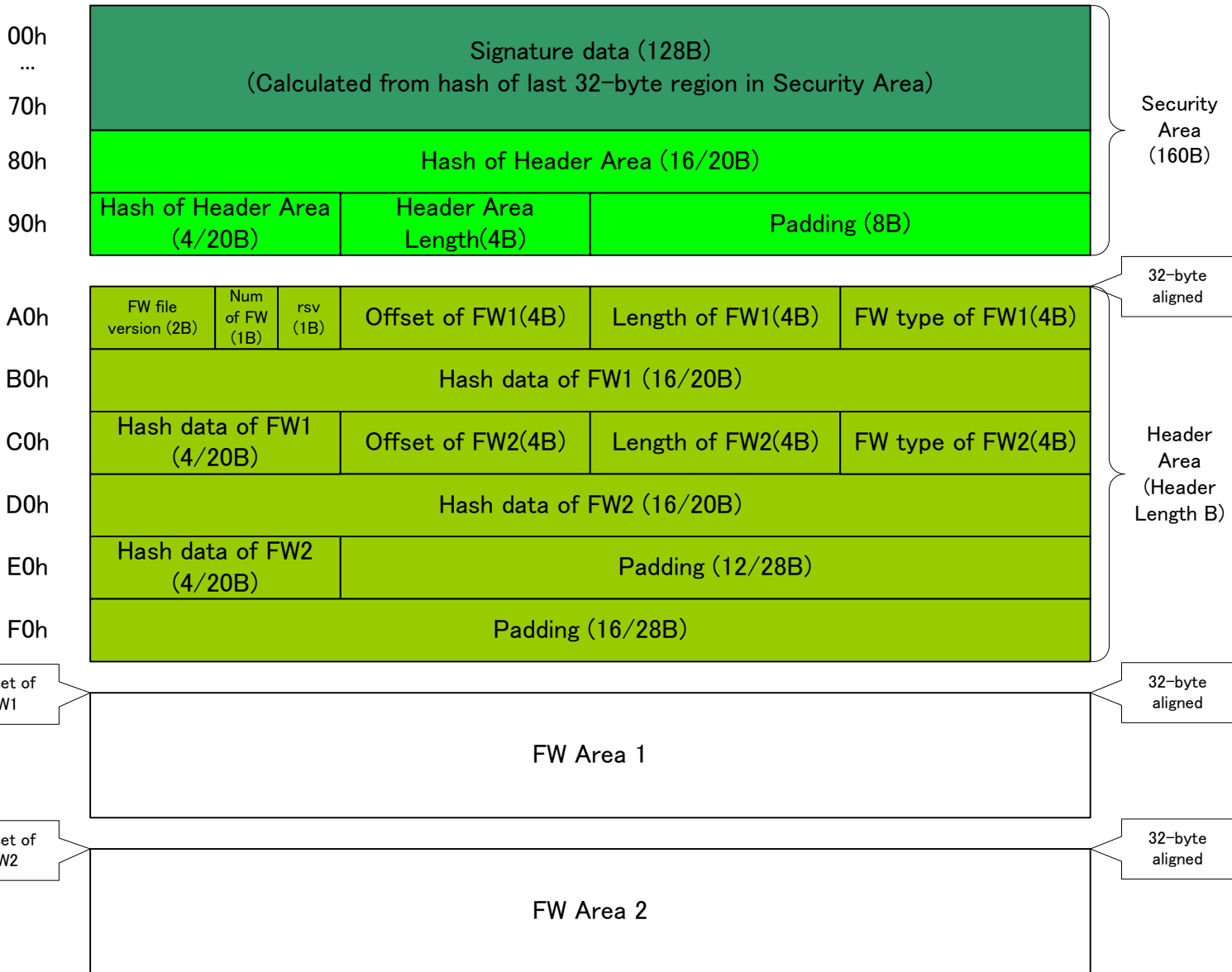


# Wireless Firmware File Format

## (Overall view)

0 1 2 3 4 5 6 7 8 9 A B C D E F



### FW type

2in1 module 1.1 : 00000000 h  
 2in1 module 2.0 : 00000001 h  
 One Chip(M&M) module : 00000002 h

### FW file version

Major version(1B)	Minor version(1B)
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# Wireless Firmware File Format

## (Discrete firmware image view)

0 1 2 3 4 5 6 7 8 9 A B C D E F

00h	Num of Section = 5	Num of Support ChipID = 2	Offset to HardwareID descriptor	Offset of Section1(4B)	Length of Section1(4B)	Attribute of Section1(4B)
10h	Target address of Section1(4B)			Offset of Section2(4B)	Length of Section2(4B)	Attribute of Section2(4B)
20h	Target address of Section2(4B)			Offset of Section3(4B)	Length of Section3(4B)	Attribute of Section3(4B)
30h	Target address of Section3(4B)			Offset of Section4(4B)	Length of Section4(4B)	Attribute of Section4(4B)
40h	Target address of Section4(4B)			Offset of Section5(4B)	Length of Section5(4B)	Attribute of Section5(4B)
50h	Target address of Section5(4B)			Supported Hardware ID 1 (8B)		Supported Hardware ID 2 (4/8B)
60h	Supported Hardware ID 2 (4/8B)			FW version (4B)	Host Interest Start Address (4B)	Target RAM Start Address (4B)
70h	Target RAM Length (4B)			Atheros reserved (12/16B)		
80h	Atheros reserved (4/16B)			Padding (12B)		

Padding for 32-byte alignment

Offset of Section1

32-byte aligned

Firmware image (Section1/Attr=1)

Offset of Section2

32-byte aligned

Datapatch image (Section2/Attr=2)

Offset of Section3

32-byte aligned

Firmware data segment image (Section3/Attr=3)  
(not included in 2in1 chip 1.0)

Offset of Section4

32-byte aligned

EEPROM text image (Section4/Attr=4)

Offset of Section5

32-byte aligned

EEPROM data image (Section5/Attr=5)

### Attribute list

NWM_FW_ATTR_WLAN	1
NWM_FW_ATTR_DATAPATCH	2
NWM_FW_ATTR_DATASEGMENT	3
NWM_FW_ATTR_EEPROMTEXT	4
NWM_FW_ATTR_EEPROMDATA	5
NWM_FW_ATTR_ROMPATCH	6
NWM_FW_ATTR_BOARDDATA	7 /* not used */
NWM_FW_ATTR_TCMD	8 /* not used */
NWM_FW_ATTR_COMP_FLAG	0x80000000

### Hardware ID

ChipID (4B)

ROM ID (4B)