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Gate Arrays

Dynamic RAM

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The diagram illustrates the power plane for the J4 connector. It shows 20 pins connected to various power and ground planes. Pins 1-10 are connected to +12V, pins 11-12 to +5V, and pins 13-20 to GND. The diagram includes labels for components like C198, C199, C200, C201, C202, C203, C204, C205, C206, C207, C208, C209, C210, C211, C212, C213, C219, and C214, along with their values and the signal names they connect to.

Pin	Component	Value	Signal
1	C198	10n	CASIN
2	C199	10n	
3	C200	40n	
4			
5	C201	40n	
6	C202	10n	H/2
7	C203	100p	RED
8	C204	100p	GREEN
9	C205	100p	BLUE
10	C206	1n	BLK
11	C207	1n	HSYNC
12	C208	1n	VSYNC
13	C209	10n	RESET
14	C210	10n	TTL5
15	C211	1n	CASOUT
16	C212	100p	BG
17	C213	10p	SGCS
18			
19	C219	40n	
20	C214	100p	

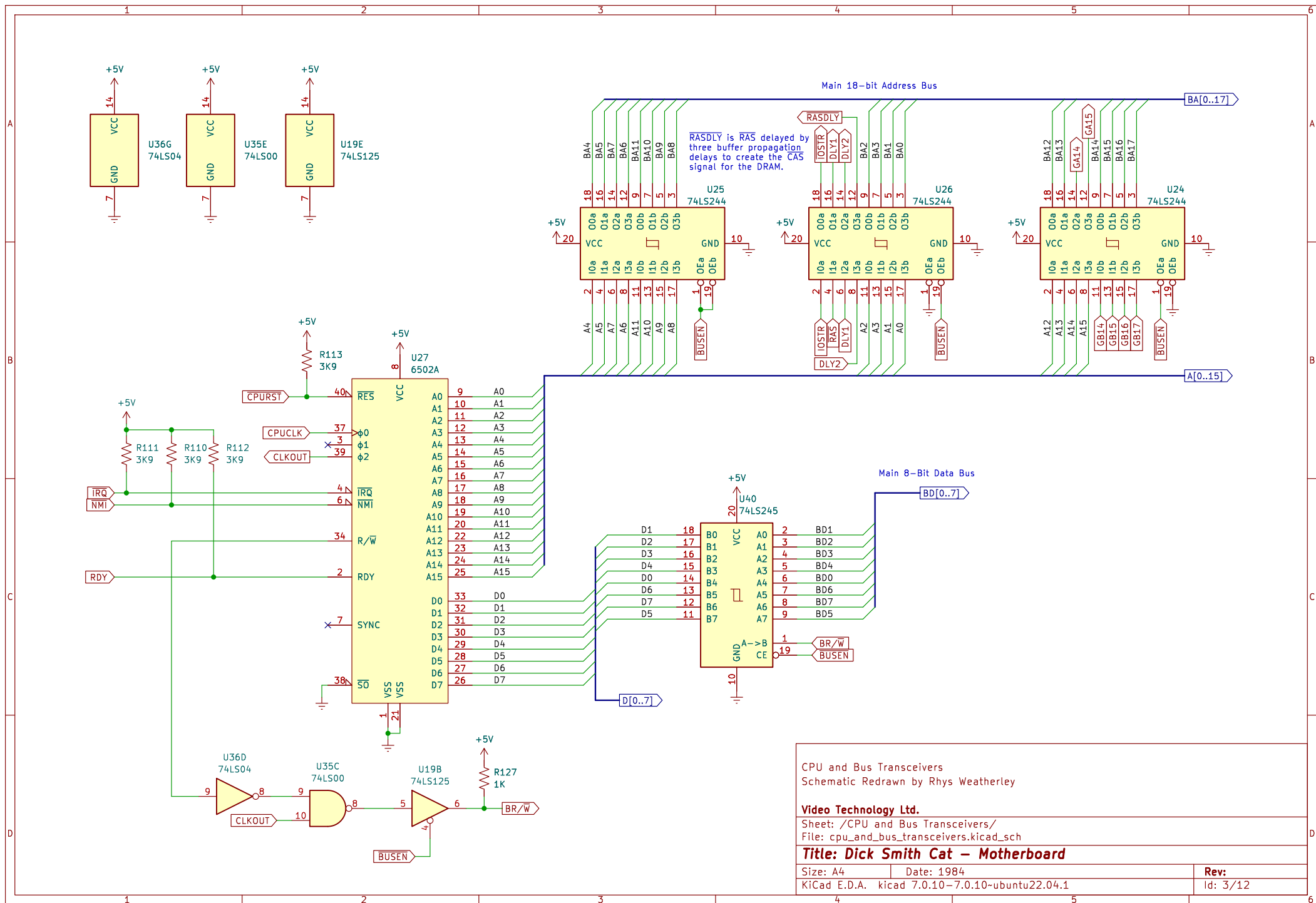
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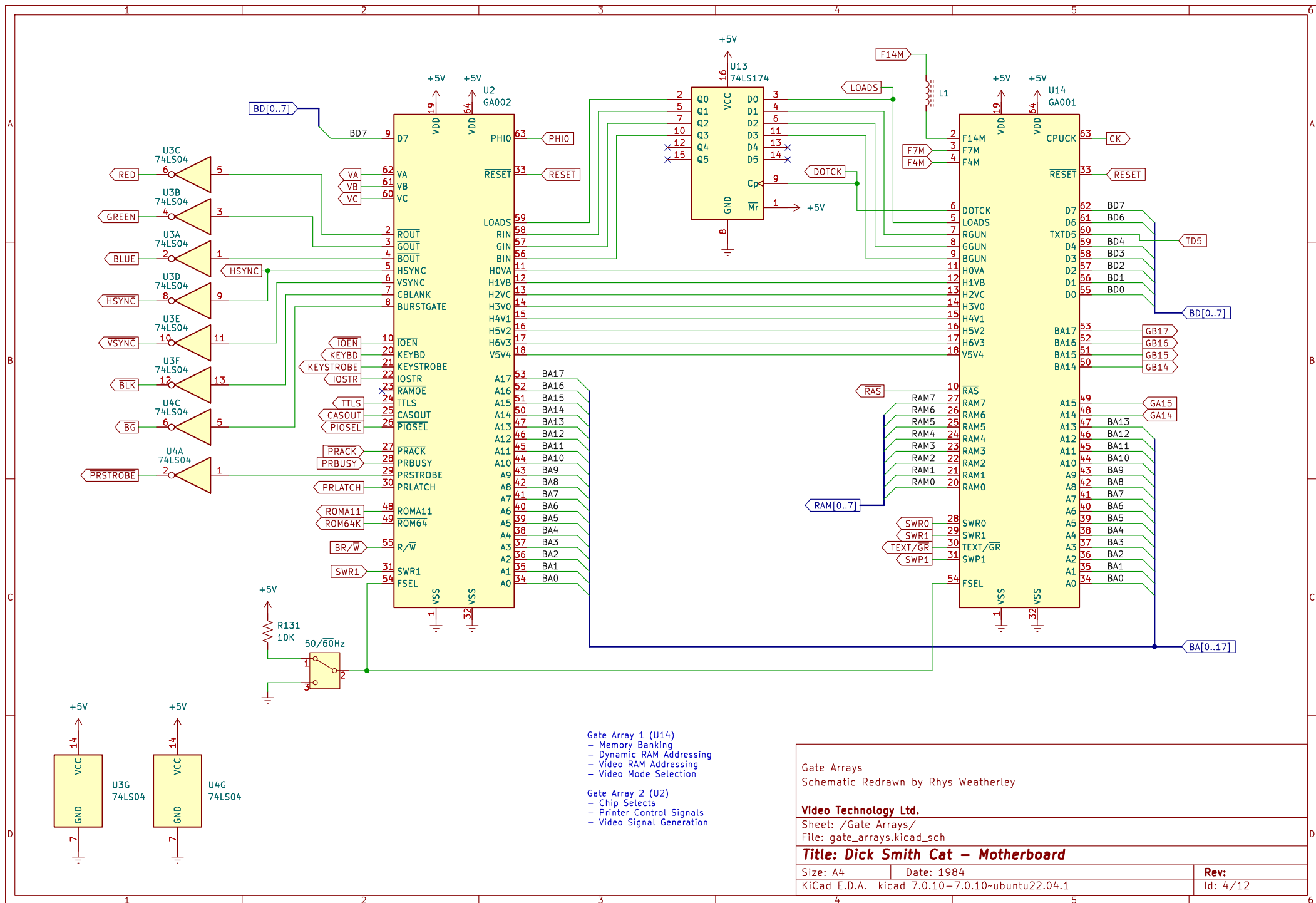
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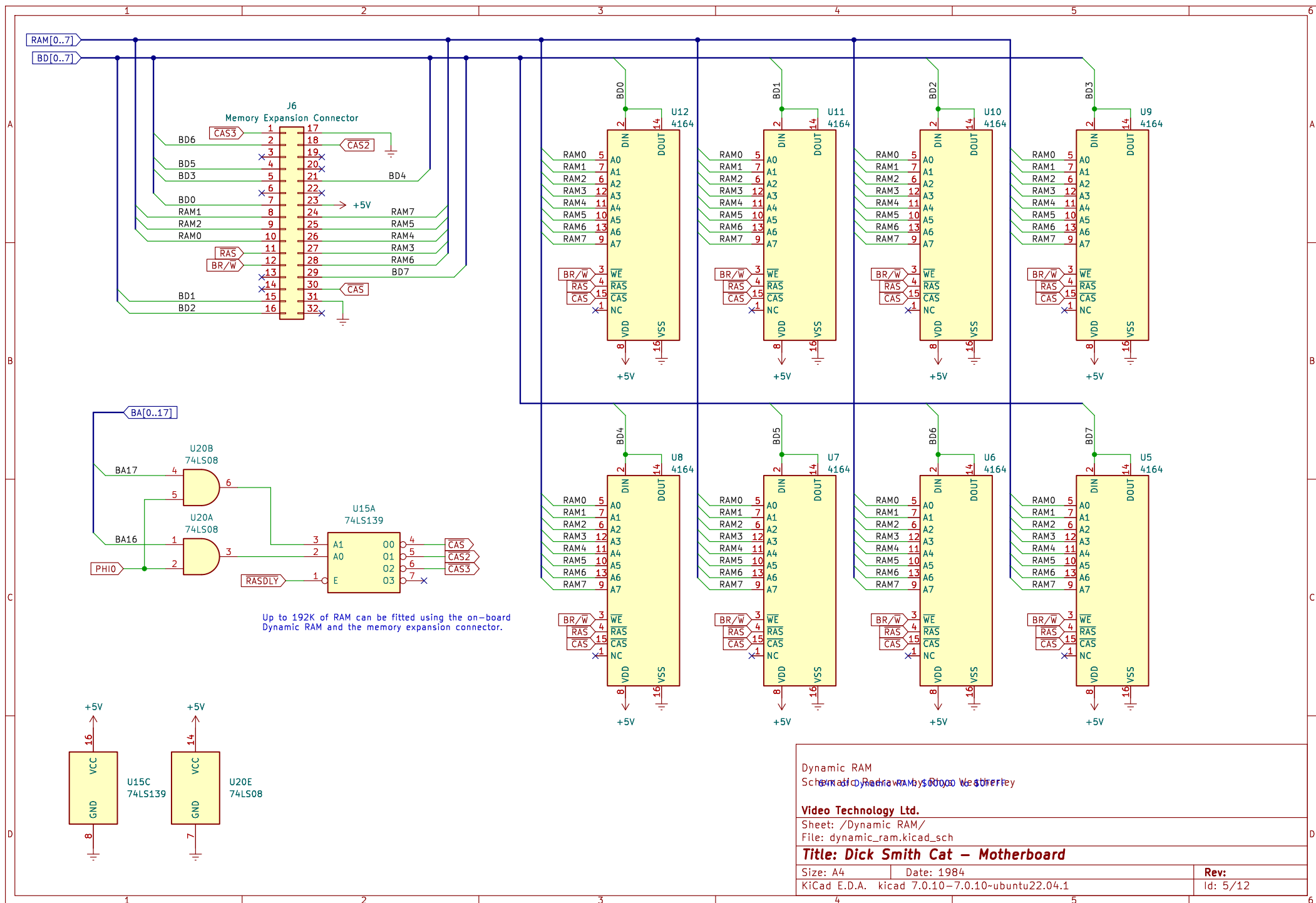


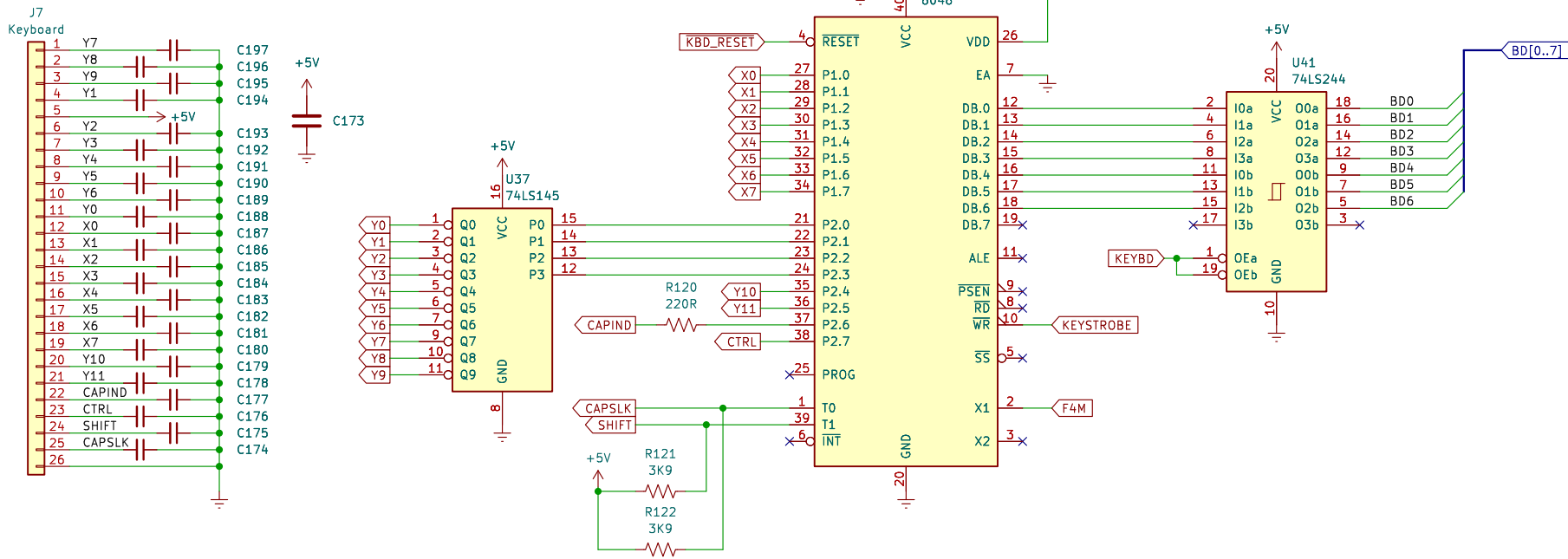
CPU and Bus Transceivers
Schematic Redrawn by Rhys Weatherley

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Size: A4	Date: 1984
KiCad E.D.A. kicad 7.0.10–7.0.10-ubuntu22.04.1	
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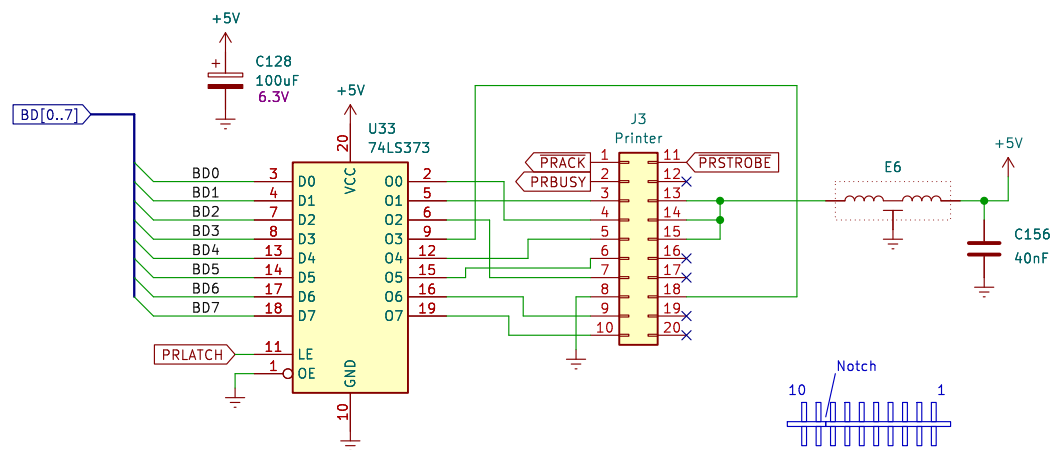




Keyboard Interface
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Title: Dick Smith Cat – Motherboard

Size: A4	Date: 1984	Rev:
KiCad E.D.A. kicad 7.0.10–7.0.10-ubuntu22.04.1		Id: 8/12



Looking at the printer edge connector on the back of the case, looking towards the front.

Printer Interface
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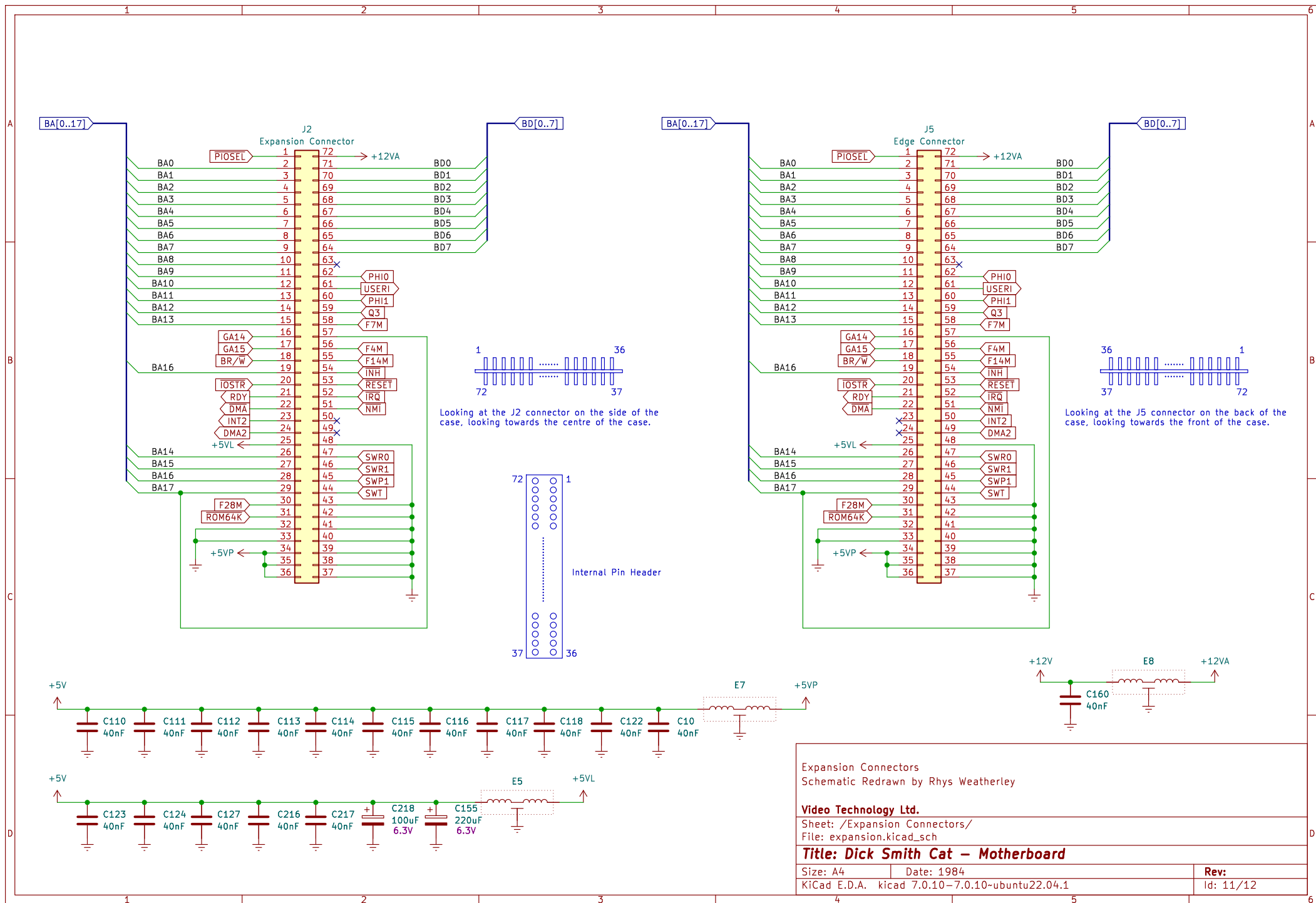
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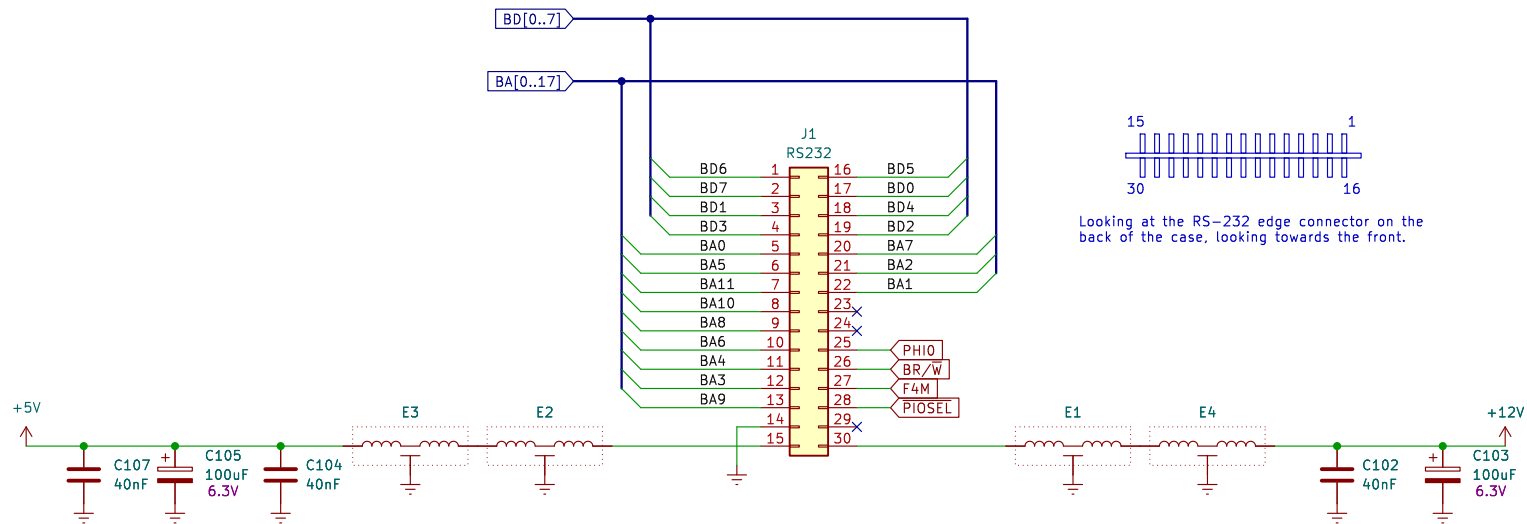
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RS232 Interface
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Title: Dick Smith Cat – Motherboard

Size: A4 Date: 1984
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