

Program RAM

Schematic Redrawn by Rhys Weatherley

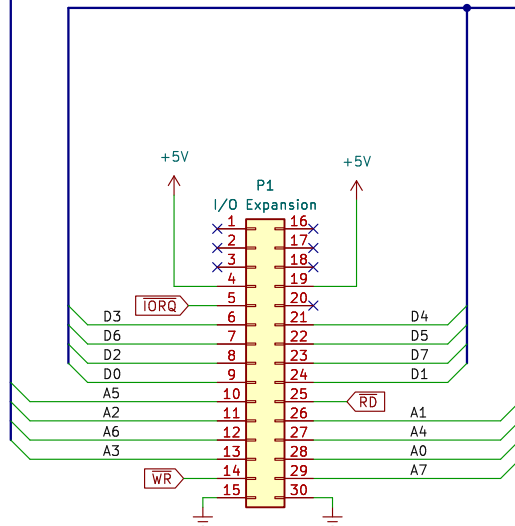
Sheet: /Program RAM/
File: program_ram.kicad_sch

Title:

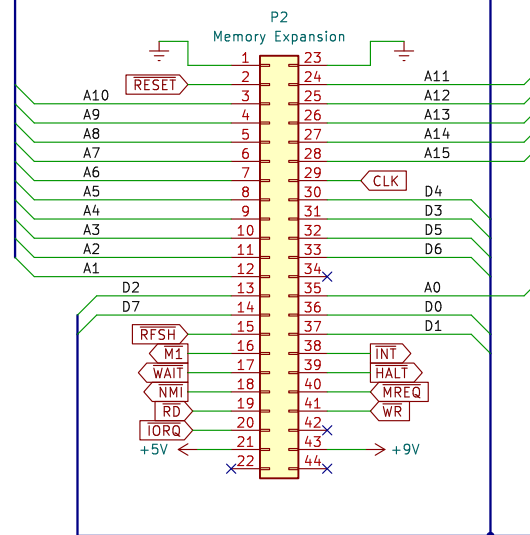
Size: A4 Date:
KiCad E.D.A. kicad 7.0.9-ubuntu22.04.1

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A[0..15]



30-pin edge connector for the I/O bus



44-pin edge connector for the memory bus

D[0..7]

Expansion Ports

Schematic Redrawn by Rhys Weatherley

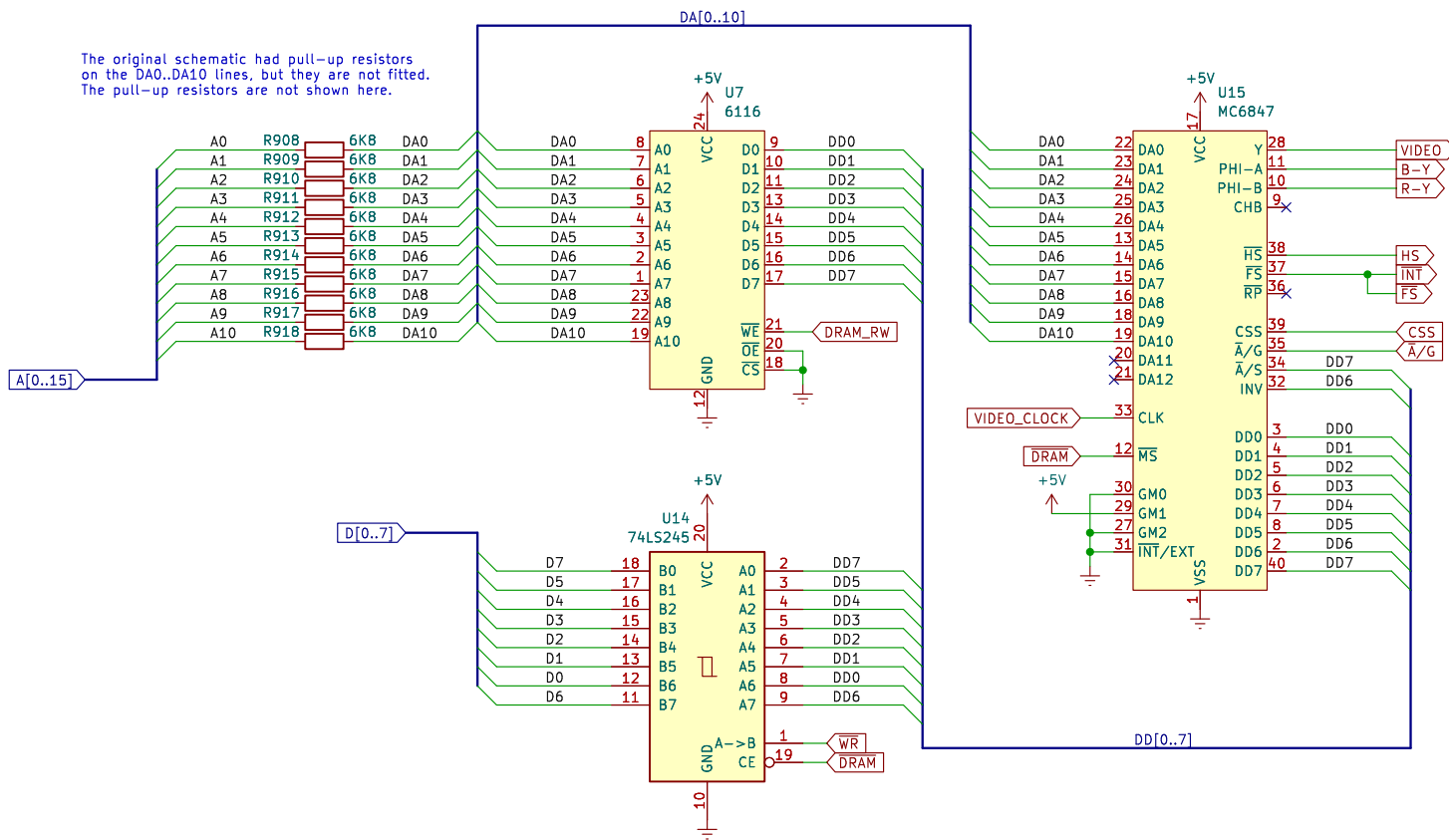
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File: expansion_ports.kicad_sch

Title:

Size: A4 Date:
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The original schematic had pull-up resistors on the DA0..DA10 lines, but they are not fitted. The pull-up resistors are not shown here.



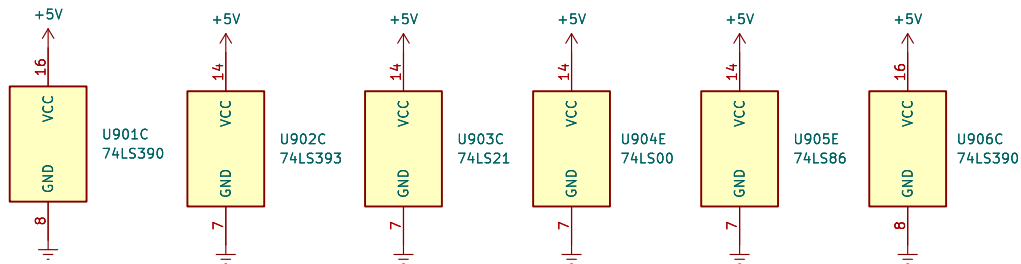
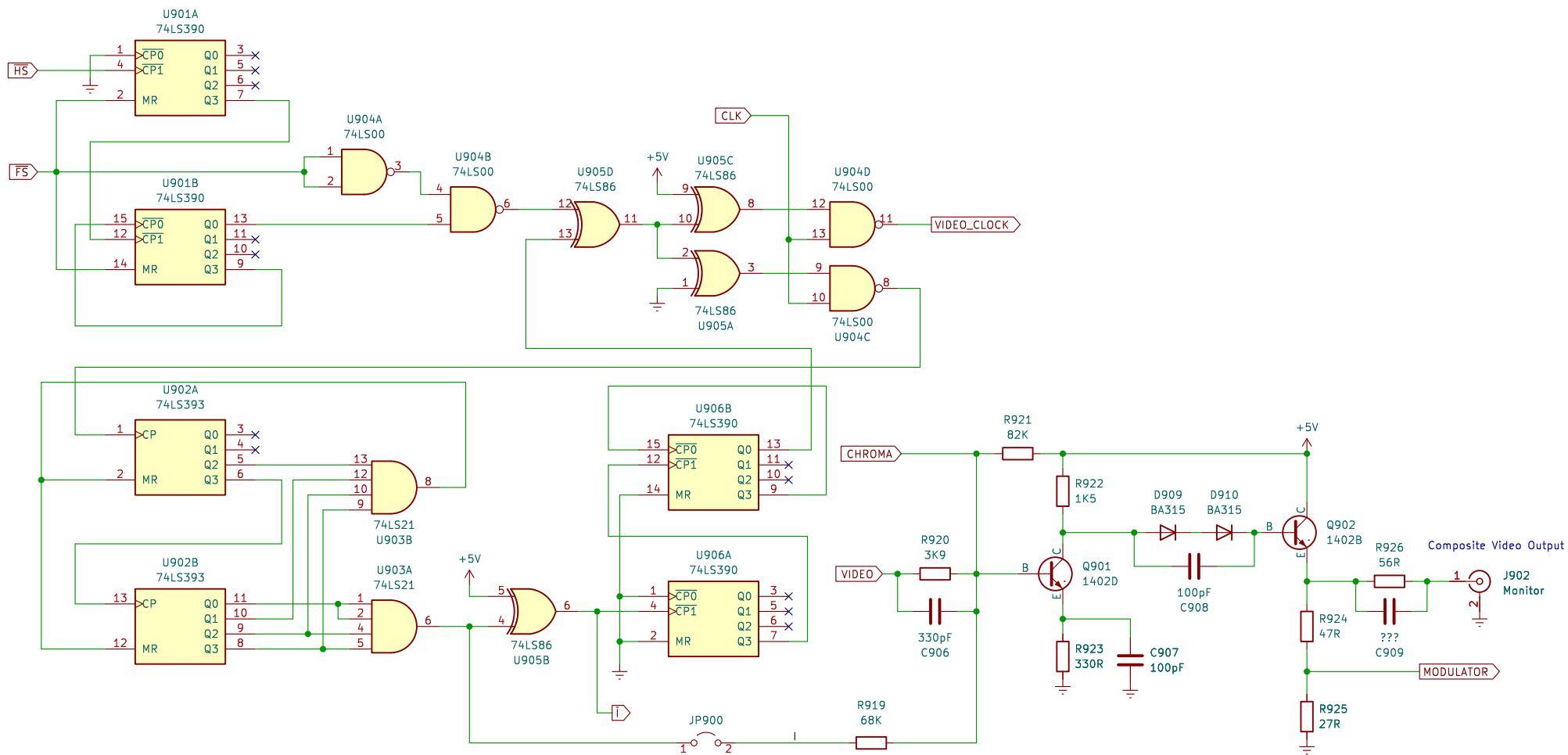
Video Display Generator and 2K of Display RAM
Schematic Redrawn by Rhys Weatherley

Sheet: /Video Controller/
File: video_controller.kicad_sch

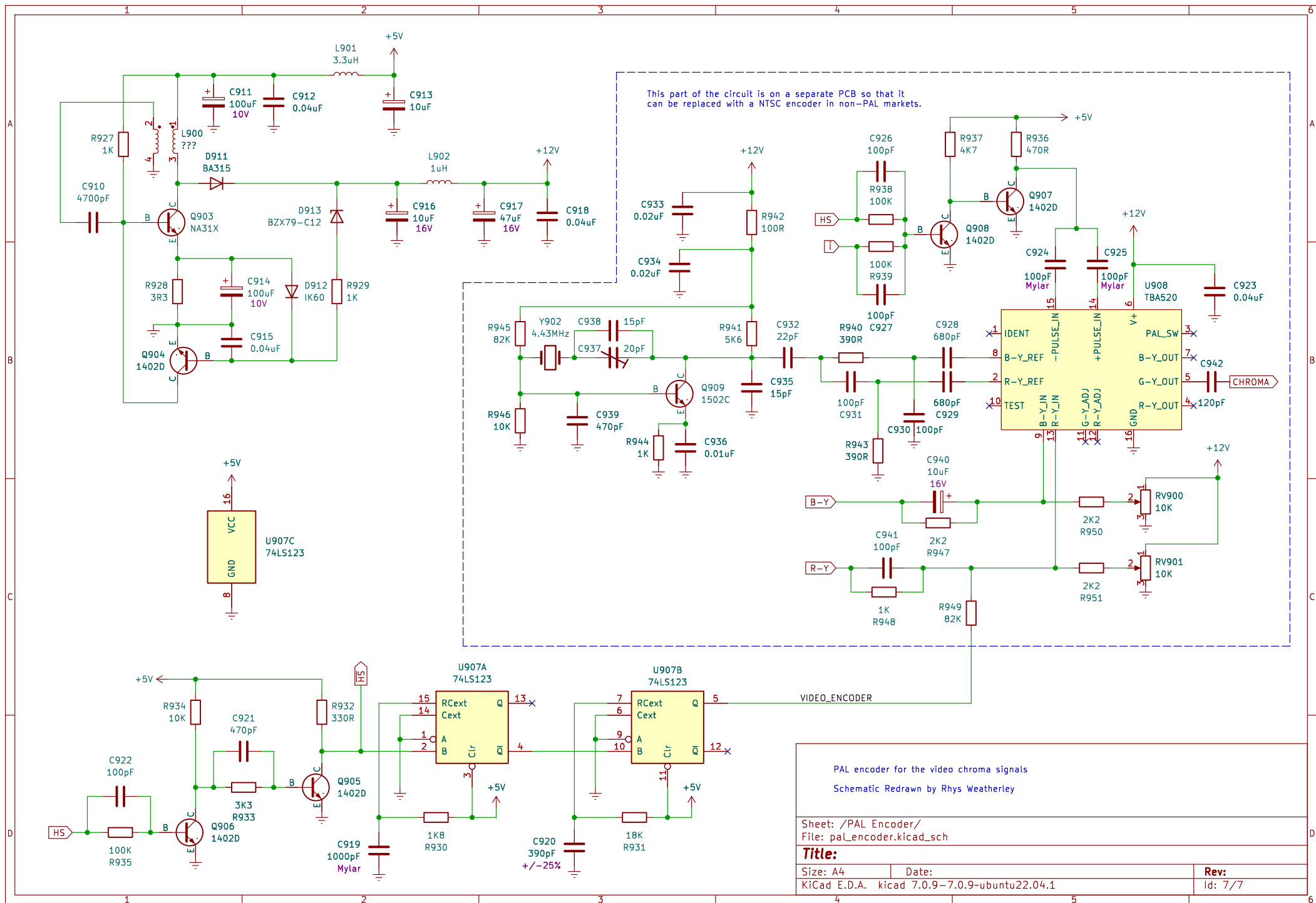
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Size: A4 Date: KiCad E.D.A. kicad 7.0.9-ubuntu22.04.1

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Video Clock Generator and Composite Video Output		
Schematic Redrawn by Rhys Weatherley		
Sheet: /Video Clock Generator/		
File: video_clock.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad 7.0.9-ubuntu22.04.1		Id: 6/7



PAL encoder for the video chroma signals		
Schematic Redrawn by Rhys Weatherley		
Sheet: /PAL Encoder/		
File: pal_encoder.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad 7.0.9-ubuntu22.04.1		Id: 7/7