

Program RAM
Schematic Redrawn by Rhys Weatherley

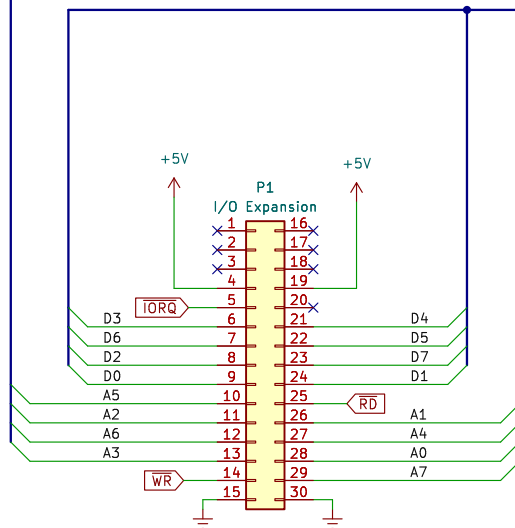
Sheet: /Program RAM/
File: program_ram.kicad_sch

Title: VZ200 Computer

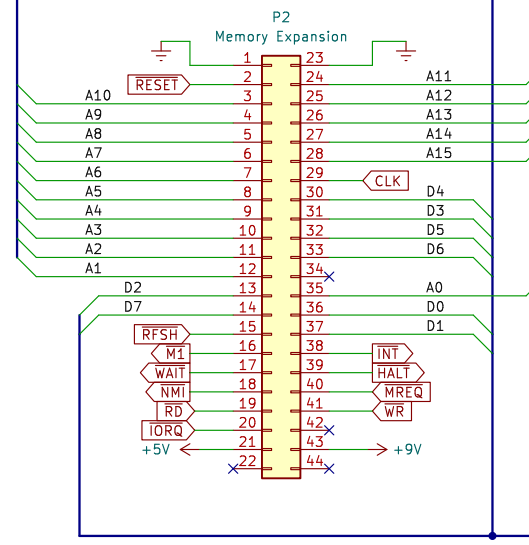
Size: A4 Date: 2024-01-05
KiCad E.D.A. kicad 7.0.10-7.0.10-ubuntu22.04.1

Rev: 2
Id: 3/7

A[0..15]



30-pin edge connector for the I/O bus



44-pin edge connector for the memory bus

D[0..7]

Expansion Ports

Schematic Redrawn by Rhys Weatherley

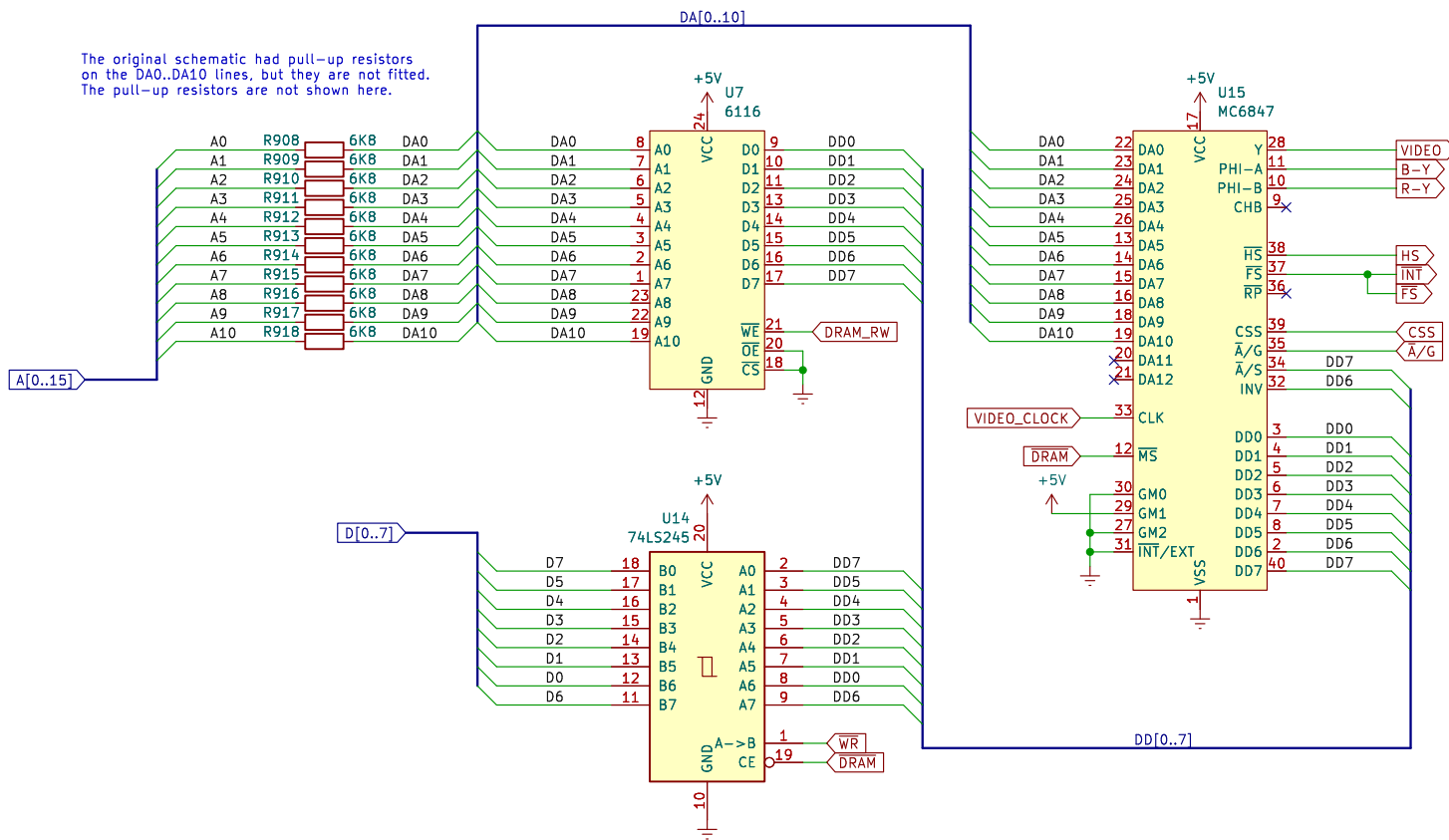
Sheet: /Expansion Ports/
File: expansion_ports.kicad_sch

Title: VZ200 Computer

Size: A4 Date: 2024-01-05
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Id: 4/7

The original schematic had pull-up resistors on the DA0..DA10 lines, but they are not fitted. The pull-up resistors are not shown here.



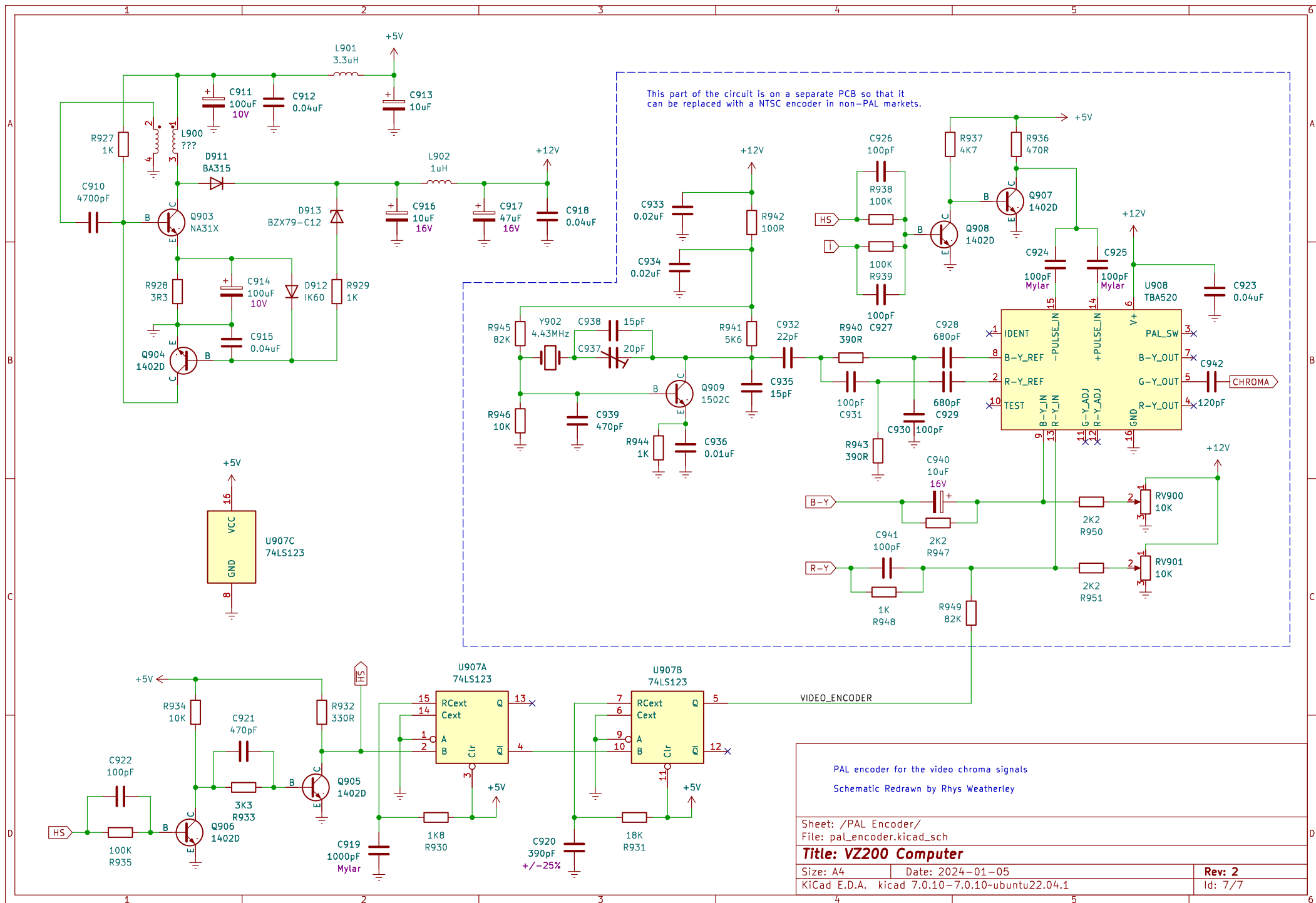
Video Display Generator and 2K of Display RAM
Schematic Redrawn by Rhys Weatherley

Sheet: /Video Controller/
File: video_controller.kicad_sch

Title: VZ200 Computer

Size: A4 Date: 2024-01-05
KiCad E.D.A. kicad 7.0.10-7.0.10-ubuntu22.04.1

Rev: 2
Id: 5/7



PAL encoder for the video chroma signals
Schematic Redrawn by Rhys Weatherley

Sheet: /PAL Encoder/
File: pal_encoder.kicad_sch

Title: VZ200 Computer

Size: A4 Date: 2024-01-05
KiCad E.D.A. kicad 7.0.10-7.0.10-ubuntu22.04.1

Rev: 2
Id: 7/7