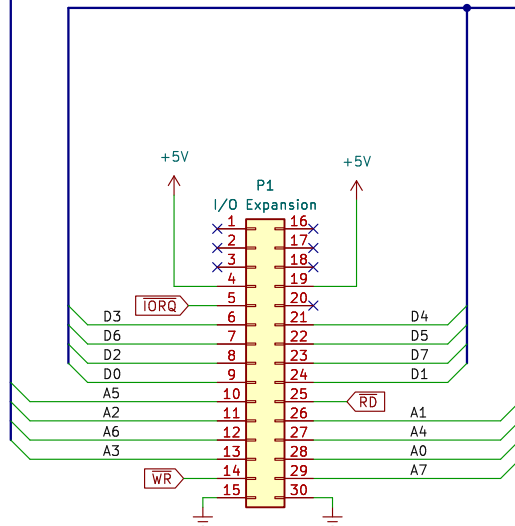


Schematic Redrawn by Rhys Weatherley

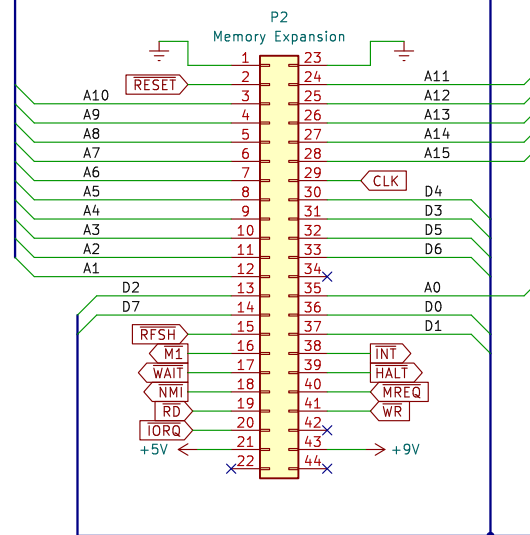
Title:	
Size: A4	Date:
KiCad E.D.A. kicad 7.0.9-7.0.9-ubuntu22.04.1	

Rev: 3/7

A[0..15]



30-pin edge connector for the I/O bus



44-pin edge connector for the memory bus

D[0..7]

Expansion Ports

Schematic Redrawn by Rhys Weatherley

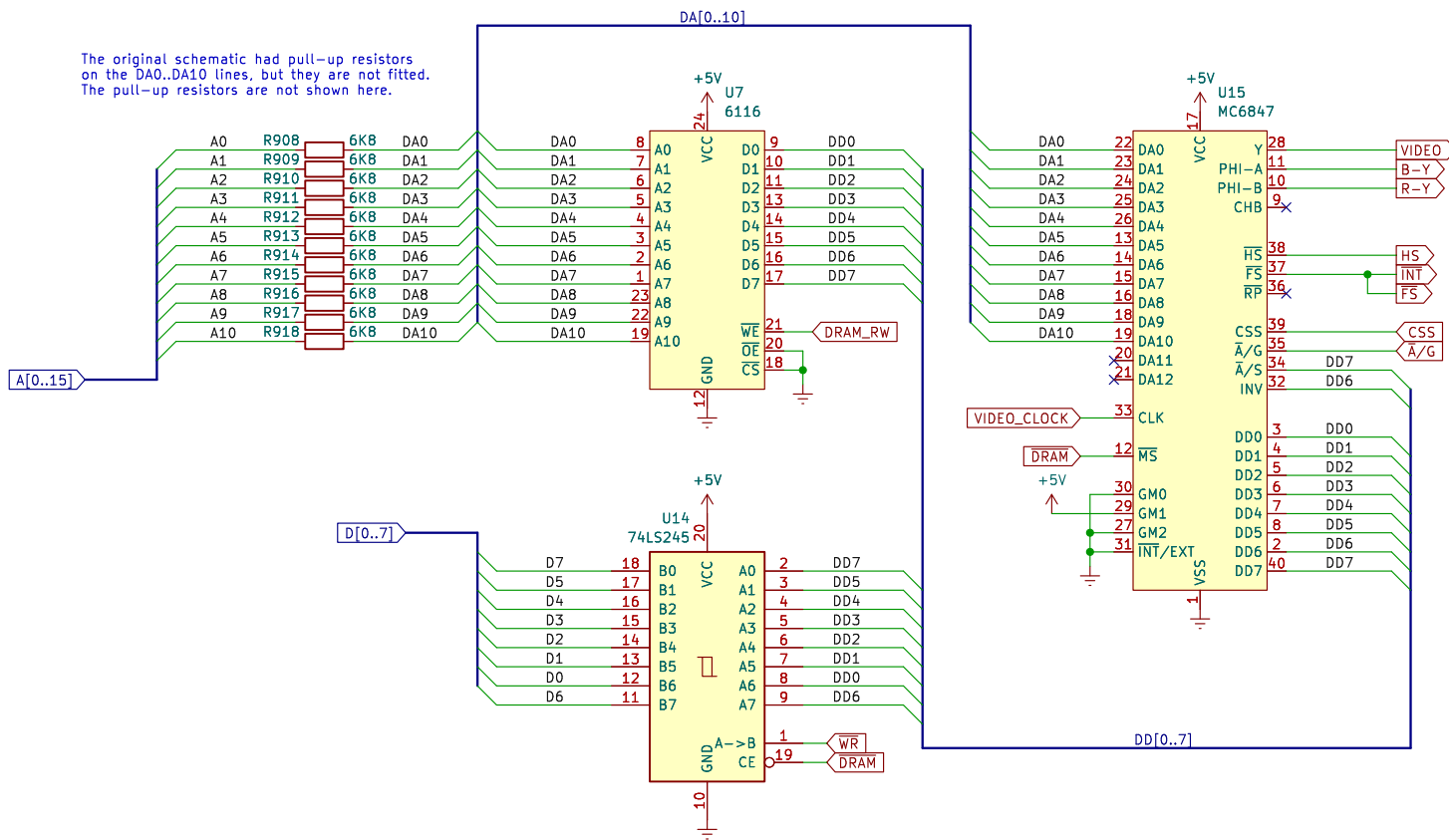
Sheet: /Expansion Ports/
File: expansion_ports.kicad_sch

Title:

Size: A4 Date:
KiCad E.D.A. kicad 7.0.9-7.0.9-ubuntu22.04.1

Rev:
Id: 4/7

The original schematic had pull-up resistors on the DA0..DA10 lines, but they are not fitted. The pull-up resistors are not shown here.



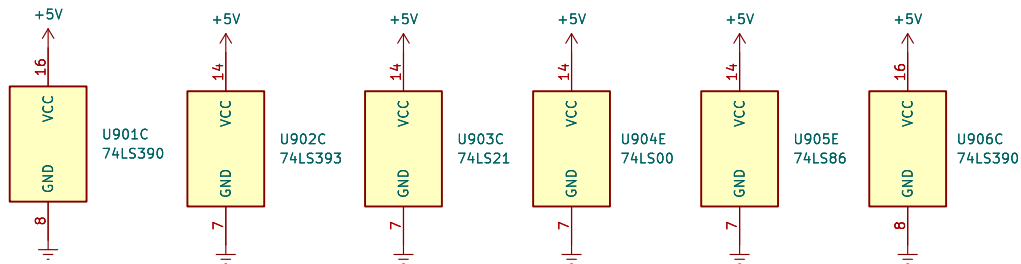
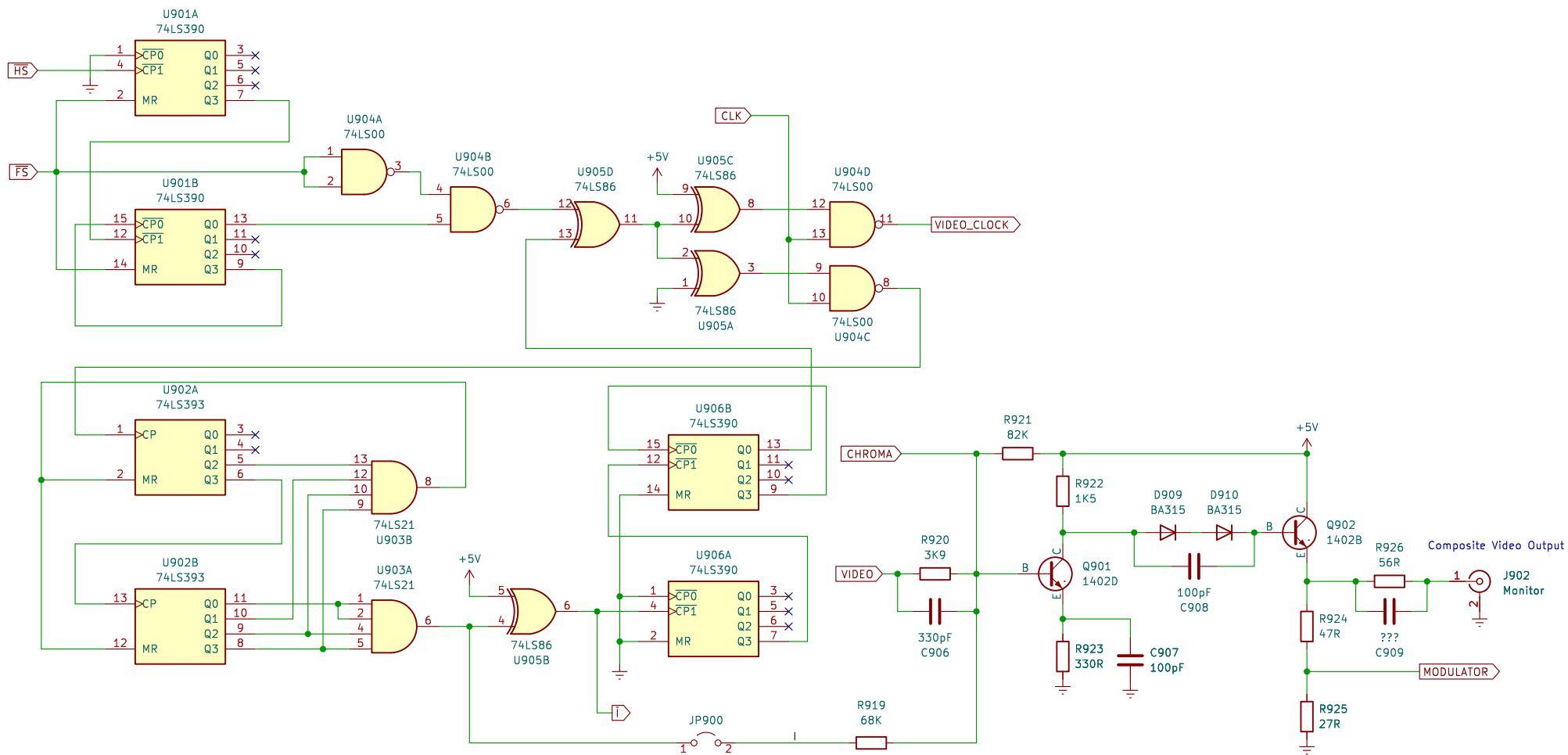
Video Display Generator and 2K of Display RAM
Schematic Redrawn by Rhys Weatherley

Sheet: /Video Controller/
File: video_controller.kicad_sch

Title:

Size: A4 Date: KiCad E.D.A. kicad 7.0.9-ubuntu22.04.1

Rev:
Id: 5/7



Video Clock Generator and Composite Video Output		
Schematic Redrawn by Rhys Weatherley		
Sheet: /Video Clock Generator/		
File: video_clock.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad 7.0.9-ubuntu22.04.1		Id: 6/7

