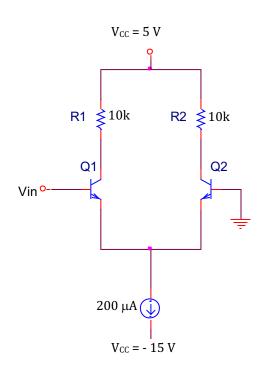
Lab#4 - The Differential Amplifier

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The goal of this exercise is to analyze the differential amplifier circuit.



R, = 14.3 = 1430 - 12

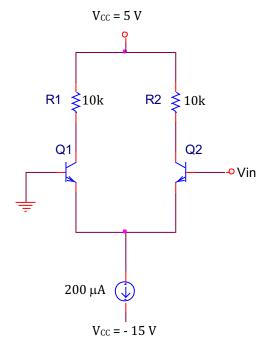


Figure 1(a) I_{OMA} I_{OMA} $I_{ORE} = V_{T} \ln \left(\frac{T_{REF}}{I_{O}} \right)$ $R_{E} = V_{T} \ln \left(\frac{T_{REF}}{I_{O}} \right)$ $R_{E} = 25 \times 10^{-3} \ln \left(\frac{10 \, \text{mA}}{200 \, \text{mA}} \right)$ $R_{E} = 489 \, \Omega$ widlar curent

 $0 - (lomA \cdot R_1) - 0.7 - (-15) = 0$ $lomA \cdot R_1 = 14.3$

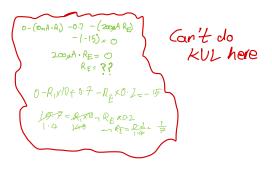


Figure 1(b)

Part-1: Basic Operation of a Differential Pair

a) Implement the circuit shown in figure 1(a). Implement the current source as shown in figure 4. Apply a dc voltage at the base of Q1. Measure currents and voltages as required in table-1. R1 and R2 must be measured by a meter (**NOT** the colour code value).

Table-1

		Measuren	nent for Q1		Measurement for Q2				
V_{in}	R_1	V_{R1}	$I_{C1} =$	Status	R_2	V_{R2}	$I_{C2} =$	Status	$V_{\rm E}$
(V)	(Ω)	(V)	V_{R1}/R_1	On/Off	(Ω)	(V)	V_{R1}/R_1	On/Off	(V)
	(Actual)		(mA)		(Actual)		(mA)		
0 (B1									
grounded)									
0.1									
2.0									
2.0									
- 2.0									
- 2.0									

b) Implement the circuit shown in figure 1(b). Apply a dc voltage at the base of Q2. Measure currents and voltages as required in table-2. R1 and R2 must be measured by a meter (**NOT** the colour code value).

Table-2

	Measurement for Q1				Measurement for Q2				
V_{in}	R_1	V_{R1}	$I_{C1} =$	Status	R_2	V_{R2}	$I_{C2} =$	Status	V_{E}
(V)	(Ω)	(V)	V_{R1}/R_1	On/Off	(Ω)	(V)	V_{R1}/R_1	On/Off	(V)
	(Actual)		(mA)		(Actual)		(mA)		
0 (B2									
grounded)									
0.1									
2.0									
-2.0									
-2.0									

Part-2: Common Mode Voltage

a) Implement the circuit shown in figure 2. Apply a common-mode (dc) voltage as shown in the circuit. Measure currents and voltages as required in table-3 and table-4. R1 and R2 must be measured by a meter (**NOT** the colour code value). Record the correct polarity of V_{CB} . Determine the biasing of the CB junction.

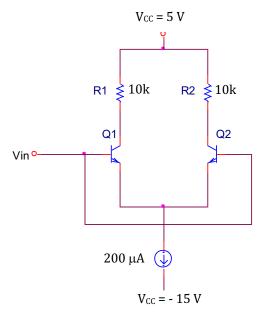


Figure 2

Table-3

V_{iCM}	V_{C1}	V_{C2}	$V_0 = V_{C1} - V_{C2}$	$I_{C1}=V_{R1}/R_1$	$I_{C2}=V_{R2}/R_2$	V_{CB1}	V_{CB2}
			V_{C1} - V_{C2}	V_{R1}/R_1	V_{R2}/R_2		
(V)	(V)	(V)	(V)	(mA)	(mA)	(V)	(V)
0							
1							
3							
4							
5							
6							

Table-4

V_{iCM}	V_{C1}	V_{C2}	V ₀ =	I _{C1} =	$I_{C2}=V_{R2}/R_2$	V_{CB1}	V_{CB2}
			V_{C1} - V_{C2}	V_{R1}/R_1	V_{R2}/R_2		
(V)	(V)	(V)	(V)	(mA)	(mA)	(V)	(V)
-2							
-4							
-6							
-8							
-12							
-14							
-15							

Determine the theoretical limits of the common-mode input voltage of this circuit.								

Part-3: Differential Input Resistance

- a) Implement the circuit shown in figure 3. Apply a differential input voltage of $150\,$ mV (peak), as shown in the circuit. Measure the differential input resistance, and compare it with the calculated value of R_{id} .
- b) Calculated Rid:

Rid =
$$2(\beta + 1) (r_e + Re)$$

Here, $r_e = (V_T)/(I_E)$
Re = 100Ω
Assume $\beta = 165$ (for 3904)

Calculation (using formula):

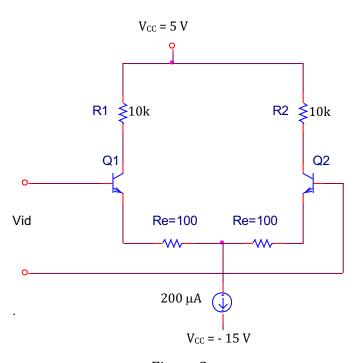


Figure 3

c) Determination of R_{id}:

 $Rid = v_{id}/i_{in}$

Apply a signal voltage of v_{id} = 20 mV and v_{id} = 400 mV. Here, i_{in} can be measured by measuring the **AC voltage drop** across Re and dividing the voltage by Re (**Not** the colour code value).

The experimentally measured value of R_{id}:

For $v_{id} = 20 \text{ mV}$ $v_{id} = 400 \text{ mV}$

Implement the following circuit and repeat Table 1. Identify the differences.

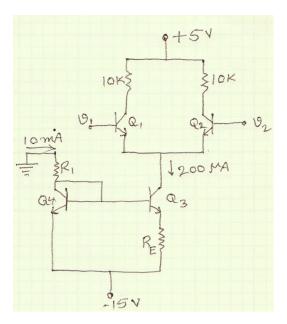


Figure 4