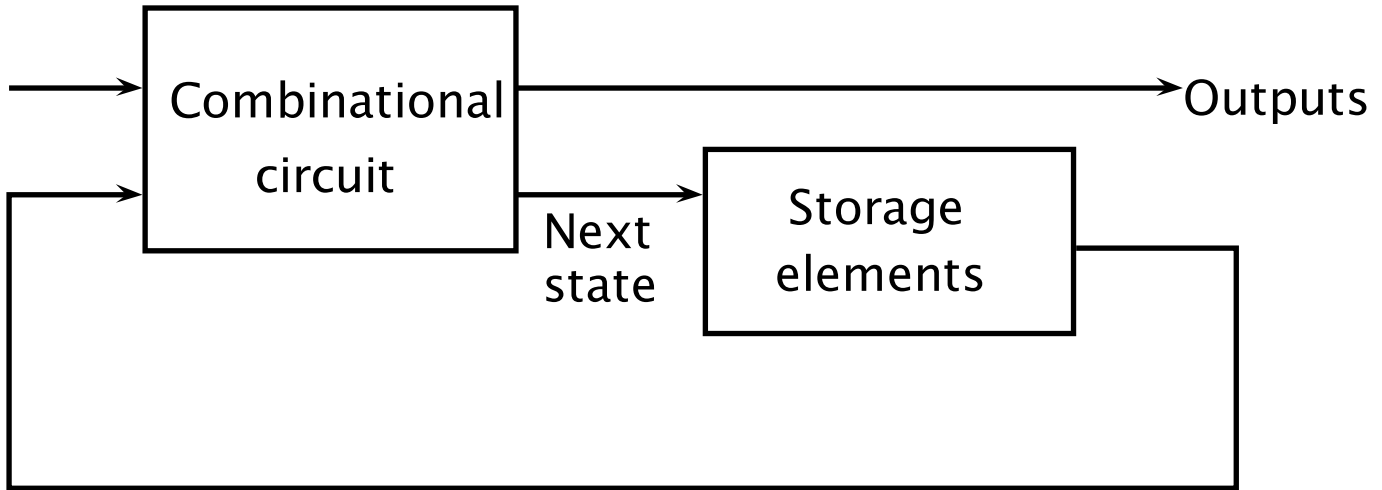


Inputs



Outputs

Combinational  
circuit

Next  
state

Storage  
elements

Present state