

The Cook-Levin Theorem

Theorem (Cook-Levin)

*circuit-SAT is **NP**-complete*

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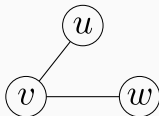
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If no, there's such t that $B(s, t) = \text{yes}$. So $s \notin X$.

□

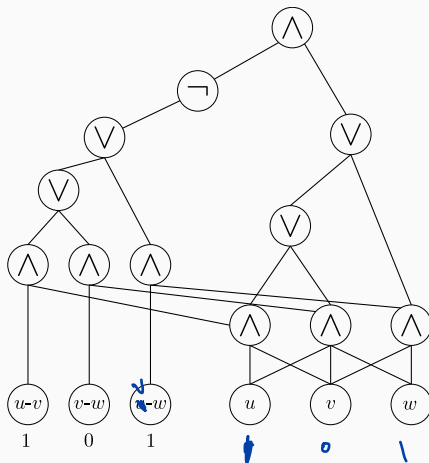
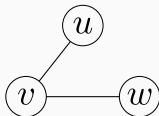
Example of such C_s

Decide if there's an IS of size 2



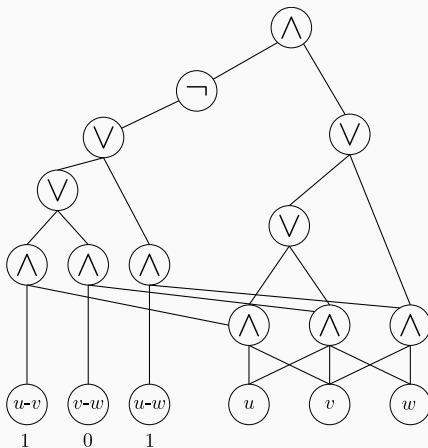
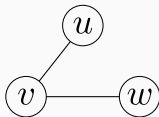
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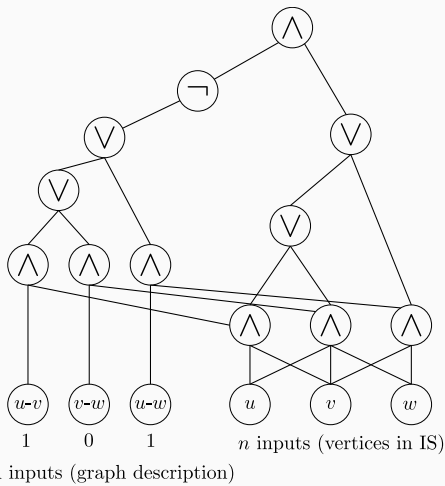
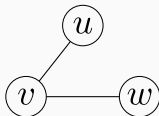
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$\binom{n}{2}$ hardwired inputs (graph description)

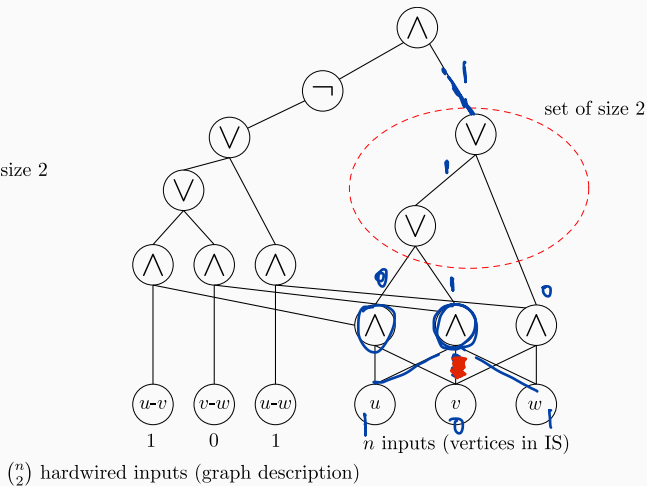
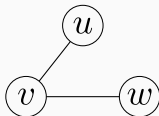
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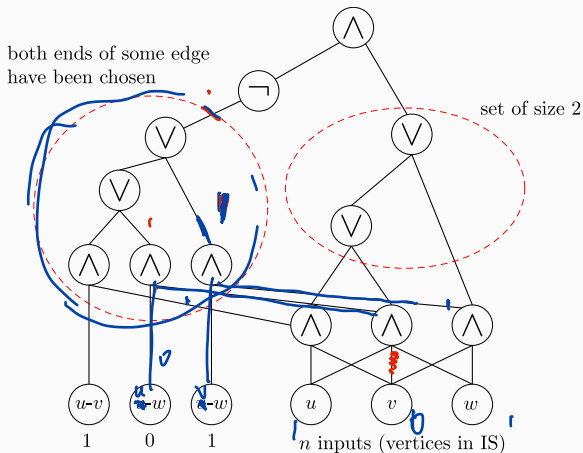
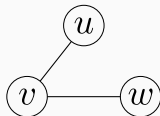
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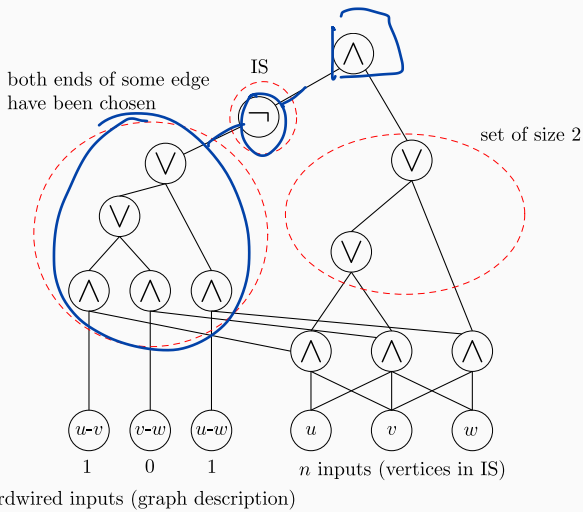
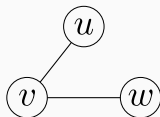
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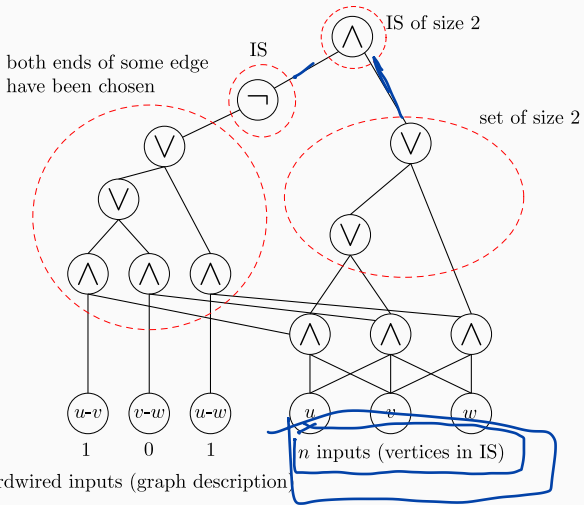
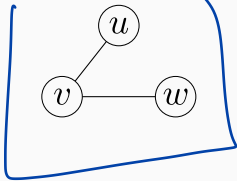
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Proving NP-completeness

Recipe for proving Y is **NP**-complete

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Step 1: Prove $Y \in \mathbf{NP}$

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Observation

*If X is **NP**-complete, $Y \in \mathbf{NP}$, and $X \leq_P Y$, then Y is **NP**-complete*

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Proof.

Let W be any problem in **NP**. Then $W \leq_P X \leq_P Y$ implies that $W \leq_P Y$. Therefore, Y is **NP**-complete □

3-SAT is NP-complete

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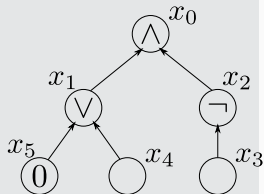
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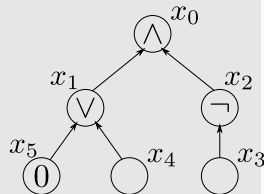
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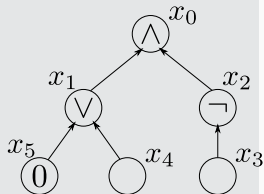
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- ~~$x_1 = x_5 \vee x_4$~~ : add 3 clauses, $(x_1 \vee \bar{x}_4)$,

$(x_1 \vee \bar{x}_5)$, $(\bar{x}_1 \vee x_4 \vee x_5)$

x_4 x_3 $x_1 = x_4 \vee x_5$

0	0	0
0	1	1
1	0	1
1	1	1



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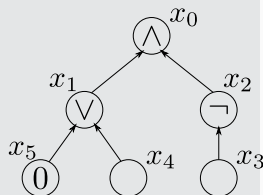
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- $x_0 = x_1 \wedge x_2$: add 3 clauses, $(\bar{x}_0 \vee x_1)$,
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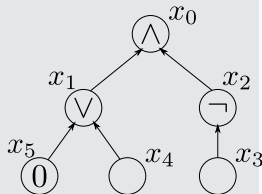
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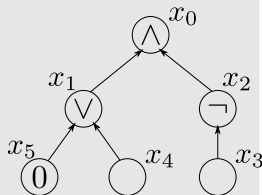
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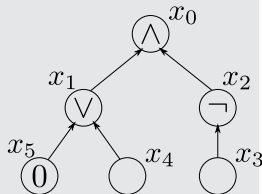
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Turn clauses of length < 3 into clauses of length exactly 3

□

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Want to learn more about this topic? Take CMPSC 464