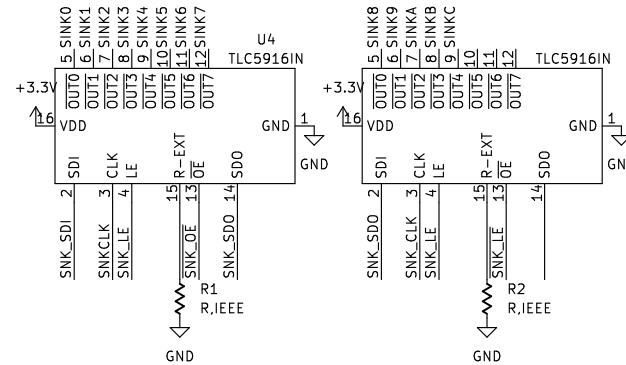
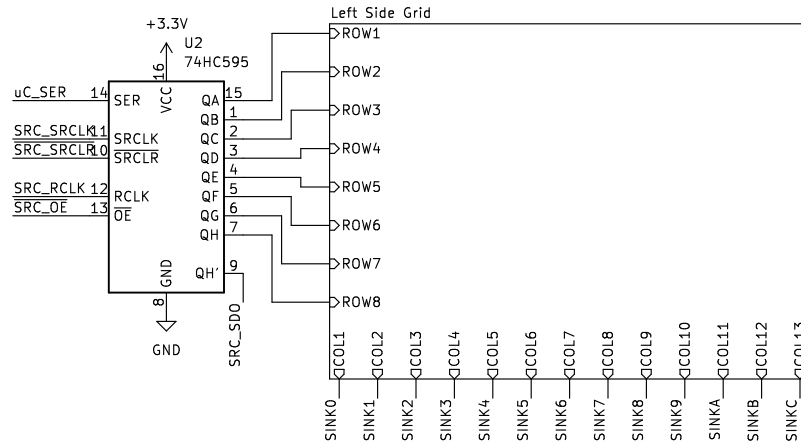
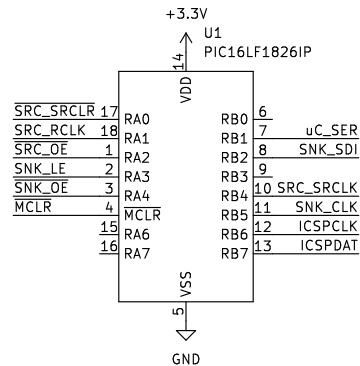


## Common Line Registers for sending data



## Access Line Registers for controlling the Scan Period



Sheet: /

File: Perf Clock.kicad\_sch

**Title: Perf Board Clock**

Size: A Date: 10-24-2023

KiCad E.D.A. eeschema 7.0.8

**Rev:**

Id: 1/3