

Home Appliances & Entertainment (HAE) Service Framework Weekly Technical Meeting – January 7, 2015

Agenda

- 1. Approve Minutes from Previous Meeting
- 2. Discussion on System Requirements
- 3. Discussion on Devices List
- 4. F2F Meeting Place

Attendees

Name	Company
Inhwan Choi	LGE
Alex Kyungho KIM	LGE
Seungchul Han	LGE
Wonchul Choi	LGE
Hwantae Kim	LGE
Giovanni Tiano	Electrolux
Jun Zhang	Haier
Xianging Wang	Haier
Milton Wang	Haier
Zhao Ru	Haier
Yoshinori Nagai	Sharp
Tomoki Ogawa	Panasonic
Tomonori Nakamura	Panasonic
Hiroo Ishikawa	Panasonic

Meeting Notes

- 1. Approve Minutes from Previous Meeting
 - A. All members approved the minutes from previous meeting.
- 2. Discussion on System Requirements

We have 3 proposals from member companies and discussed all proposals.

A. Sharp proposal

- i. Yoshinori gave a presentation of Sharp contribution to system requirements.
- ii. Giovanni commented that we have to clearly identify the scenarios for not only normal behavior but also malfunctions. Scenarios for the actions, monitoring, information exchange and error handling should be clearly defined and detailed behavior should be described.
- iii. There was a question on any limitation of maximum bytes for transmission in the AllJoyn specifications.Inhwan proposed to continue this discussion online after some investigation of the AllJoyn system protocol. Possibly before the next call.

B. Electrolux proposal

- i. Giovanni introduced Electrolux contribution to system requirements.
- ii. Members discussed the general issue on Shared interfaces. It may influence the behavior of other interfaces but members couldn't understand clearly. Electrolux will provide example diagrams and detailed explanation.
- iii. There was a question, what is "Application Profile description"? Giovanni answered that it would be sequential diagrams of monitoring, controlling and information report.
- Regarding to mandatory, optional, vendor specific features, versioning mechanism, and error handling, Inhwan encouraged all members to read the DRAFT Interface Design Guidelines. The link was provided in the meeting slides.

C. Panasonic proposal

- i. Ogawa provided additional system requirements.
- ii. Appropriate security procedures needs to be considered (e.g, Authentication, Authorization and etc). Maintainer said it is an important requirement But it would be out of scope of this project. Since Security 2.0 is being developed under Core working group, it could be utilized by this service framework.
- iii. One of the requirements was that we need to carefully and deeply consider that which features can endanger the user or user's property. But since it is very subjective to decide if a certain feature can endanger or not, it should be considered case by case.

Discussion on Devices List.

- A. Maintainer showed the device list for HAE Service Framework v1.0
- B. We agreed all participants will review the device list and discuss it via email.

4. F2F Meeting Place

2/24~26 F2F workshop (hosted by Electrolux, Italy)
The meeting venue and travel information will be provided

Action Items

- 1. System Requirements
 - A. Distribute the draft system requirements with tracking options (LGE)
 - B. Study the Interface Design Guidelines. (All members)

 Please find the below link: https://wiki.allseenalliance.org/irb/interface_design_guidelines
 - C. Provide example diagrams of sharing interfaces and issues (Electrolux)
 - D. Bring opinions on the maximum size of message (All members)
 - E. Make some edits or additions on draft system requirements (All members)
- 2. Device List
 - A. Review the device category and description for v1.0 (All members)
 - B. Continue discussion online before next call (All members)
- 3. Device Model General Structure
 - A. Prepare a general structure of device model (Haier)
 - B. Share it to mail reflector in advance for effective discussion (Haier)
 - C. Study the Haier's proposal (All members)

Next Meeting Agenda

- 1. Decide the final system requirements for v1.0
- Decide the scope of target devices for v1.0
- 3. Discuss Draft Device Model General Structure