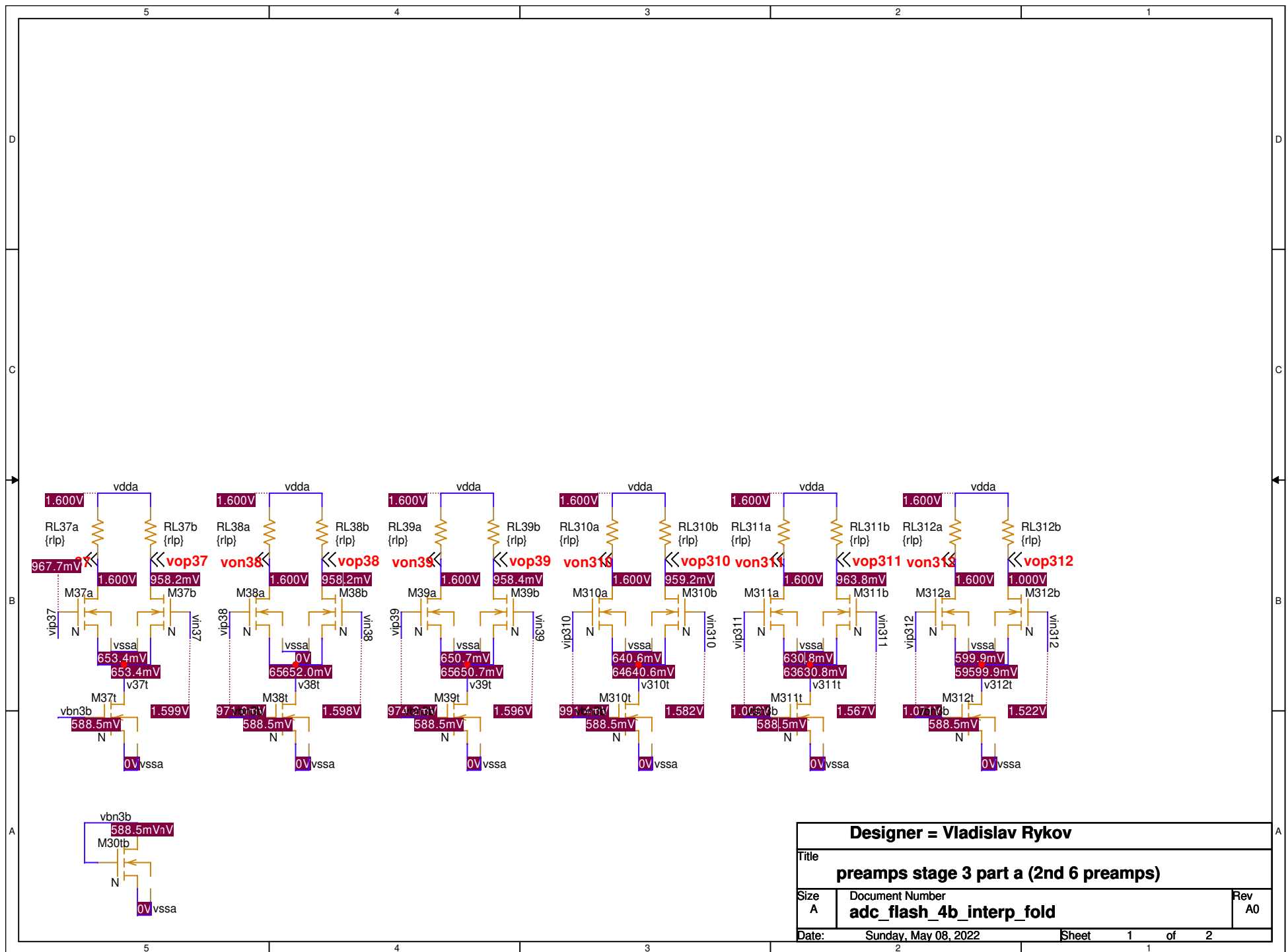
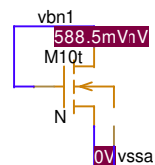
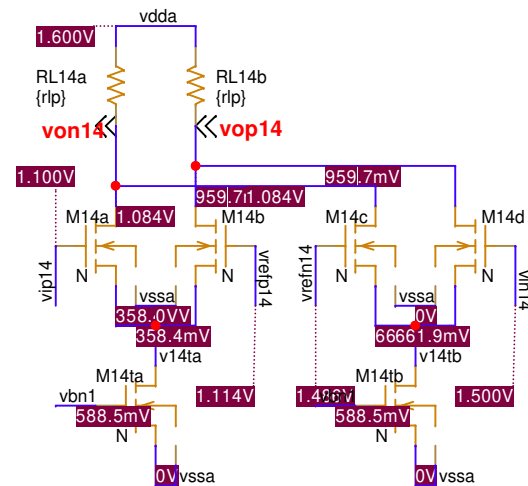
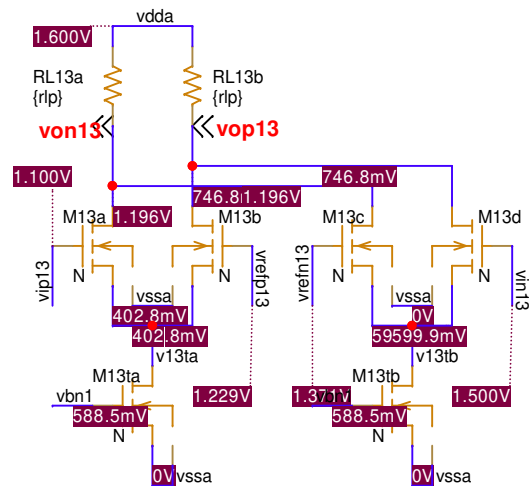
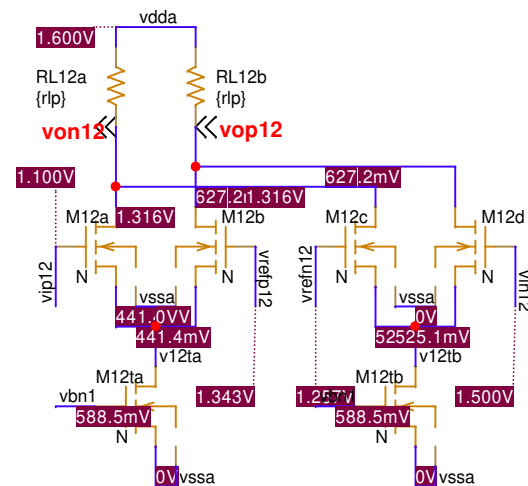
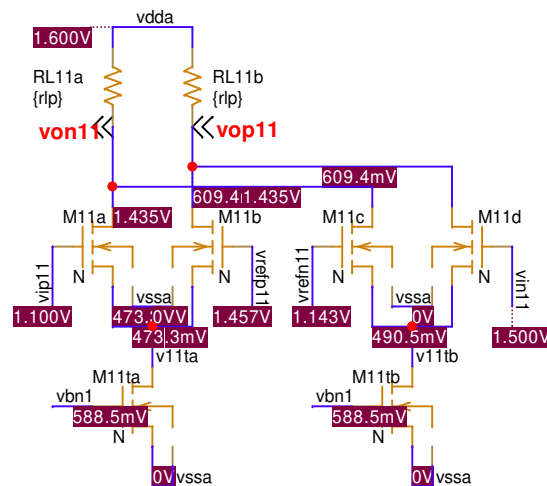


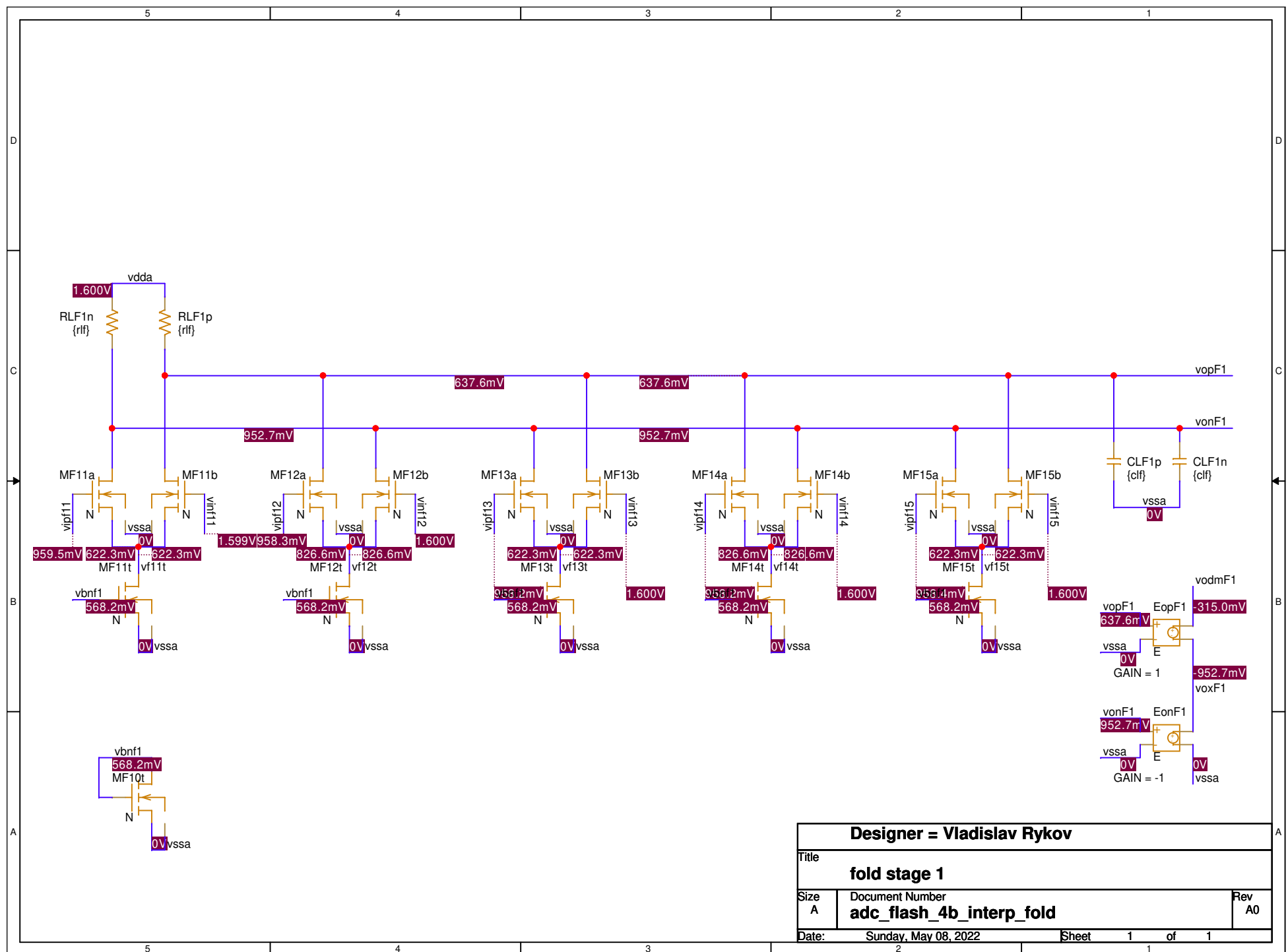
Designer = Vladislav Rykov		
Title		
preamps stage 2		
Size A	Document Number	Rev A0
adc_flash_4b_interp_fold		
Date:	Sunday, May 08, 2022	Sheet 1 of 2

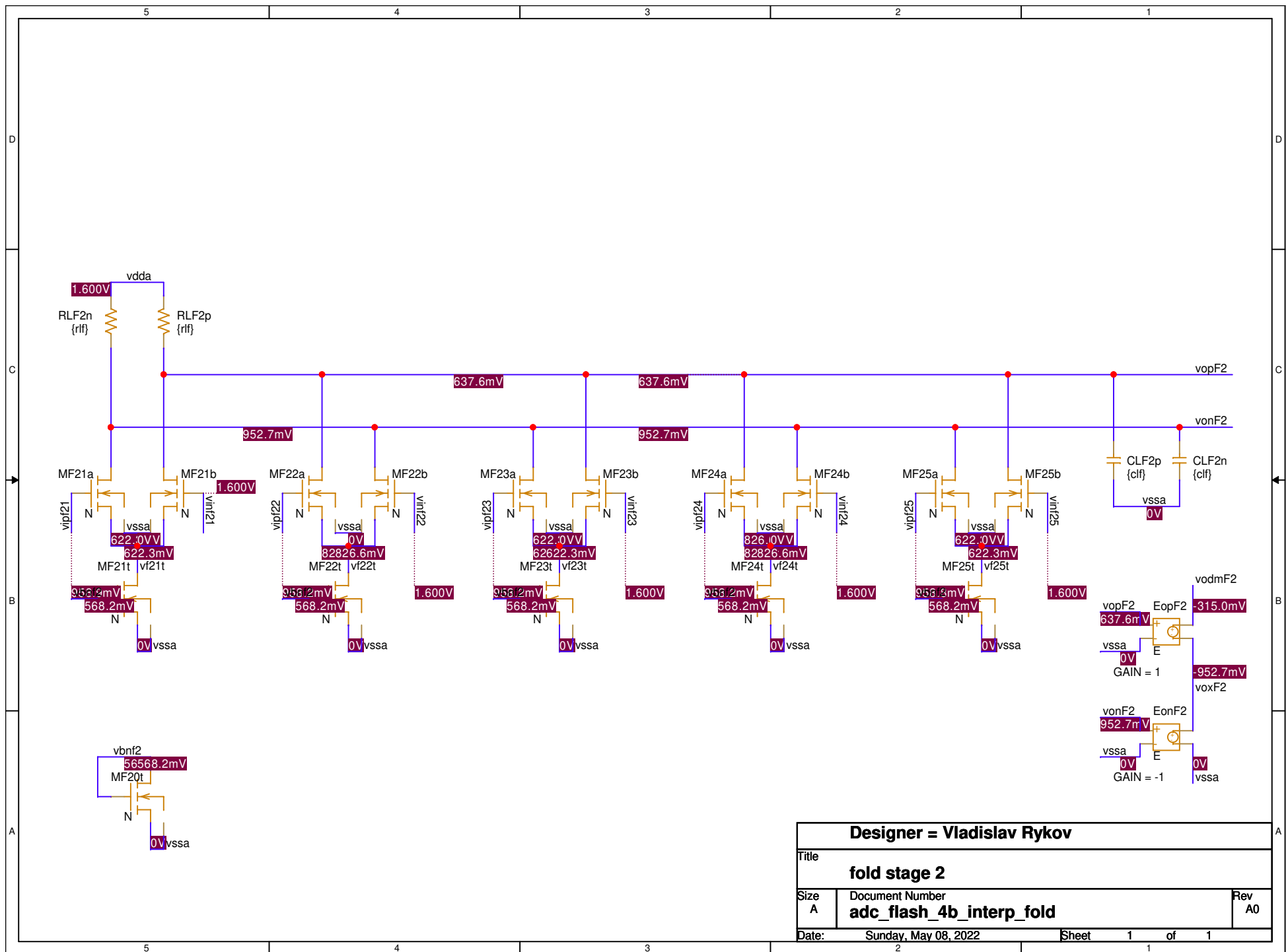


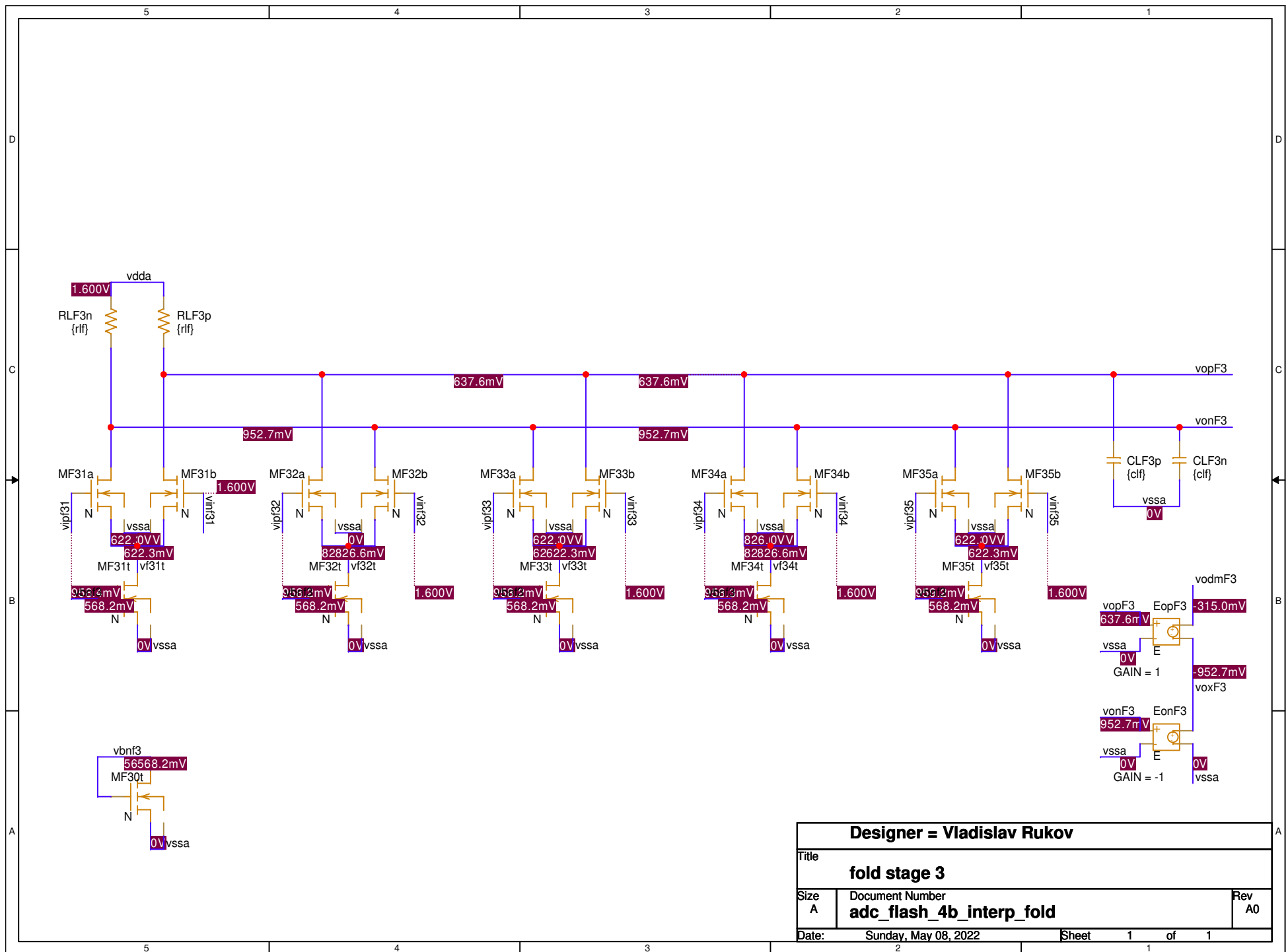
Designer = Vladislav Rykov		
Title		
preamps stage 3 part a (2nd 6 preamps)		
Size A	Document Number	Rev A0
adc_flash_4b_interp_fold		
Date:	Sunday, May 08, 2022	Sheet 1 of 2



Designer = Vladislav Rykov		
Title		
preamps stage 1		
Size A	Document Number	Rev A0
adc_flash_4b_interp_fold		
Date:	Sunday, May 08, 2022	Sheet 1 of 2







Designer = Vladislav Rukov		
Title		
fold stage 3		
Size	Document Number	Rev
A	adc_flash_4b_interp_fold	A0
Date:	Sunday, May 08, 2022	Sheet 1 of 1

vdda = 1.6  
vssa = 0  
  
vicm = {vdda - 0.3}  
vref = 400m

ib1 = 50u  
ib2 = 50u  
ib3 = 50u  
ibf = 40u  
rmax = 1e12  
rmin = 11u

rref = 1k  
rlp = 3.2k  
ravg = 10k  
rlf = 2k  
clp = 10f  
clf = 10f

wp = 1.3u  
wpt = 10.8u  
lp = 0.18u  
lpt = 1.8u  
wf = 9.54u  
wft = 10.8u  
lf = 0.18u  
lft = 1.8u

mp = 2  
mpt = 8  
mpt0 = 2  
mf = 2  
mft = 8  
mft0 = 2

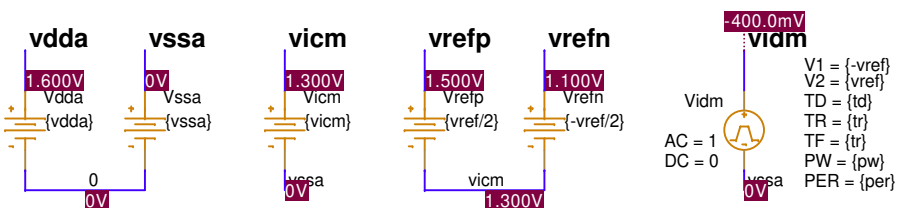
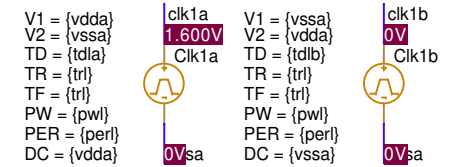
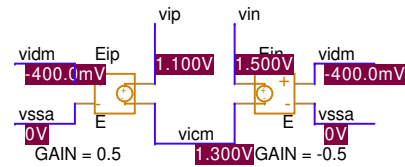
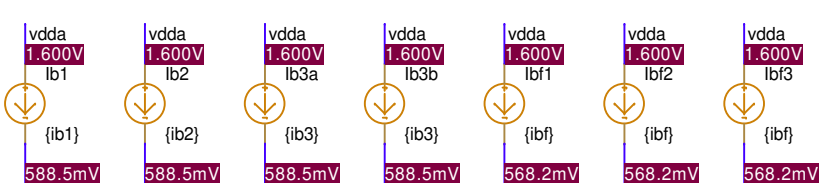
per = {perl\*5000}  
pw = 0  
td = 0  
tr = {per/2}

PARAMETERS:

w0 = 3.6u  
w3 = 0.9u  
w7 = 0.18u  
w9 = 0.9u  
w11 = 0.9u  
w13 = 0.9u  
w15 = 0.9u  
w16 = 0.36u  
w18 = 0.36u  
l0 = 1.8u  
l3 = 0.18u  
l7 = 0.18u  
l9 = 0.18u  
l11 = 0.18u  
l13 = 0.18u  
l15 = 0.18u  
l16 = 0.18u  
l18 = 0.18u  
m1 = 4  
m0 = 2  
m3 = 2  
m7 = 28  
m9 = 2  
m11 = 2  
m13 = 2  
m15 = 1  
m16 = 1  
m18 = 1

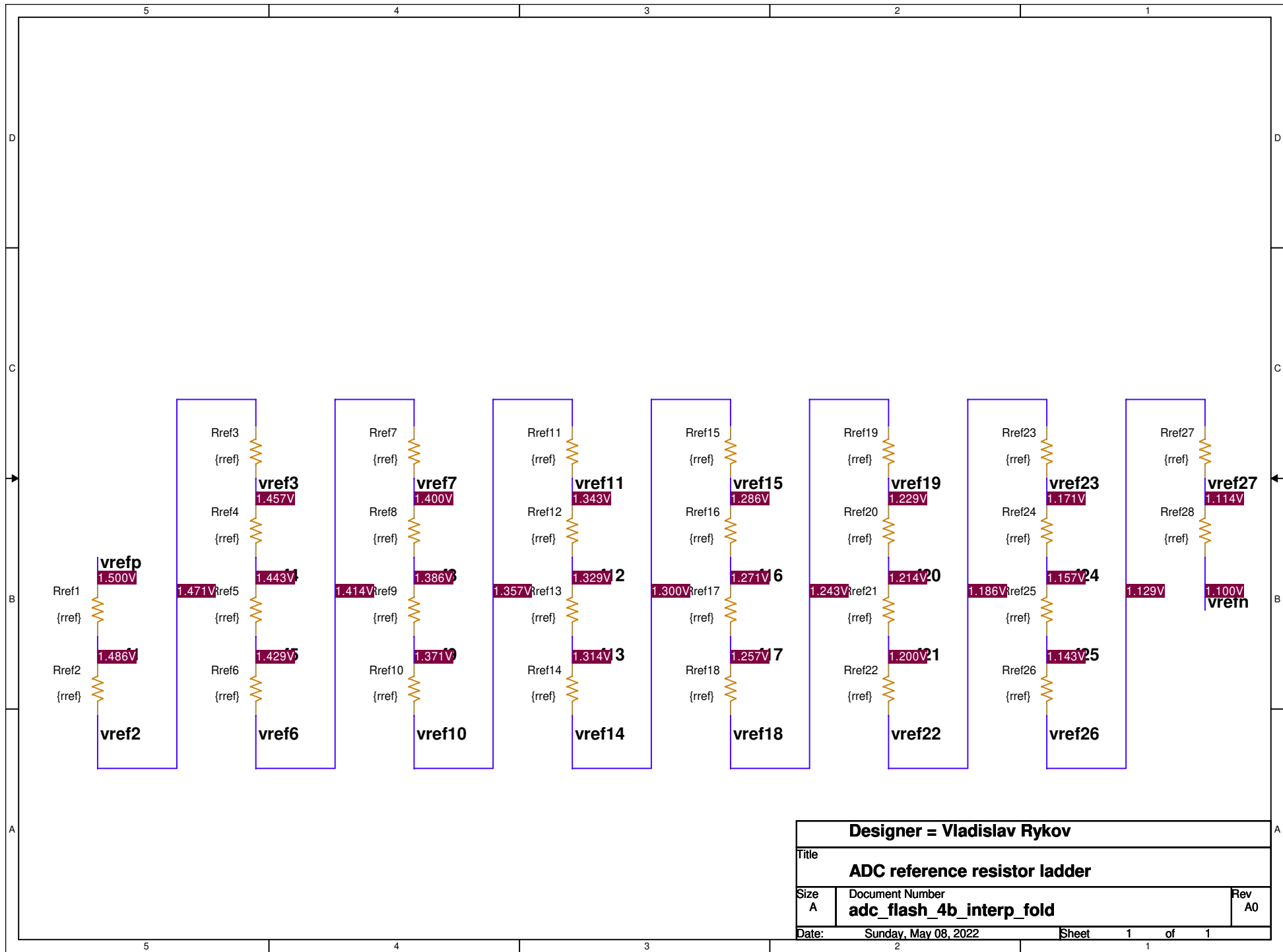
ibias\_L1 = 200u  
ibias\_L2 = 200u  
ibias\_L3 = 200u

perl = 2n  
trl = 100p  
pwl = {perl/2 - trl}  
tdla = {0.5\*perl - 20p}  
tdlb = {0.5\*perl}

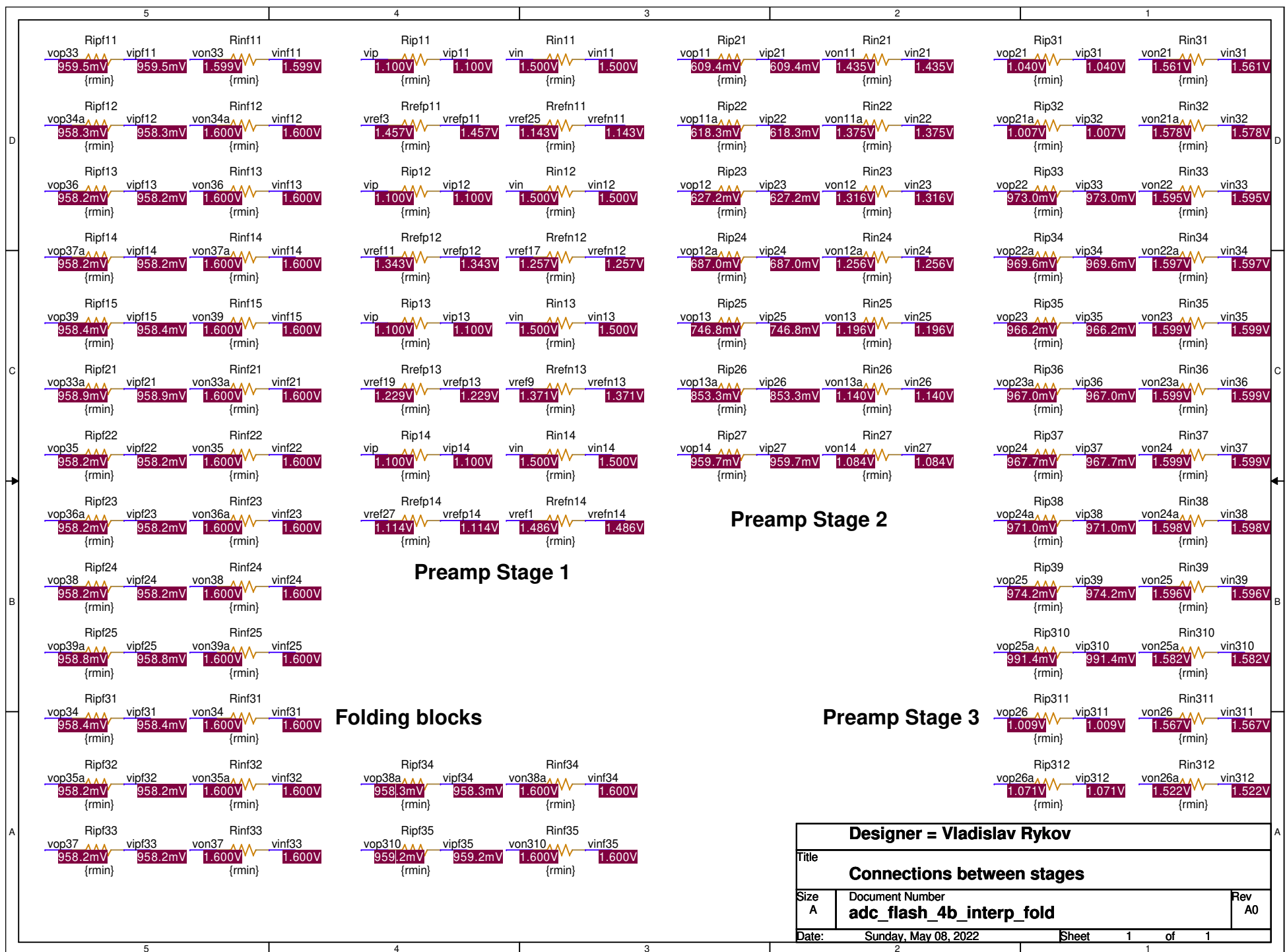


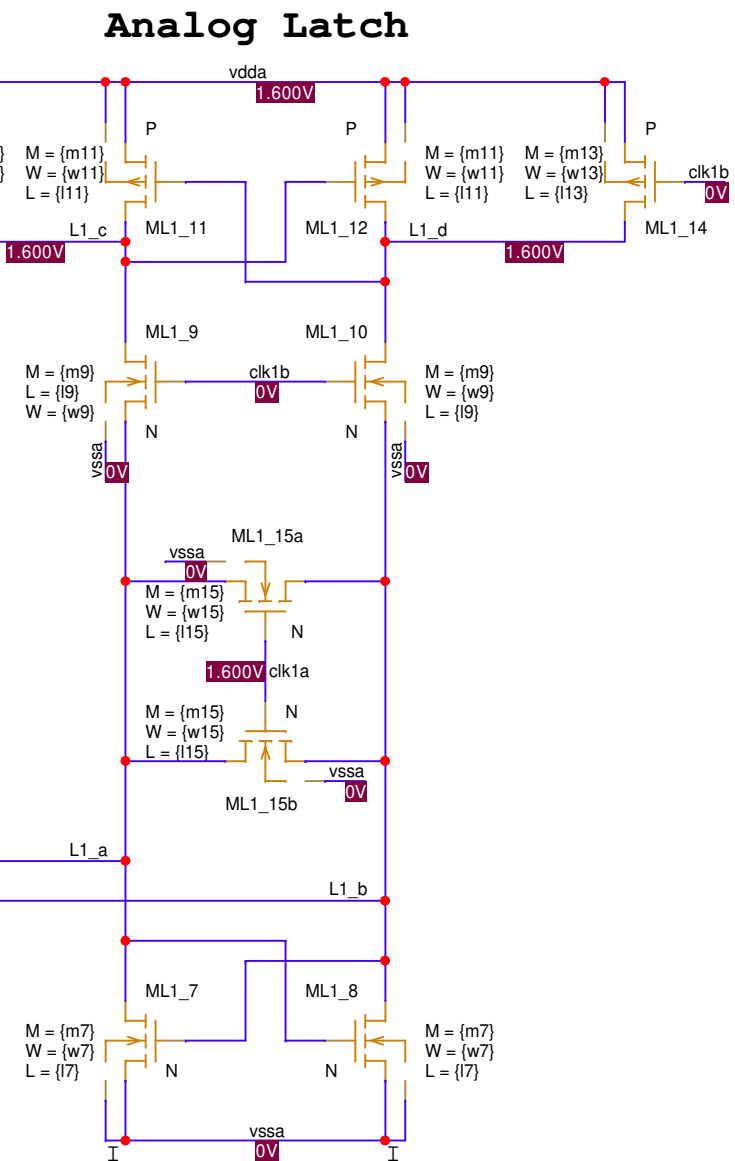
Designer = Vladislav Rykov		
Title		
Sources		
Size A	Document Number adc_flash_4b_interp_fold	Rev A0
Date: Sunday, May 08, 2022	Sheet 1 of 1	





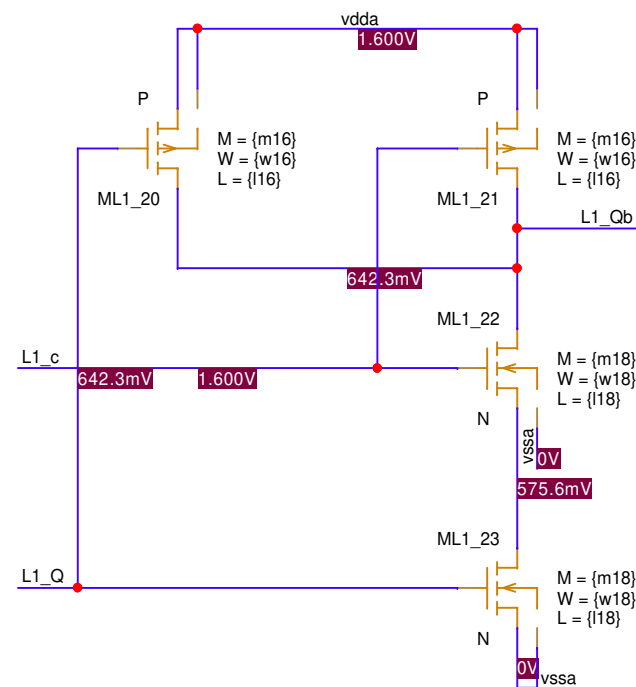
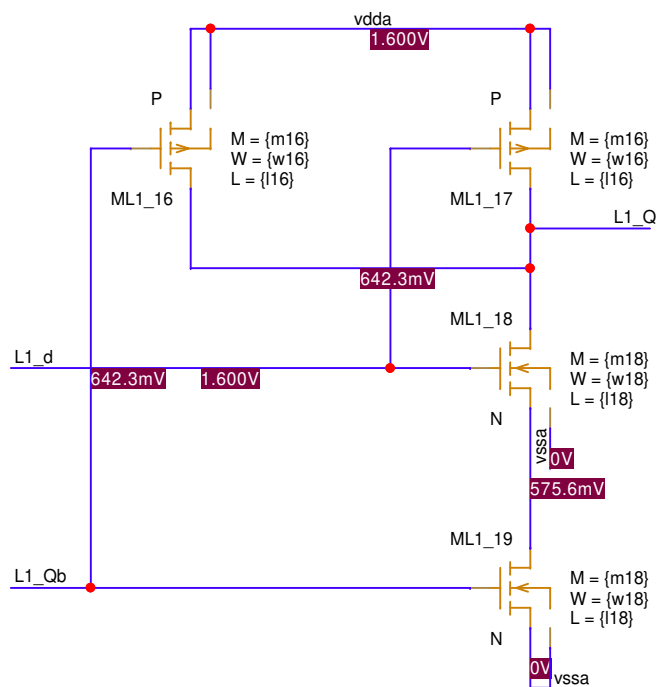
Designer = Vladislav Rykov		
Title		
ADC reference resistor ladder		
Size	Document Number	Rev
A	adc_flash_4b_interp_fold	A0
Date:	Sunday, May 08, 2022	Sheet 1 of 1





Title			
<b>Differential Latching Comparator 1. Preamp and Analog Latch</b>			
Size A	Document Number <b>adc_flash_4b_interp_fold</b>	Rev A0	
Date:	Sunday, May 08, 2022	Sheet	1 of 1

# Digital S/R Latch



Designer = Vladislav Rykov

Title

Differential Latching Comparator 1. SR Flip-Flop

Size  
A

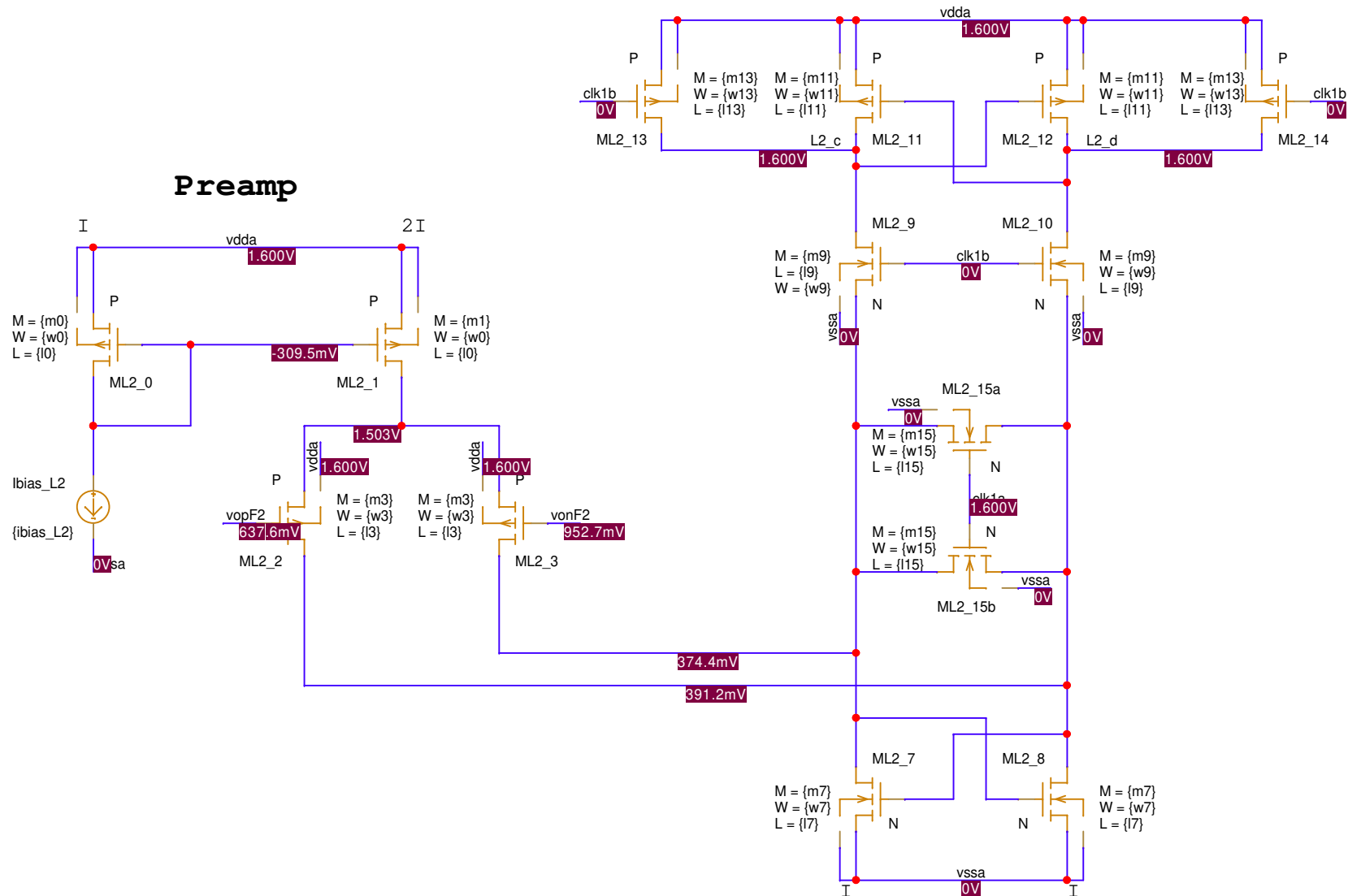
Document Number  
**adc\_flash\_4b\_interp\_fold**

Rev  
A0

Date: Sunday, May 08, 2022

Sheet 1 of 1

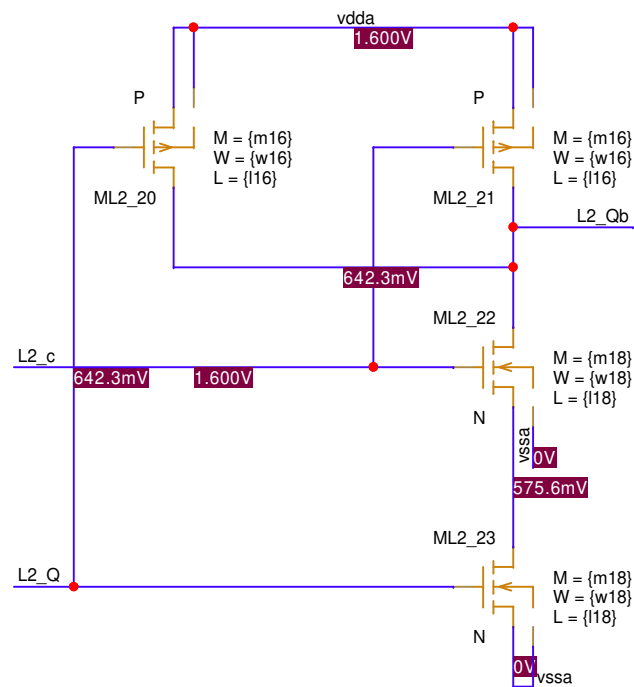
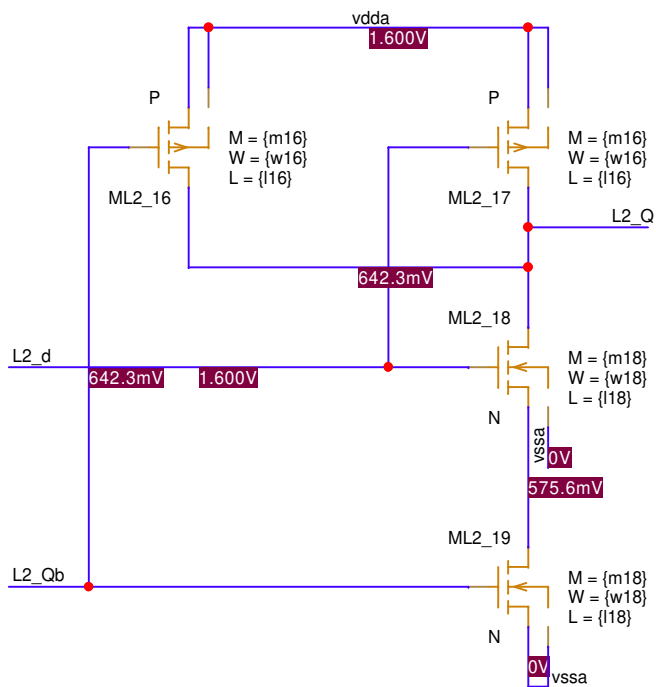
# Analog Latch



Designer = Vladislav Rykov

Title		
Differential Latching Comparator 2. Preamp and Analog Latch		
Size	Document Number	Rev
A	adc_flash_4b_interp_fold	A0
Date:	Sunday, May 08, 2022	Sheet 1 of 1

## Digital S/R Latch



Designer = Vladislav Rykov

Title

Differential Latching Comparator 2. SR Flip-Flop

Size  
A

Document Number  
**adc\_flash\_4b\_interp\_fold**

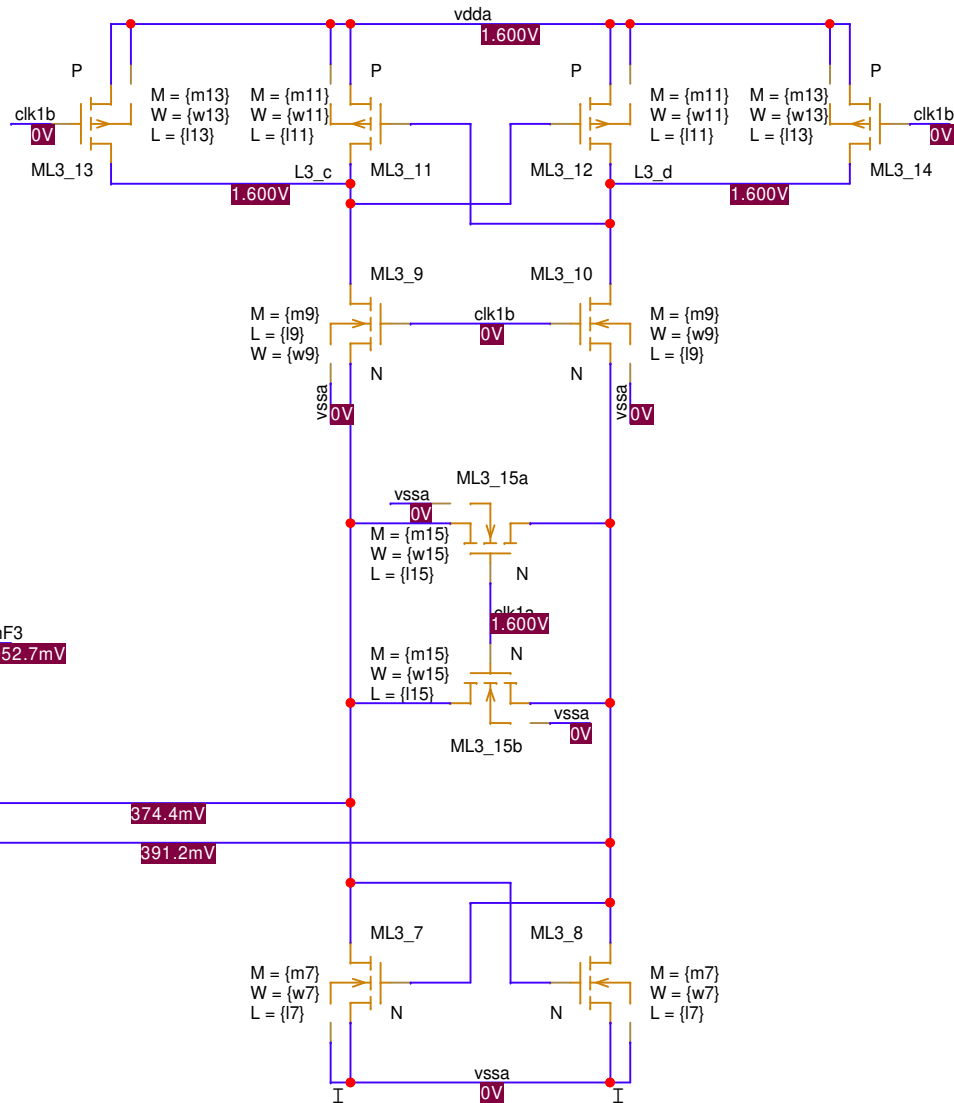
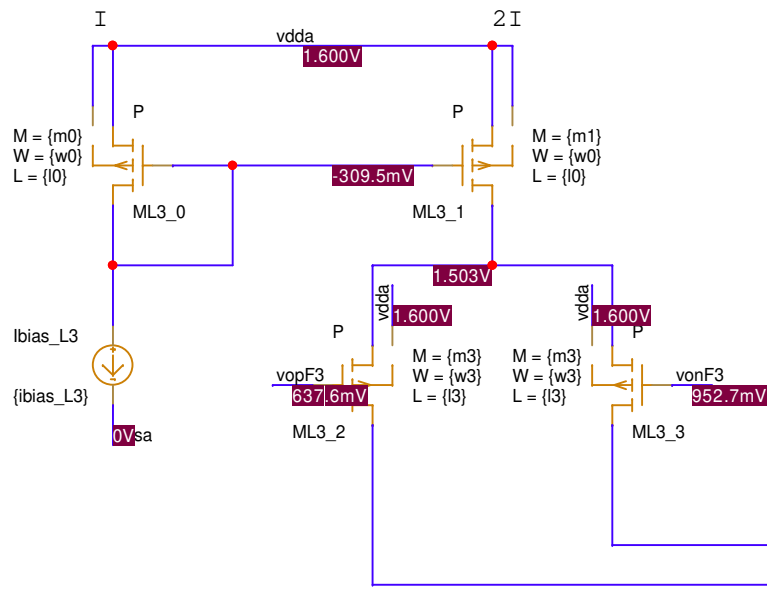
Rev  
A0

Date: Sunday, May 08, 2022

Sheet 1 of 1

# Analog Latch

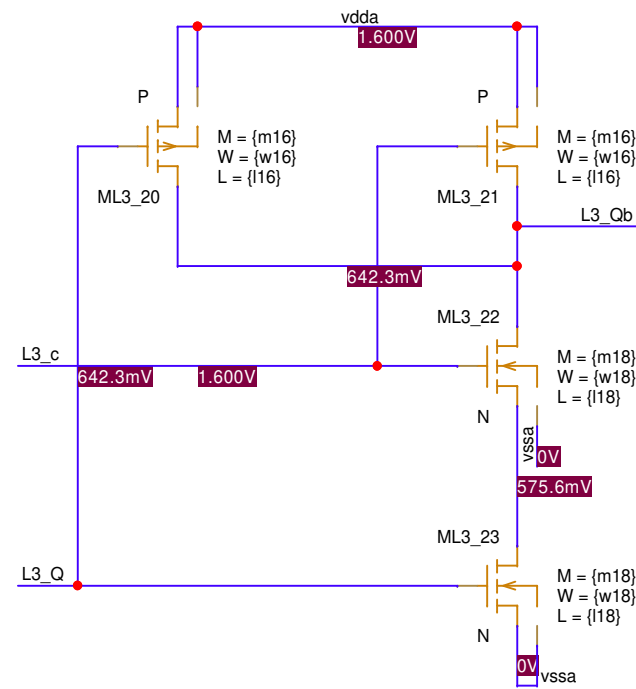
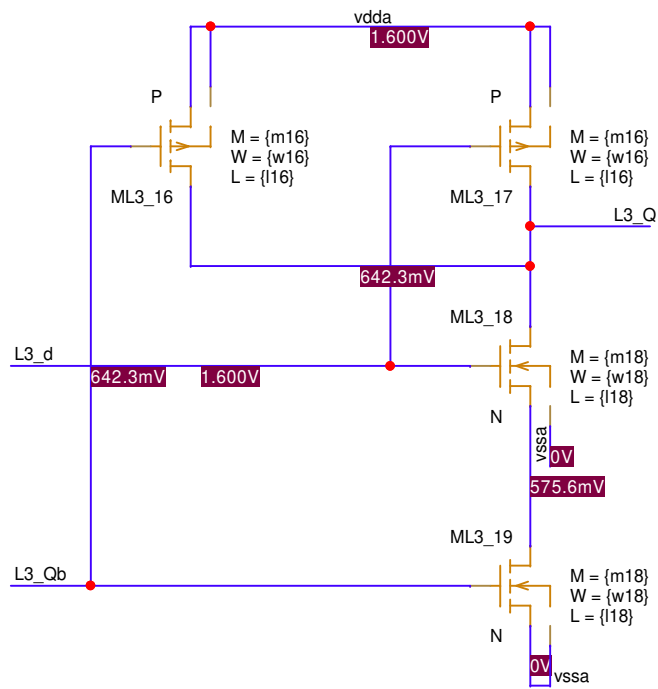
## Preamp



Designer = Vladislav Rykov

Title		
Differential Latching Comparator 3. Preamp and Analog Latch		
Size	Document Number	Rev
A	adc_flash_4b_interp_fold	A0
Date:	Sunday, May 08, 2022	Sheet 1 of 1

## Digital S/R Latch



Designer = Vladislav Rykov

Title

Differential Latching Comparator 3. SR Flip-Flop

Size  
A

Document Number  
adc\_flash\_4b\_interp\_fold

Rev  
A0

Date: Sunday, May 08, 2022

Sheet 1 of 1





