

[illegible]

The diagram shows two pins, BOOT0 and BOOT1, each connected to ground (GND) through a resistor. Resistor R13 (10k) is connected to BOOT0, and resistor R14 (10k) is connected to BOOT1. The ground symbol is shown as a vertical line with three horizontal bars of decreasing width.

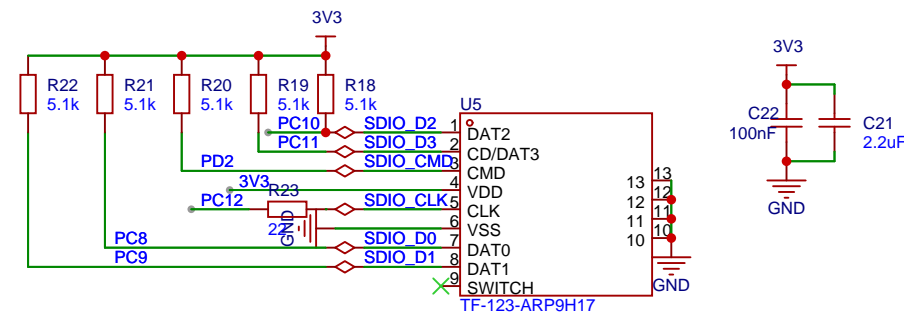
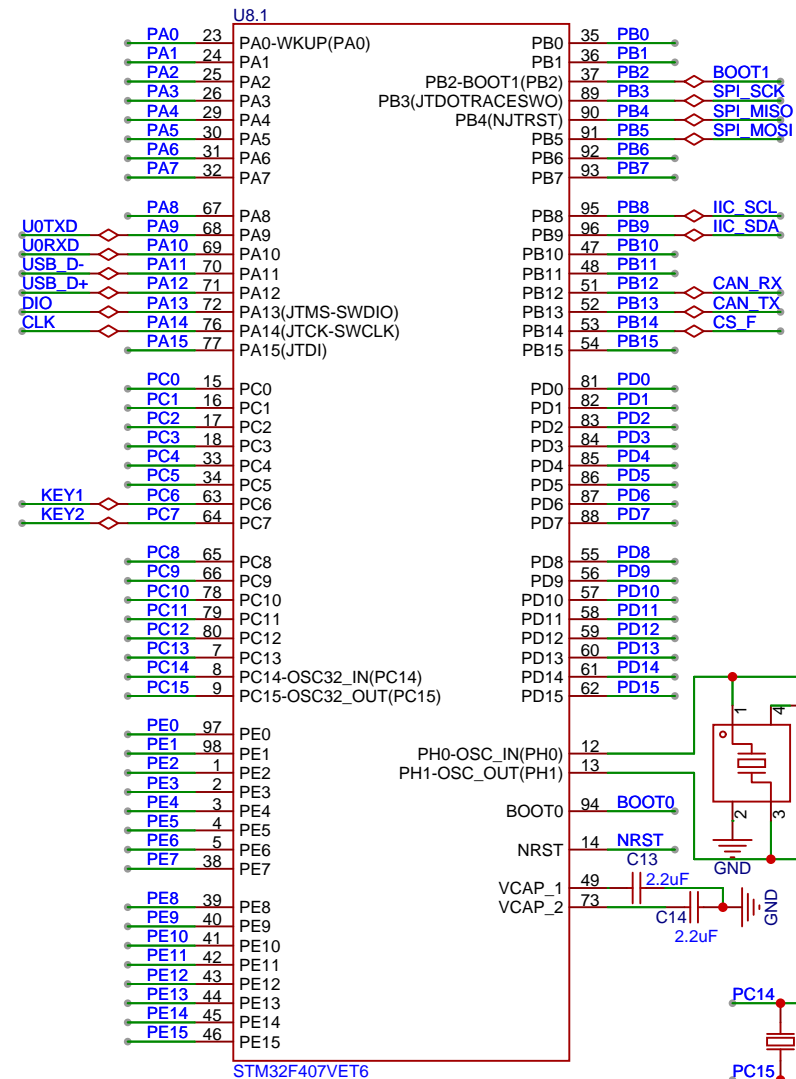
The schematic diagram illustrates the SPI and CAN interfaces for the TJA1050T/CM113 CAN transceiver. The SPI interface is connected to the U6 microcontroller, and the CAN interface is connected to the U7 transceiver. The SPI interface includes a 3V3 supply, a 100nF capacitor (C18), and a 3V3 supply. The CAN interface includes a 3V3 supply, a 100nF capacitor (C19), and a 120 ohm resistor (R17).

SPI Interface:

- U6:** Microcontroller pins 1 (CS#), 2 (DO(IO1)), 3 (WP#(IO2)), 4 (GND), 5 (DI(IO0)), 6 (CLK), 7 (HOLD# or RESET#(IO3)), 8 (VCC).
- U6 Pinout:**
 - 1: CS#
 - 2: DO(IO1)
 - 3: WP#(IO2)
 - 4: GND
 - 5: DI(IO0)
 - 6: CLK
 - 7: HOLD# or RESET#(IO3)
 - 8: VCC
- U6 Labels:** W25Q128FV51G
- U6 Connections:**
 - CS# (PB14) to pin 1
 - DO(IO1) (PB4) to pin 2
 - WP#(IO2) (PB3) to pin 3
 - DI(IO0) (PB5) to pin 5
 - CLK (PB3) to pin 6
 - HOLD# or RESET#(IO3) (PB5) to pin 7
 - VCC (3V3) to pin 8
- U6 Capacitors:** C18 (100nF) connected to VCC and GND.

CAN Interface:

- U7:** TJA1050T/CM113 CAN transceiver pins 1 (TXD), 2 (GND), 3 (VCC), 4 (RXD), 5 (VREF), 6 (CANL), 7 (CANH), 8 (S).
- U7 Pinout:**
 - 1: TXD
 - 2: GND
 - 3: VCC
 - 4: RXD
 - 5: VREF
 - 6: CANL
 - 7: CANH
 - 8: S
- U7 Labels:** TJA1050T/CM113
- U7 Connections:**
 - TXD (PB13) to pin 1
 - GND to pin 2
 - VCC (3V3) to pin 3
 - RXD (PB12) to pin 4
 - VREF (GND) to pin 5
 - CANL (PB12) to pin 6
 - CANH (PB13) to pin 7
 - S (GND) to pin 8
- U7 Capacitors:** C19 (100nF) connected to VCC and GND.
- U7 Resistor:** R17 (120 ohms) connected to CANH and CANL.



原理图	F407			更新日期	2024-04-11
				创建日期	2024-04-11
图页	P1			物料编码	
绘制		RY-STM32F407 V2.0			
审阅					
		版本	尺寸	页	1 共 1
		V2.0	A4	嘉立创EDA	