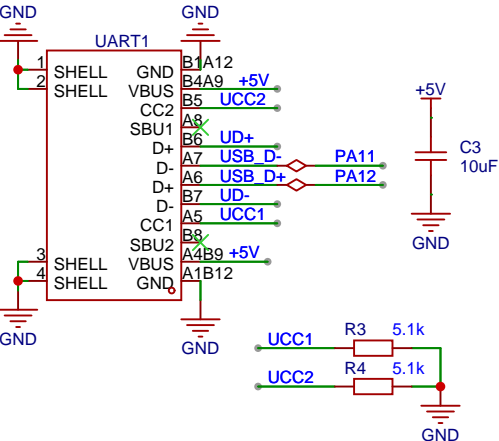
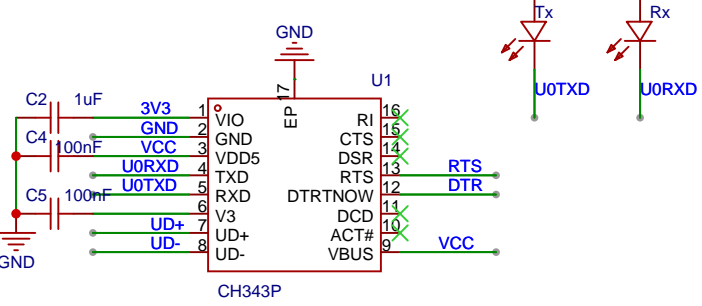


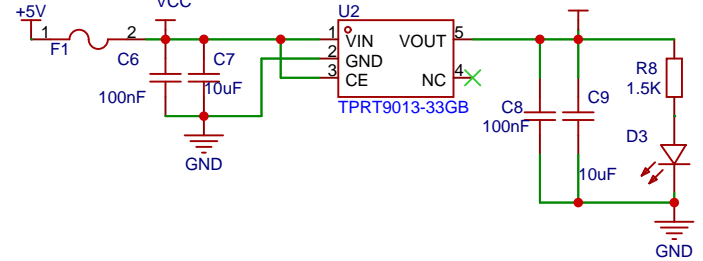
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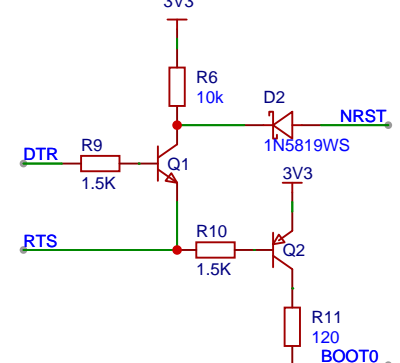
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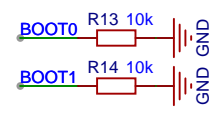
5V TO 3.3V



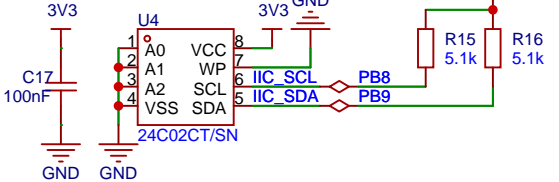
AUTO Program



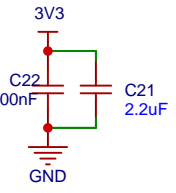
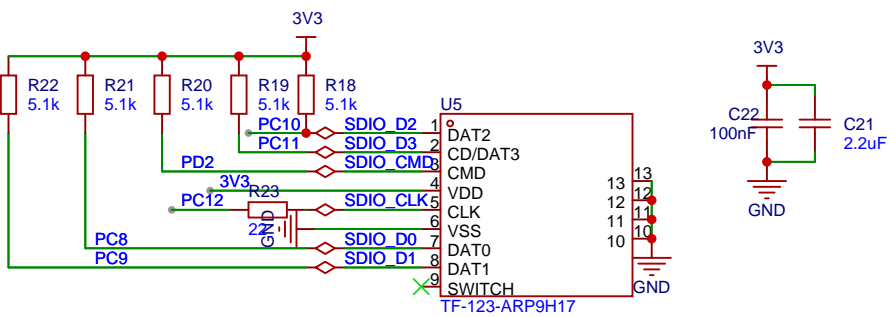
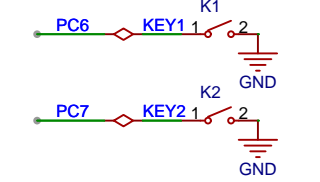
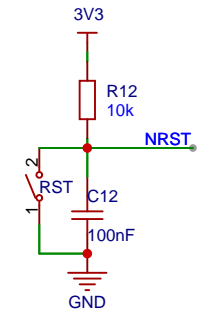
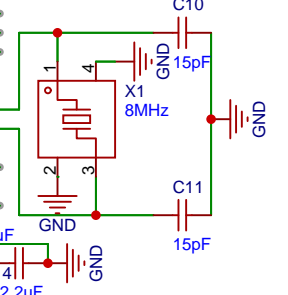
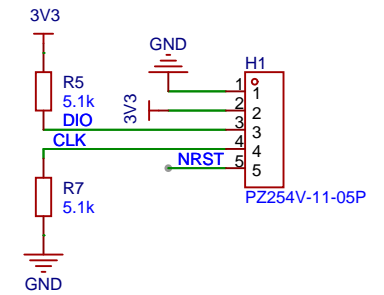
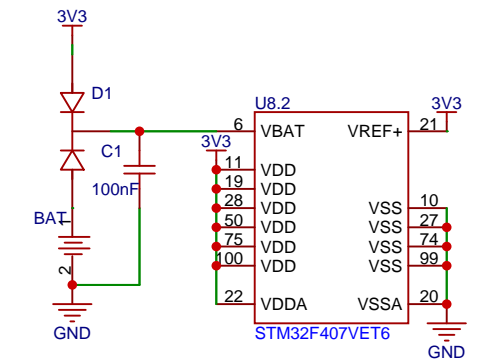
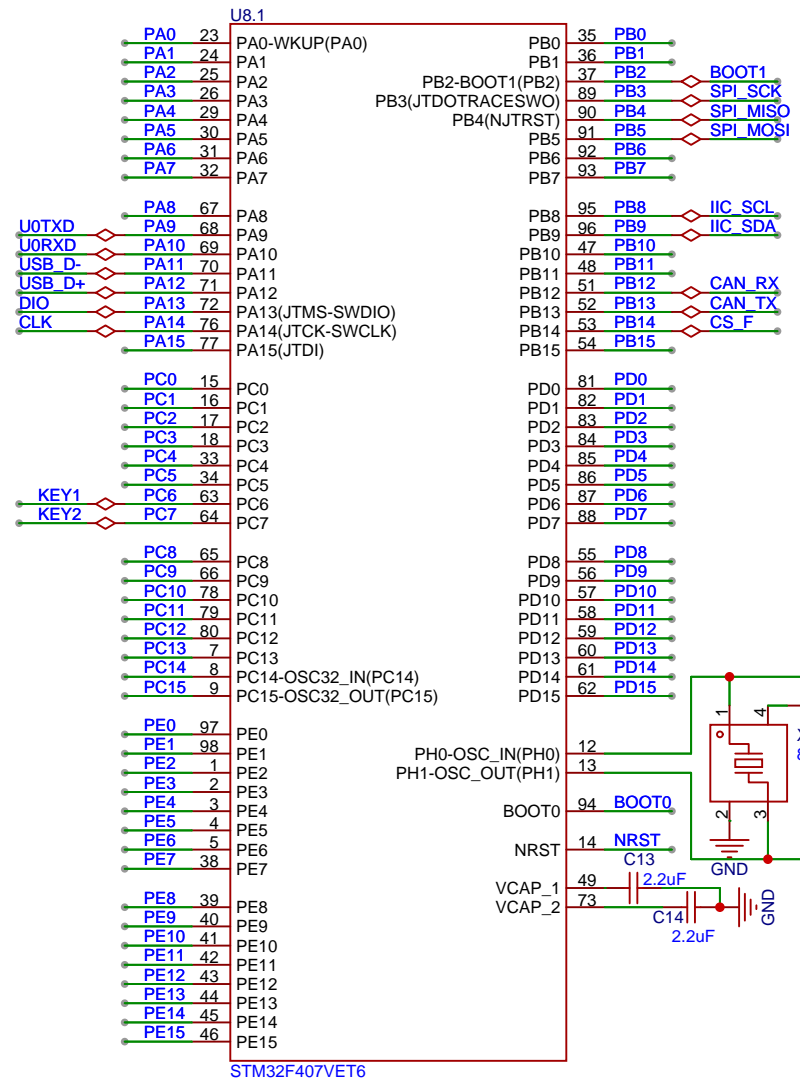
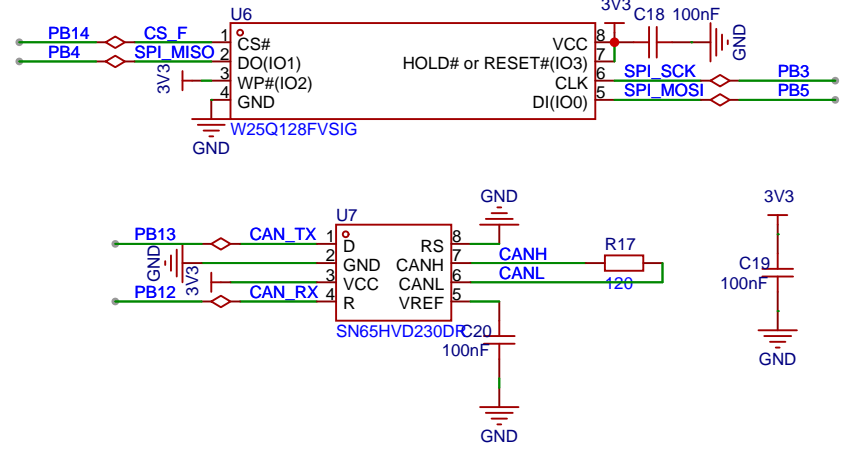
BOOT默认接地



EEPROM



FLASH



原理图	F407		更新日期	2024-11-25
图页	P1		创建日期	2024-02-27
绘制			物料编码	
审阅			RY-STM32F407 V1.0	
	版本	尺寸	页	1 共 1
嘉立创EDA		V2.1	A4	嘉立创EDA