Module MA-INF 4207	**Dynamically Reconfigurable Systems**				
Workload 180 h	**Credit points** 6 CP	**Duration** 1 semester	**Frequency** at least every 2 years		
Module coordinator	Prof. Dr. Joachim K. Anlauf				
Lecturer(s)	Prof. Dr. Joachim K. Anlauf				
Classification	**Programme** M. Sc. Computer Science		**Mode** Optional	**Semester** 2.	
Technical skills	Knowledge of the most important FPGA architectures, ability to select appropriate FPGAs for a given application, overview of programming tools				
Soft skills	Communicative skills (oral and written presentation of solutions), social skills (ability to solve problems in small teams, discussions of solution concepts) self competences (ability to accept and formulate criticism, ability to analyze problems)				
Contents	Architecture of FPGAs, Configurable Logic Blocks, Wiring Ressources, Special Blocks, Hardware Description Languages, Synthesis, Technology Mapping, Place and Route, FPGA Computing, Partial Reconfigurability				
Prerequisites	none				
Format	**Teaching format**	**Group size**	**h/week**	**Workload[h]**	**CP**
	Lecture	60	2	30 T / 45 S	2.5
	Exercises	30	2	30 T / 75 S	3.5
	T = face-to-face teaching; S = independent study				
Exam achievements	Oral exam (graded)				
Study achievements	Successful exercise participation (not graded)				
Forms of media					
Literature	Current research papers and technical documentation				