

Module MA-INF 1305	Graduate Seminar Chip Design				
Workload 180 h	Credit points 6 CP	Duration 1 semester	Frequency every year		
Module coordinator	Prof. Dr. Jens Vygen				
Lecturer(s)	All lecturers of Discrete Mathematics				
Classification	Programme M. Sc. Computer Science		Mode Optional	Semester 3.	
Technical skills	Competence to understand new theoretical results and practical solutions in VLSI design and related applications, as well as presentation of such results				
Soft skills	Ability to read and understand research papers, abstract thinking, presentation of mathematical results in a talk				
Contents	Current topics in chip design and related applications				
Prerequisites	Required: At least 1 of the following: MA-INF 1102 – Combinatorial Optimization MA-INF 1202 – Chip Design				
Format	Teaching format	Group size	h/week	Workload[h]	CP
	Seminar	10	4	60 T / 120 S	6
	T = face-to-face teaching; S = independent study				
Exam achievements	Oral presentation, written report (graded)				
Study achievements	none (not graded)				
Forms of media					
Literature	The topics and the relevant literature will be announced towards the end of the previous semester				