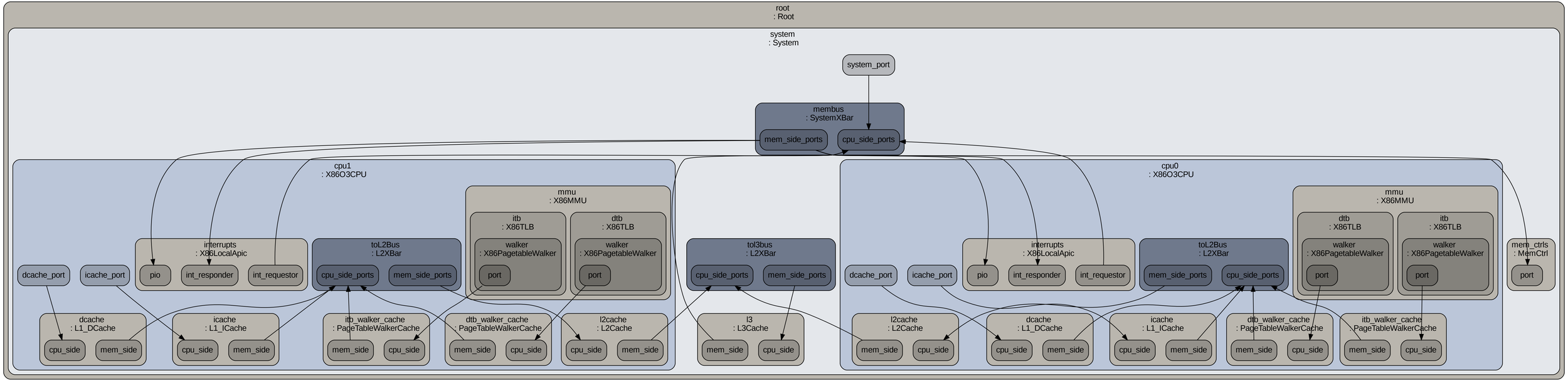
root
: Root
system
: System
system_port
membus
: SystemXBar
mem_side_ports
cpu_side_ports
cpu1
: X86O3CPU
mmu
: X86MMU
itb
: X86TLB
walker
: X86PagetableWalker
port
dtb
: X86TLB
walker
: X86PagetableWalker
port
interrupts
: X86LocalApic
pio
int_responder
int_requestor
toL2Bus
: L2XBar
cpu_side_ports
mem_side_ports
dcache_port
icache_port
dcache
: L1_DCache
cpu_side
mem_side
icache
: L1_ICache
cpu_side
mem_side
itb_walker_cache
: PageTableWalkerCache
mem_side
cpu_side
dtb_walker_cache
: PageTableWalkerCache
mem_side
cpu_side
l2cache
: L2Cache
cpu_side
mem_side
tol3bus
: L2XBar
cpu_side_ports
mem_side_ports
l3
: L3Cache
mem_side
cpu_side
cpu0
: X86O3CPU
mmu
: X86MMU
dtb
: X86TLB
walker
: X86PagetableWalker
port
itb
: X86TLB
walker
: X86PagetableWalker
port
interrupts
: X86LocalApic
pio
int_responder
int_requestor
toL2Bus
: L2XBar
mem_side_ports
cpu_side_ports
dcache_port
icache_port
l2cache
: L2Cache
mem_side
cpu_side
dcache
: L1_DCache
cpu_side
mem_side
icache
: L1_ICache
cpu_side
mem_side
dtb_walker_cache
: PageTableWalkerCache
mem_side
cpu_side
itb_walker_cache
: PageTableWalkerCache
mem_side
cpu_side
mem_ctrls
: MemCtrl
port