

# Internship Notification Form, IIT Delhi

## About Organisation

<b>Name of Company:</b>	Jaguar Land Rover Technology and Business Services India Limited
<b>Date of Establishment:</b>	2024-07-25
<b>Number of Employees:</b>	600
<b>Social Media Page Link:</b>	<a href="http://www.jaguar.com">www.jaguar.com</a>
<b>Website:</b>	<a href="http://www.jaguar.com">www.jaguar.com</a>
<b>Type of Organization:</b>	Private
<b>Nature of Business:</b>	Core Engineering & Technology

## Internship Profile

<b>Job Title:</b>	Electronics HW Design Intern
<b>Job Description:</b>	<p>Electronics Design is a key discipline area in the development of low voltage hardware upcoming vehicles. Members of this Team will engage in the design and development of Low Voltage Hardware Architecture components for vehicle Electronic Control Units (ECUs). Our internship program has been designed to give right level of industry exposure for a budding engineer.</p> <p>The purpose of the internship program is:</p> <ul style="list-style-type: none"><li>• To offer interns the opportunity to complement university education.</li><li>• To provide practical experience and technical, cultural, scientific and relationship improvement.</li><li>• To identify, attract and develop interns with potential to hold future positions within Jaguar Land Rover.</li></ul> <p>As an intern you will be matched with an experienced engineer who will mentor you for the duration of internship. You will have an opportunity to influence the evolution of JLR products. Your design, code and innovative mindset will contribute to solving some of the most complex technical challenges in the area of connected cars, electrification of cars, autonomous driving, vehicle diagnostics, vehicle data analytics just to name a few.</p> <p>What to expect?</p> <p>You will work in electrical engineering function, with mentors who are at the forefront of their field. JLR believes the best training can be had, when you are working on live programs. You will have an opportunity to make tangible, strategic contributions to the company's success even within the short 8-10 weeks of internship.</p>

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In addition to working on impactful assignments, you will have the opportunity to engage with fellow JLR engineers for both professional and personal development, expand your professional network, participate in activities with other interns, college seniors throughout your internship. You will get to work with teams that drive product strategy and collaborate closely with engineering development and cross-functional teams to define and deliver on the next vehicle programs

Who we are looking for:

Our cars are the embodiment of our approach to life. We believe in making every day extraordinary; that life is about feelings, not just figures. We feel the same about the people we hire.

First, you need to be passionate and motivated to contribute to the business growth and on-going success. Beyond that, we value resilience, a sense of responsibility, a willingness to learn, keen problem-solving skills and the ability to work with others.

Our people are amongst the most talented in their field. Working alongside them, you'll play your part in developing advanced products in a company that's committed to building on every aspect of its success.

We're looking for individuals who have taken the time to think about who we are and what we're looking for. Our selection process is aimed at showcasing the best of your skills, expertise and personality.

Key Performance Indicators

- Willingness and ability to learn
- Work seamless in teams within and outside JLR (e.g., Software and Design Partners)
- Ability to work independently
- Communication skills
- Work discipline

Key Accountabilities and Responsibilities:

- Work in Hardware Design team in the areas of:
  - o Hardware circuit design for ECUs
  - o Hardware Component Identification for Design
  - o Printed Circuit Board (PCB) Layout
  - o Detailed Reviews of Hardware Design
  - o Building deep knowledge in semiconductors and HW interfacing
  - o Hardware/System Fault analysis
  - o Hardware and system debugging
- Work with a multi-disciplinary engineering development team that includes application engineering, controls engineering, mechanical design, control hardware design, and test / validation in an Agile fashion.
- Collaborate with cross-discipline teams to design, develop and test ECUs
- Design and execute test cases for unit, function, subsystem testing and acceptance testing
- Benchmark and optimize the performance of new and existing units
- Adhere to department's quality targets and participate in best practice discussions

Knowledge, Skills and Experience:

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Essential:

- Candidates must have completed 6th semester (entering final year of their undergraduate program) in Electrical and Electronics, Electronics & Communication Engineering, Power Electronics OR related technical field OR equivalent
- Dual degree program candidates must have completed 8th semester (entering final year of their dual degree program) in the aforementioned areas are also eligible to apply.

Minimum 6.5 CPI

- Have a passion for electric mobility and for automotive engineering
- Deep interest in electric/electronic circuits design, digital/ analog electronics.

Working knowledge of processors/micro controllers, basic electronic material science

- Exposure to MATLAB/ Simulink environments
- Basic knowledge of real-time operating system
- Good software debugging skills.
- Effective technical documentation skills
- Creativity and a willingness to learn.
- Excellent technical and problem-solving skills
- Excellent communication and teamwork skills
- High level of self-motivation

Desirable:

- Ability to deliver presentations and efficiently communicate with both internal and external stakeholders.
- Familiarity with Electronic Hardware Design tools like VHDL
- Good knowledge of processors, memory controllers, MATLAB Simulink,
- Familiarity with Instrumentation and measurement in electronic circuitry.
- Familiarity with Design Process, Industry Standards.
- Familiar with EMI/EMC test standards and Test procedure.
- Knowledge of C/C++ programming

Personal Profile:

Essential:

- Demonstrated excellent academic and leadership during school and college education
- Electronics related experience during Internship
- Freely and proactively shares knowledge with others
- Demonstrated ability to meet goals and objectives.
- Displays a proactive willingness to volunteer for work elements / projects outside job scope where the individual can contribute, and it is a company priority
- Acts with freedom to take on and resolve technical challenges
- Flexibility to travel to other JLR / partner locations when required to assist in delivery of project objectives

**Minimum No. of Hires:** 1

**Expected No. of Hires:** 1

**Location(s)/Place of Posting/Online:** Bengauru

**Skillset:** Electrical, Electronics

<b>Minimum CGPA:</b>	6.5
<b>Students with backlog eligible:</b>	No

## Selection Process

<b>Resume Shortlist:</b>	Yes
<b>Mode of Selection:</b>	Hybrid
<b>Resume shortlisting before test?:</b>	Yes
<b>Test:</b>	Yes
<b>Mode of Test:</b>	Online
<b>Test duration (minutes):</b>	120
<b>Aptitude/Psycometric:</b>	Yes
<b>Technical:</b>	Yes
<b>Group Discussion:</b>	No
<b>Personal Interview:</b>	No
<b>Technical Round:</b>	Yes
<b>HR Round:</b>	No
<b>Medical Test:</b>	No

## Eligible Academic Programs

<b>Diversity Recruiting:</b>	No
<b>Eligible Years:</b>	Graduating in 2026 (Pre-Final Year Students) - B.Tech / Dual / Master's
<b>Eligible Departments:</b>	B.Tech in Electrical Engineering, B.Tech in Electrical Engineering (Power and Automation), B.Tech in Energy Engineering, B.Tech in Engineering Physics, B.Tech in Engineering and Computational Mechanics

## Stipend Details

<b>Stipend (per month) (In INR Per Month):</b>	100,000 INR Per Month
<b>Accommodation:</b>	Yes
<b>Provision of PPO based on</b>	Yes

performance?

**Tentative CTC for PPO select:** 2,100,000 INR Per Annum