

# Internship Notification Form, IIT Delhi

## About Organisation

<b>Name of Company:</b>	Intel India Ltd.
<b>Date of Establishment:</b>	1968-07-18
<b>Number of Employees:</b>	-
<b>Social Media Page Link:</b>	-
<b>Website:</b>	<a href="https://www.intel.com/content/www/us/en/homepage.html">https://www.intel.com/content/www/us/en/homepage.html</a>
<b>Type of Organization:</b>	MNC (Foreign Origin)
<b>Location of Head office:</b>	Santa Clara, California
<b>Nature of Business:</b>	Core Engineering & Technology

## Internship Profile

<b>Job Title:</b>	Intern
<b>Job Description:</b>	<ul style="list-style-type: none"><li>- Hardware Engineer conducts research, definition, design, verification &amp; documentation for SoC (System on a Chip) development.</li><li>- Performs Logic design for integration of cell libraries, functional units, and sub-systems into SoC full chip designs. Register Transfer Level coding. and simulation for SoCs</li><li>- Contributes to the development of multidimensional designs involving the layout of complex integrated circuits</li><li>- Will be working on aspects of the SoC design flow from high-level design to synthesis, place and route, timing, and power to create a design database that is ready for manufacturing.</li><li>- Initiates, guides, and coordinates overall design and development of new ideas and products</li><li>- He/She should be willing to ramp up quickly on required areas like RTL, Verification/Validation, Structural Design / Physical Design, Analog Firmware, Embedded etc</li></ul>
<b>Minimum No. of Hires:</b>	5
<b>Expected No. of Hires:</b>	5
<b>Location(s)/Place of Posting/Online:</b>	Bangalore / Hyderabad
<b>Skillset:</b>	-
<b>Minimum CGPA:</b>	7
<b>Students with backlog eligible:</b>	No

## Selection Process

<b>Resume Shortlist:</b>	Yes
<b>Mode of Selection:</b>	Virtual
<b>Resume shortlisting before test?:</b>	No
<b>Test:</b>	No
<b>Group Discussion:</b>	No
<b>Personal Interview:</b>	No
<b>Technical Round:</b>	Yes
<b>HR Round:</b>	No
<b>Medical Test:</b>	No

## Eligible Academic Programs

<b>Diversity Recruiting:</b>	No
<b>Eligible Years:</b>	Graduating in 2026 (Pre-Final Year Students) - B.Tech / Dual / Master's
<b>Eligible Departments:</b>	B.Tech in Computer Science & Engineering, B.Tech in Electrical Engineering, B.Tech in Electrical Engineering (Power and Automation), B.Tech in Engineering Physics, B.Tech and M.Tech in Computer Science & Engineering

## Stipend Details

<b>Stipend (per month) (In INR Per Month):</b>	40,000 INR Per Month
<b>Accommodation:</b>	No