

# Internship Notification Form, IIT Delhi

## About Organisation

<b>Name of Company:</b>	APT Research Pvt Ltd
<b>Date of Establishment:</b>	2009-11-06
<b>Number of Employees:</b>	130
<b>Social Media Page Link:</b>	<a href="https://www.linkedin.com/company/a.p.t.-portfolio-pvt-ltd/about/">https://www.linkedin.com/company/a.p.t.-portfolio-pvt-ltd/about/</a>
<b>Website:</b>	<a href="https://aptportfolio.com/">https://aptportfolio.com/</a>
<b>Type of Organization:</b>	Private
<b>Nature of Business:</b>	Finance & Consulting

## Internship Profile

<b>Job Title:</b>	Software Engineer - Core Engineering
<b>Job Description:</b>	<p>=&gt; Developing low latency C++ software for electronic trading in the most competitive financial markets. Writing efficient code by choosing the best algorithms and data structures, and also developing and leveraging knowledge of x86 processor architecture and FPGA to optimize code for specific execution environments.</p> <p>=&gt; Co-designing and developing low latency execution algorithms with traders and FPGA developers.</p> <p>=&gt; Design and development of our in-house machine learning platform, covering all aspects like data validation and integration, feature and model development etc.</p> <p>=&gt; Scaling and optimizing the platform for faster learning as well as prediction using selected models.</p> <p>=&gt; Reviewing research papers and experimenting with new features and modeling techniques</p> <p>=&gt; Implementing application-level network protocols in C++ to connect to new trading venues. Developing a deep understanding of the nuances in each such protocol and optimizing code to gain advantage from these.</p> <p>=&gt; Investigating latencies and jitter from test and production data and fixing identified issues. Designing experiments to root cause such issues without disrupting production environments.</p> <p>=&gt; Improving our big data infrastructure which is used for market simulations and post-trade analysis. Enhancing capabilities of our statistical learning and market-simulation logic which models a real market, including all its variances, as accurately as possible.</p>
<b>Minimum No. of Hires:</b>	2
<b>Expected No. of Hires:</b>	4
<b>Location(s)/Place of Posting/Online:</b>	Delhi

<b>Skillset:</b>	Software Engineering
<b>Minimum CGPA:</b>	8
<b>Students with backlog eligible:</b>	No

## Selection Process

<b>Resume Shortlist:</b>	Yes
<b>Mode of Selection:</b>	Virtual
<b>Resume shortlisting before test?:</b>	Yes
<b>Test:</b>	Yes
<b>Mode of Test:</b>	Online
<b>Test duration (minutes):</b>	120
<b>Aptitude/Psycometric:</b>	No
<b>Technical:</b>	Yes
<b>Group Discussion:</b>	No
<b>Personal Interview:</b>	Yes
<b>Technical Round:</b>	Yes
<b>HR Round:</b>	Yes
<b>Medical Test:</b>	No

## Eligible Academic Programs

<b>Diversity Recruiting:</b>	No
<b>Eligible Years:</b>	Graduating in 2026 (Pre-Final Year Students) - B.Tech / Dual / Master's
<b>Eligible Departments:</b>	B.Tech in Computer Science & Engineering, B.Tech in Electrical Engineering, B.Tech in Electrical Engineering (Power and Automation), B.Tech in Engineering Physics, B.Tech in Mathematics & Computing, B.Tech and M.Tech in Computer Science & Engineering, M.Sc in Mathematics, M.Sc in Physics

## Stipend Details

<b>Stipend (per month) (In INR Per Month):</b>	250,000 INR Per Month
<b>Accommodation:</b>	Yes

Accommodation: Yes

Any other perks/ benefits/ components: Accommodation, round-trip flight tickets to and from campus, health insurance

Provision of PPO based on performance? Yes

Tentative CTC for PPO select: 4,600,000 INR Per Annum