

PAVITHRA

GRADUATE ENGINEER

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SUMMARY

Enthusiastic Embedded Systems trainee with a deep interest in building smart, efficient, and reliable embedded solutions. Alongside my technical journey, I have developed strong skills in technical writing simplifying complex concepts into clear, engaging content that connects technology with people.

EDUCATION

Year	Institution	Details
2021 - 2025	MEPCO SCHLENK ENGINEERING COLLEGE, SIVAKASI	Bachelors in Electronics and Communication Engineering CGPA – 6.61 – First Class
2020 - 2021	SNM MATRIC HR SEC SCHOOL, SIVASASI	HSC – 85.68%
2018 - 2019	SNM MATRIC HR SEC SCHOOL, SIVAKASI	SSLC – 83.4%

PROFESSIONAL COURSE

YEAR	INSTITUTE	COURSE NAME
2025 - Now	Emertxe	Embedded Systems

CERTIFICATIONS

YEAR	CERTIFICATION
JAN - APR 2024	NPTEL -Introduction to Industry 4.0 and Industrial IoT – 73%
JUL - OCT 2023	NPTEL - Introduction to IoT – 76%
JAN - APR 2023	NPTEL - The Joy of Computing using Python – 60%

INTERSHIP

AUG – NOV 2021	Content Writer at GAOTek.inc In this internship I gain practical experience in Technical Writing, Content writing, Product Sourcing, Reviewing the file of my team members as Squad Leader. As a team lead, I also developed strong leadership, coordination, and communication skills.
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PROJECTS

FINAL YEAR PROJECT - Design of approximate adder architectures for image blending using Verilog.
ADDRESSBOOK IN C -

MINI PROJECTS

Design of Aperture Based Patch Antenna for Sub 6 GHz Application Using Ansys

Automatic Railway Gate Control Using 8051 Microprocessor

Blood Bank Management Using Data Structure and Algorithm in C

Digital Pulse Counter

TECHNICAL SKILLS

●C ●C++ ●Python ●Micro controllers ●Linux Kernal ●MS office

LANGUAGES KNOWN

●English – Professional ● Tamil – Native ●Telugu – Speakable ●Hindi – Understandable

SOFT SKILLS

●Clear Communication ● Time management ●Team coordination