

Key:

CI: O. IMF, 20V ceromil (recommended)

CZ: O. IMF, 20V ceromic (required)

RI: 10k Ol (recommended)

RZ: 100 Db - 470 Db (recommend)

RZ: 100 Db - 470 Db (required)

```
Initialization (only runs once When the code starts)

- configure TRISA, ANSEA and LATA registers

- configure RAT pin to high

While (1) {

- create counter

- ise bit rise AND to taggle the corresponding pins to go high
```

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