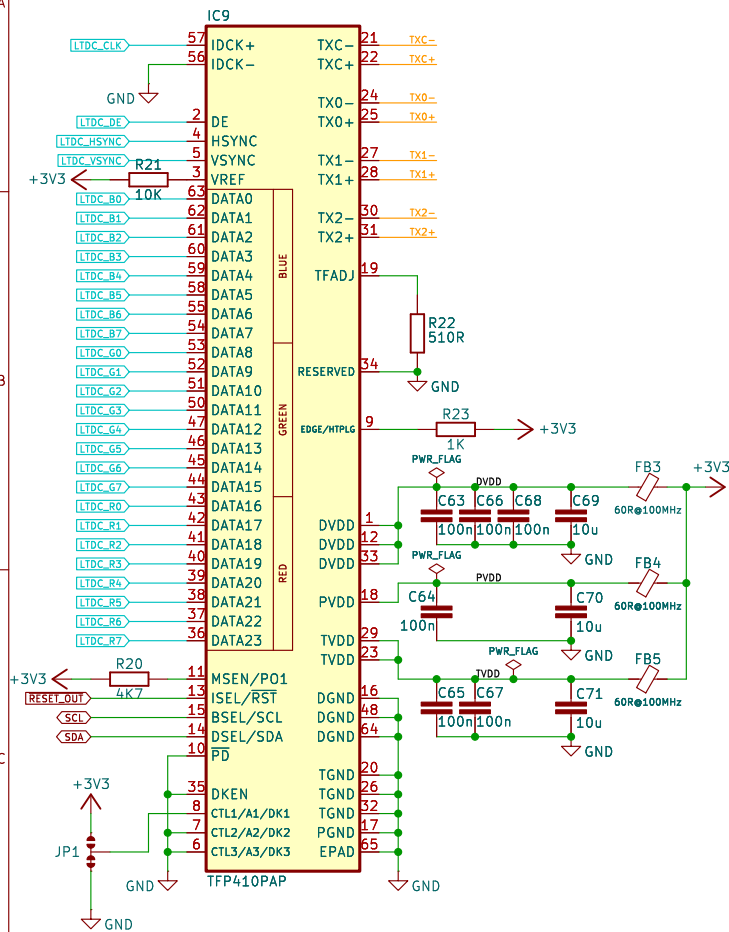


# RGB-to-DVID converter



# Video MUX 1:2

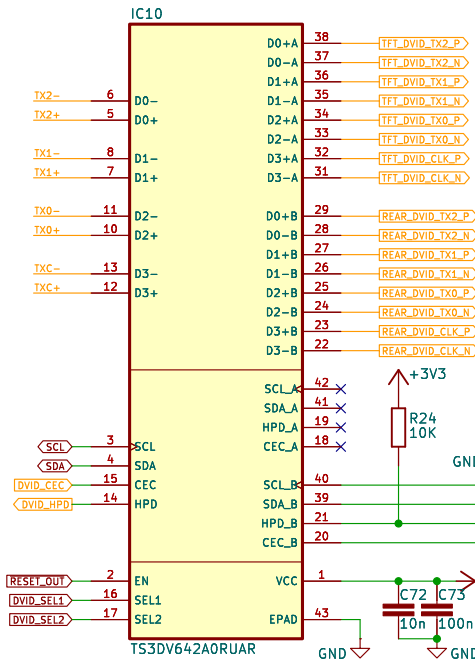
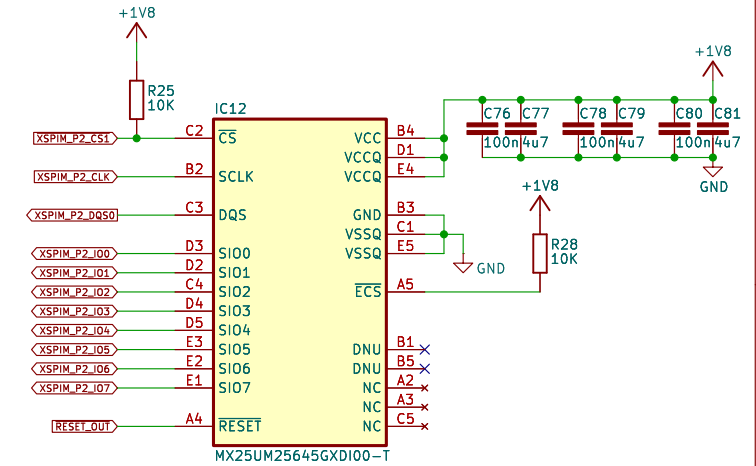


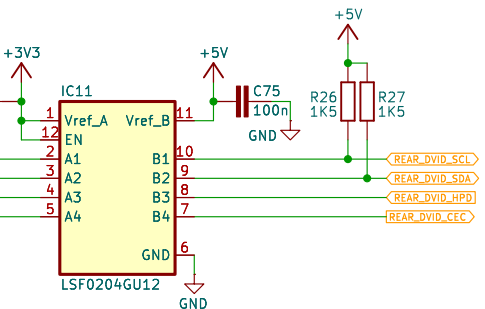
Table 8-1. Functional Table

EN	SEL1	SEL2	FUNCTION
L	X	X	Switch disabled. All channels are H-Z.
H	L	L	Channel D0+D0- to D0+D0- A is ON. All the other channels (D1+D1-, D2+D2-, D3+D3-, SCL, SDA, HPD, CEC) are H-Z.
H	L	H	Channel D0+D0- to D0+D0- B is ON. All the other channels (D1+D1-, D2+D2-, D3+D3-, SCL, SDA, HPD, CEC) are H-Z.
H	H	L	All A channels are enabled. All B channels are H-Z.
H	H	H	All B channels are enabled. All A channels are H-Z.

# NOR flash memory 256 Mbit (32 MB x 8, 200 MHz)



OCTOSPI Impedance Constraint: Min = 45.00, Max = 55.00  
DQ50 with 100.7 (Byte0), and CLK, all in 50 mils  
NCS length can be in the range of up to CLK length +/- 750 mils



<https://www.envox.eu>  
32-bit STM32 MCU module  
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Envox d.o.o.

Sheet: /DVID converter, NOR Flash/  
File: Ethernet\_flash.kicad\_sch

Title: EEZ BB3+ STM32H7 CM5 module

Size: A4 Date: 2026-01-17

KiCad E.D.A. 9.0.7

Rev: r1B3

Id: 4/5