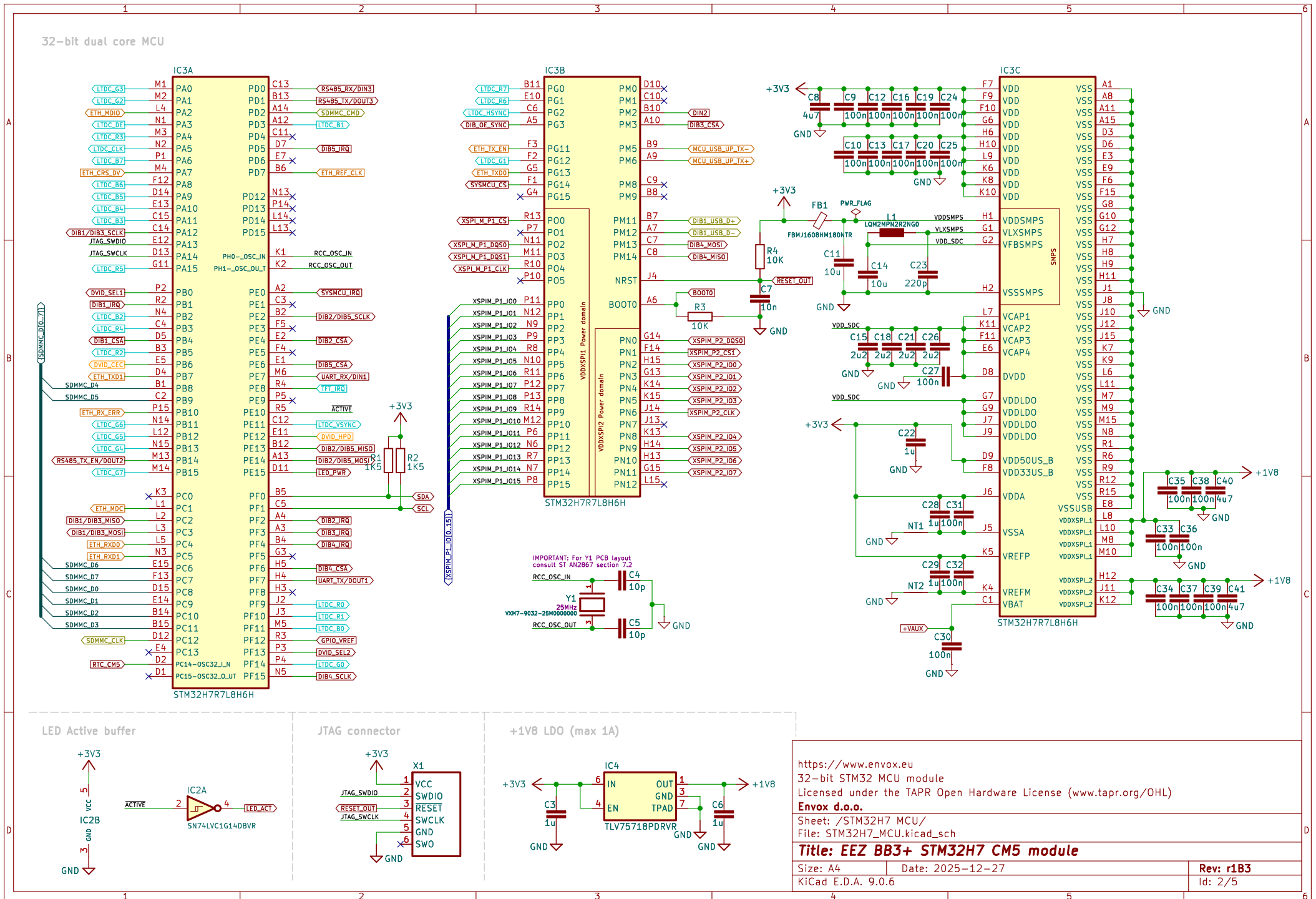
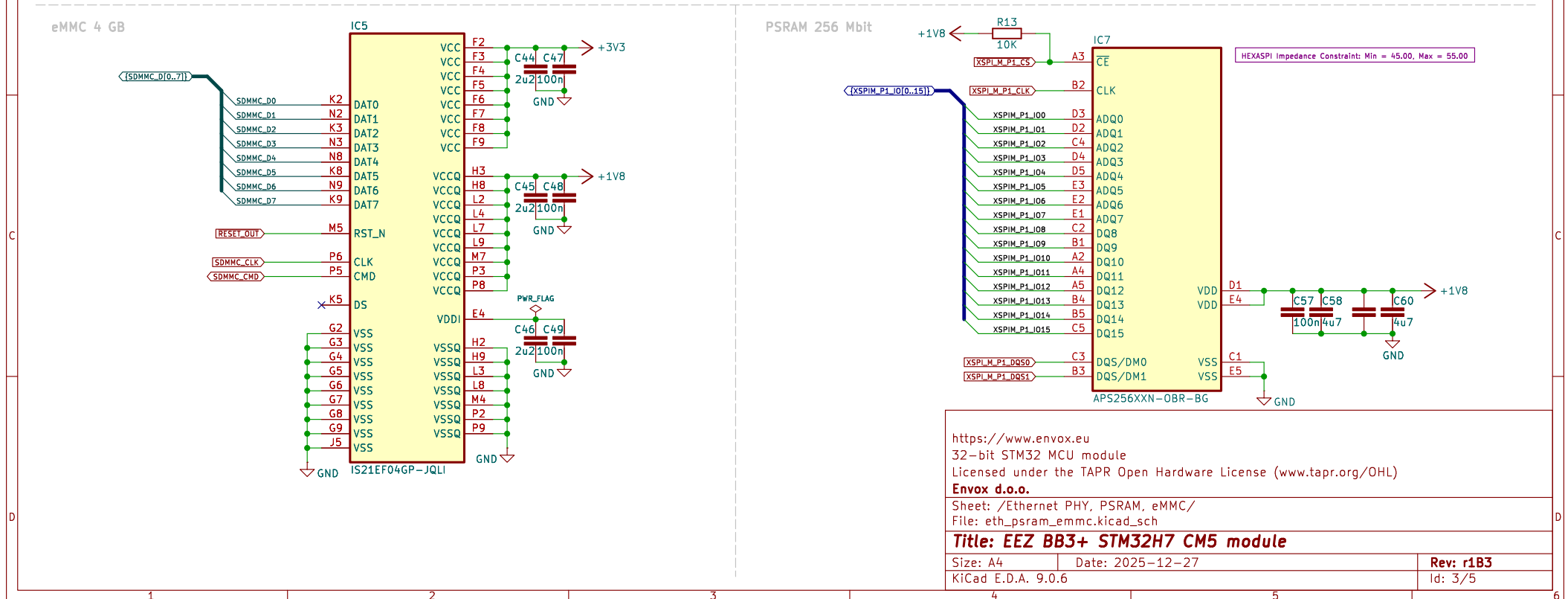
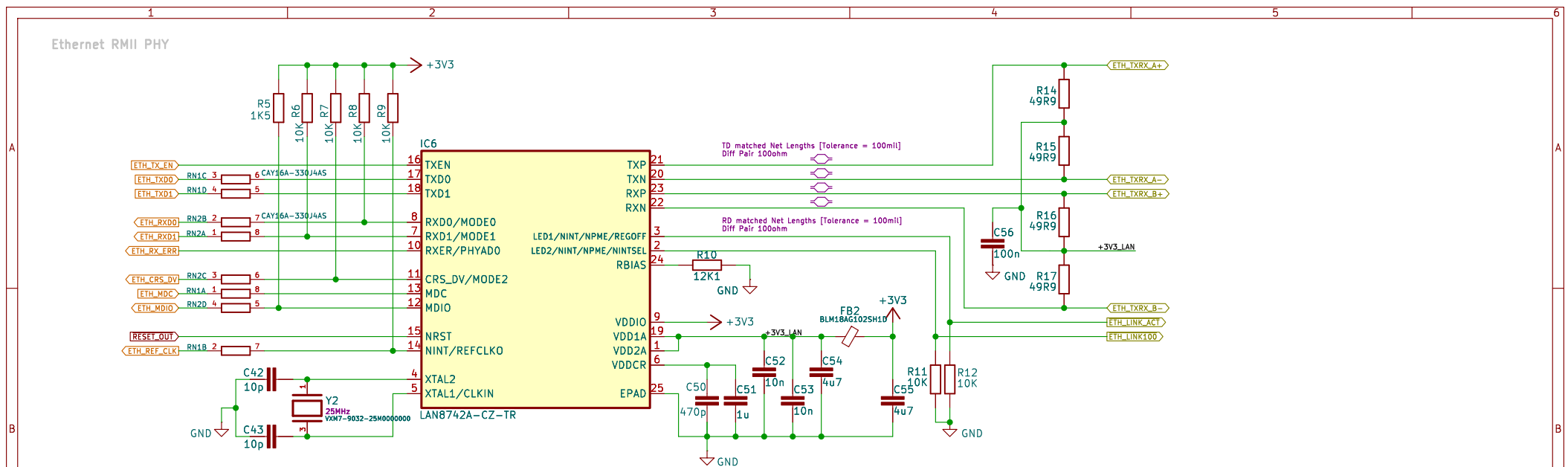


32-bit dual core MCU





PSRAM 256 Mbit

HEXASPI Impedance Constraint: Min = 45.00, Max = 55.00

<https://www.envox.eu>
32-bit STM32 MCU module
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Sheet: /Ethernet PHY, PSRAM, eMMC/
File: eth_psram_emmc.kicad_sch

Title: EEZ BB3+ STM32H7 CM5 module

Size: A4	Date: 2025-12-27
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KiCad E.D.A. 9.0.6

Rev: r1B3

Id: 3/5

