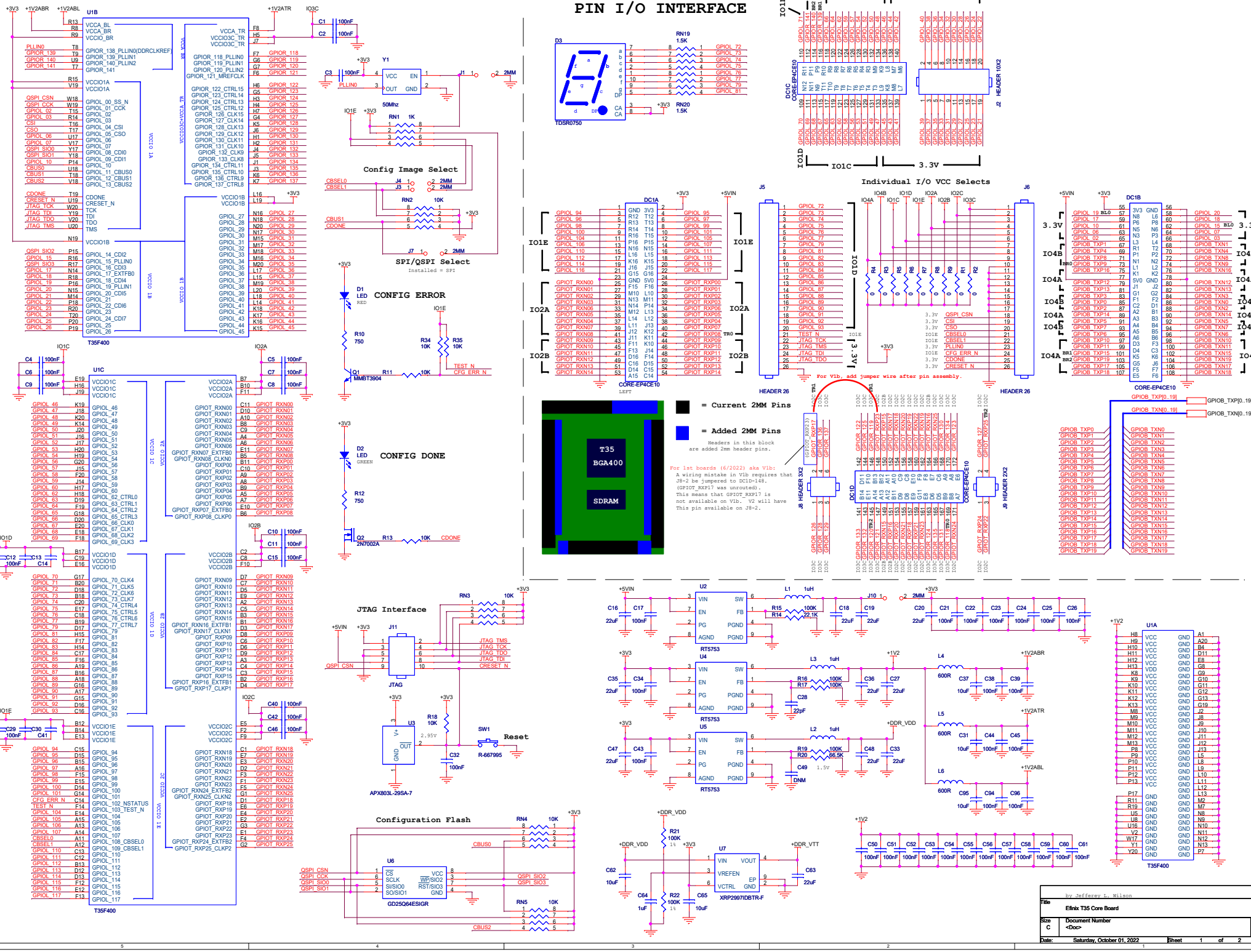
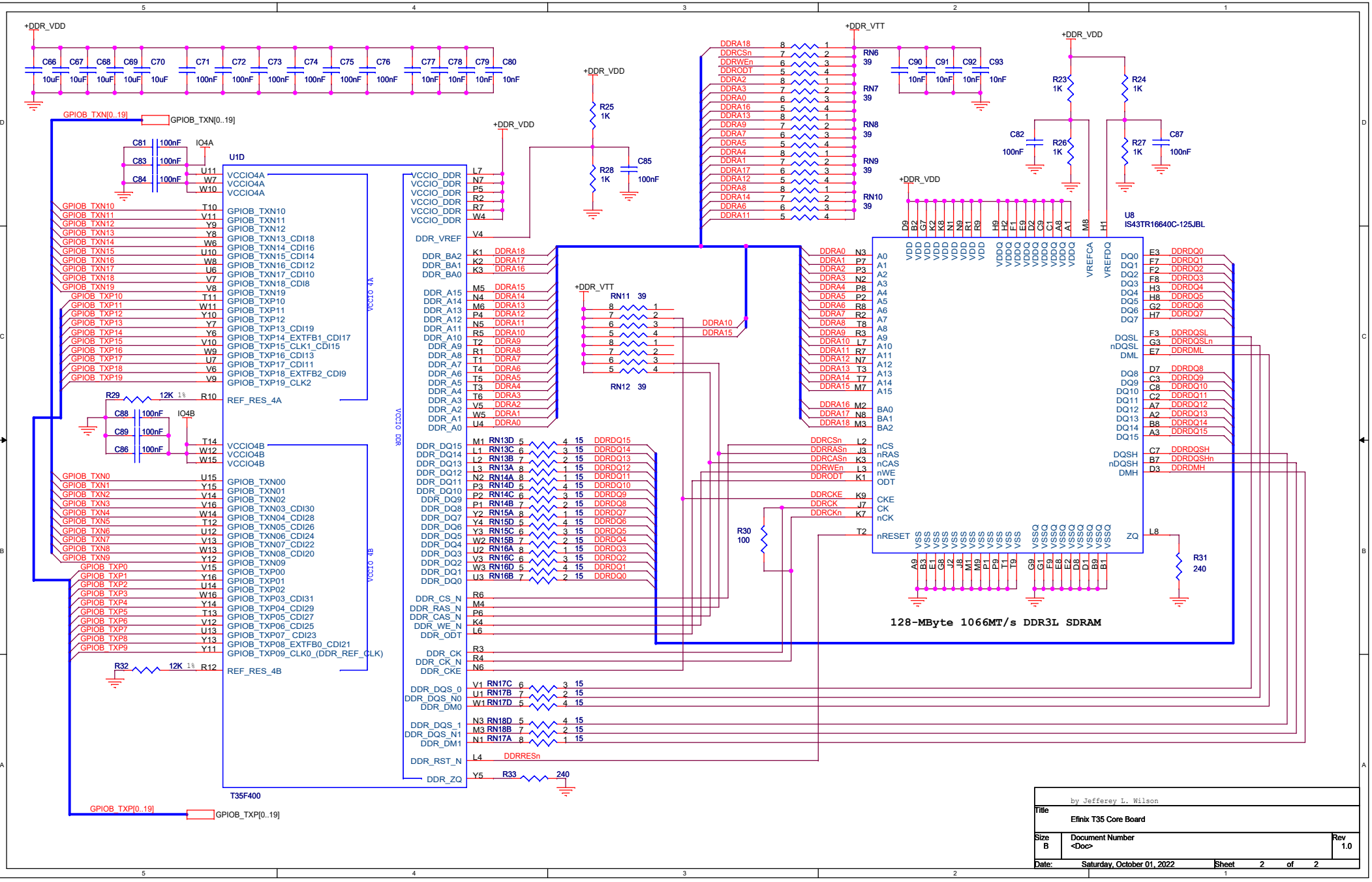
PIN I/O INTERFACE




+DDR_VDD
C66 C67 C68 C69 C70 C71 C72 C73 C74 C75 C76 C77 C78 C79 C80
10uF 10uF 10uF 10uF 10uF 100nF 100nF 100nF 100nF 100nF 100nF 10nF 10nF 10nF 10nF
GPIOB_TXN[0..19]
GPIOB_TXN[0..19]
C81 C83 C84 100nF
IO4A
U1D
VCCIO4A
VCCIO4A
VCCIO4A
GPIOB_TXN10
GPIOB_TXN11
GPIOB_TXN12
GPIOB_TXN13_CDI18
GPIOB_TXN14_CDI16
GPIOB_TXN15_CDI14
GPIOB_TXN16_CDI12
GPIOB_TXN17_CDI10
GPIOB_TXN18_CDI8
GPIOB_TXN19
GPIOB_TXP10
GPIOB_TXP11
GPIOB_TXP12
GPIOB_TXP13_CDI19
GPIOB_TXP14_EXTFB1_CDI17
GPIOB_TXP15_CLK1_CDI15
GPIOB_TXP16_CDI13
GPIOB_TXP17_CDI11
GPIOB_TXP18_EXTFB2_CDI9
GPIOB_TXP19_CLK2
R29 12K 1%
REF_RES_4A
C88 C89 C86 100nF
IO4B
VCCIO4B
VCCIO4B
VCCIO4B
GPIOB_TXN00
GPIOB_TXN01
GPIOB_TXN02
GPIOB_TXN03_CDI30
GPIOB_TXN04_CDI28
GPIOB_TXN05_CDI26
GPIOB_TXN06_CDI24
GPIOB_TXN07_CDI22
GPIOB_TXN08_CDI20
GPIOB_TXN09
GPIOB_TXP00
GPIOB_TXP01
GPIOB_TXP02
GPIOB_TXP03_CDI31
GPIOB_TXP04_CDI29
GPIOB_TXP05_CDI27
GPIOB_TXP06_CDI25
GPIOB_TXP07_ CDI23
GPIOB_TXP08_EXTFB0_CDI21
GPIOB_TXP09_CLK0_(DDR_REF_CLK)
R32 12K 1%
REF_RES_4B
VCCIO_4A
VCCIO_4B
VCCIO_DDR
VCCIO_DDR
VCCIO_DDR
VCCIO_DDR
VCCIO_DDR
VCCIO_DDR
VCCIO_DDR
DDR_VREF
DDR_BA2
DDR_BA1
DDR_BA0
DDR_A15
DDR_A14
DDR_A13
DDR_A12
DDR_A11
DDR_A10
DDR_A9
DDR_A8
DDR_A7
DDR_A6
DDR_A5
DDR_A4
DDR_A3
DDR_A2
DDR_A1
DDR_A0
DDR_DQ15
DDR_DQ14
DDR_DQ13
DDR_DQ12
DDR_DQ11
DDR_DQ10
DDR_DQ9
DDR_DQ8
DDR_DQ7
DDR_DQ6
DDR_DQ5
DDR_DQ4
DDR_DQ3
DDR_DQ2
DDR_DQ1
DDR_DQ0
DDR_CS_N
DDR_RAS_N
DDR_CAS_N
DDR_WE_N
DDR_ODT
DDR_CK
DDR_CK_N
DDR_CKE
DDR_DQS_0
DDR_DQS_N0
DDR_DM0
DDR_DQS_1
DDR_DQS_N1
DDR_DM1
DDR_RST_N
DDR_ZQ
R33 240
T35F400
GPIOB_TXP[0..19]
GPIOB_TXP[0..19]
+DDR_VDD
R25 1K
R28 1K
C85 100nF
+DDR_VTT
RN11 39
RN12 39
DDRA10
DDRA15
DDRA18
DDRCSn
DDRWEn
DDRODT
DDRA2
DDRA3
DDRA0
DDRA16
DDRA13
DDRA9
DDRA7
DDRA5
DDRA4
DDRA1
DDRA17
DDRA12
DDRA8
DDRA14
DDRA6
DDRA11
RN6 39
RN7 39
RN8 39
RN9 39
RN10 39
C90 C91 C92 C93
10nF 10nF 10nF 10nF
R23 1K
R24 1K
R26 1K
R27 1K
C82 100nF
C87 100nF
U8
IS43TR16640C-125JBL
DDRA0
DDRA1
DDRA2
DDRA3
DDRA4
DDRA5
DDRA6
DDRA7
DDRA8
DDRA9
DDRA10
DDRA11
DDRA12
DDRA13
DDRA14
DDRA15
DDRA16
DDRA17
DDRA18
DDRCSn
DDRRASn
DDRCASn
DDRWEn
DDRODT
DDRCKE
DDRCK
DDRCKn
DDRRESn
R30 100
R31 240
DDRDQ0
DDRDQ1
DDRDQ2
DDRDQ3
DDRDQ4
DDRDQ5
DDRDQ6
DDRDQ7
DDRDQSL
DDRDQSLn
DDRDML
DDRDQ8
DDRDQ9
DDRDQ10
DDRDQ11
DDRDQ12
DDRDQ13
DDRDQ14
DDRDQ15
DDRDQSH
DDRDQSHn
DDRDMH
128-MByte 1066MT/s DDR3L SDRAM
by Jefferey L. Wilson
Title
Efinix T35 Core Board
Size B
Document Number
<Doc>
Rev 1.0
Date: Saturday, October 01, 2022
Sheet 2 of 2