

In board directory.  
The "base" devicetree.  
Includes SoC `.dtsi` file,  
may include other files.

&lt;BOARD&gt;.dts

Devicetree overlays:  
optional DTS format  
files which override  
<BOARD>.dts.

```
FILE_1.overlay
...
FILE n.overlay
```

zephyr.dts.p

Intermediate output in  
build/zephyr/. C pre-  
processor output of  
including <BOARD>  
.dts and overlays.

Devicetree bindings:  
contain rules for  
devicetree's contents.

```
BINDING_1.yaml
...
BINDING n.yaml
```

Devicetree  
scripts in  
scripts/dts/