

The diagram shows a 5V LED driver circuit. It starts with a POWER\_JACK (J1) connected to a fuse (F1, 1206L075/16). The circuit then passes through a 7805 voltage regulator (U1) with a 10uF capacitor (C1) on its input. The output of the regulator is connected to a 220E resistor (R3) and another 10uF capacitor (C2), which then drives an LED (LED2, Red). The ground connection is labeled GND.

The diagram illustrates the connection of the RESET pin to the JP5 header. The RESET pin is connected to pin 1 of JP5. The diagram also shows the pin configurations for JP1, JP2, JP3, and JP4 headers.

Header	Pin	Signal
JP1	1	A0
	2	A1
	3	A2
	4	A3
	5	A4
	6	A5
JP2	1	VCC
	2	GND
	3	DTR
	4	RX
	5	TX
	6	D2
	7	D3
	8	D4
	9	VCC
	10	GND
JP3	1	D9
	2	D10
	3	D11
	4	D12
	5	D13
JP4	1	D5
	2	D6
	3	D7
	4	D8