|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| Control TABLE | **RT/RD**  **INTO RD** | **Reg.Write**  **(RW)** | **R/I**  **Select**  **ALU** | **ALU**  **OP** | **lw** | **sw** | **Result select from**  **ALU/**  **Memory** |
| **AND**  **0000** | 0 | 1 | 0 | 00 | 0 | 0 | 0 |
| **OR**  **0001** | 0 | 1 | 0 | 01 | 0 | 0 | 0 |
| **sub**  **0010** | 0 | 1 | 0 | 10 | 0 | 0 | 0 |
| **add**  **0011** | 0 | 1 | 0 | 11 | 0 | 0 | 0 |
| **addi**  **0100** | 1 | 1 | 1 | 11 | 0 | 0 | 0 |
| **lw**  **0101** | 1 | 1 | 1 | 11 | 1 | 0 | 1 |
| **Sw**  **0110** | 1 | 0 | 1 | 11 | 0 | 1 | 0 |