			. AFO	AF1	AF2	AF3	ΔF4	ΔFS	AF6	ΔΕ7	ΔFR	ΔF9	AF10	AF11	AF12	AF13			
			- AHU	AFI	AFZ	AF3	AF4	AFS	Are	AF/	AF8 90	1/1	AF1U	AFII	AF12	AF13	AF14	AF15	
	lon		A							2/3	1/1/5/	13/14	z,		2,68				
	Aain Function	Je	tion al			170		4/5/6	9	RT 1/2	JART4	IM12,	VOTG1		0/OTG				
	in Fi	head	Fine		5	3/4/5	82	17/3/	8/8/	88	RT6/L	7/2/1	2, HS,		olds/:	=			
2	Σ	ш.	Pin Own	£	ME .	<u> </u>	52	iids	28	SPI3	SA	₹ 8	OTG	ETH_MII_RX_CLK/	FIN	ò	5	EVENTOUT	3
1 PA	1	P1 :	11 -	TIM2_CH2	TIMS_CH2				-	USART2_RTS	UART4_RX			ETH_RMII_REF_CLK	-		-		ADC123_IN1
2 PA	2	P1 :	14	TIM2_CH3	TIM5_CH3	TIM9_CH1				USART2_TX				ETH_MDIO	-			EVENTOUT	ADC123_IN2
B PA	3	P1 :	13	TIM2_CH4	TIM5_CH4	TIM9_CH2				USART2_RX			OTG_HS_ULPI_D0	ETH_MII_COL			LCD_B5	EVENTOUT	ADC123_IN3
4 PA		P2	43 MCO1	TIM1_CH1			I2C3_SCL			USART1_CK			OTG_FS_SOF				LCD_R6	EVENTOUT	
		H									•				-				
5 PA	.15	P2 4	40 JTDI	TIM2_CH1/TIM2_ETR			-	SPI1_NSS	SPI3_NSS/12S3_WS		•	•		•	-	•	-	EVENTOUT	
6 PBI	0	P1 :		TIM1_CH2N	TIM3_CH3	TIM8_CH2N			•			LCD_R3	OTG_HS_ULPI_D1	ETH_MII_RXD2	-	•	•	EVENTOUT	ADC12_IN8
7 PB:	1	P1 :		TIM1_CH3N	TIM3_CH4	TIM8_CH3N	-		-			LCD_R6	OTG_HS_ULPI_D2	ETH_MII_RXD3	-		-	EVENTOUT	ADC12_IN9
B PB:	2	P1 :				-		-	-			-			-		-	EVENTOUT	BOOT ?
9 PB4	4	P2 :	25 NJTRST		TIM3_CH1			SPI1_MISO	SPI3_MISO	I2S3ext_SD		-			-		-	EVENTOUT	
10 PR	5	P2 :	26		TIM3_CH2		I2C1_SMBA	SPI1_MOSI	SPI3_MOSI/ I2S3_SD			CAN2_RX	OTG_HS_ULPI_D7	ETH_PPS_OUT	FMC_SDCKE1	DCMI_D10		EVENTOUT	
11 000		P2 :			TIM4_CH2		I2C1_SDA			USART1_RX					FMC_NL	DCMI_VSYNC		EVENTOUT	
		P2 :								DJAN11_IIX								EVENTOUT	
12 PB:					TIM4_CH3	TIM10_CH1	12C1_SCL		,	*		CAN1_RX		ETH_MII_TXD3 ETH_MII_TX_EN/ ETH_RMII_TX_EN	SDIO_D4		LCD_B6		1
13 PB:	11	P1 :	35 -	TIM2_CH4	-	+	I2C2_SDA	-	-	USART3_RX	-	-		ETH MII TXDO/	-	-	LCD_g5	EVENTOUT	-
14 PB:	12	P1 :	-	TIM1_BKIN	-	-	I2C2_SMBA	SPI2_NSS/12S2_WS	-	USART3_CK		CAN2_RX	OTG_HS_ULPI_D5	ETH RMII TXD0	OTG_HS_ID		-	EVENTOUT	-
15 PB:		P1		TIM1_CH1N		-		SPI2_SCK/ I2S2_CK		USART3_CTS		CAN2_TX	OTG_HS_ULPI_D6	ETH_MII_TXD1/ ETH_RMII_TXD1			-	EVENTOUT	OTG_HS_VBUS
16 PB:	14	P1 :	-	TIM1_CH2N	-	TIM8_CH2N		SPI2_MISO	I2S2ext_SD	USART3_RTS		TIM12_CH1			OTG_HS_DM		-	EVENTOUT	
17 PR	15	P1	39 RTC_REFIN	TIM1_CH3N		TIM8_CH3N		SPI2_MOSI/12S2_SD				TIM12_CH2			OTG_HS_DP			EVENTOUT	
18 PC:		P1 :												ETH_MDC				EVENTOUT	ADC123_IN11
		P1 :																	
19 PC:		-			•		-	SPI2_MISO	I2S2ext_SD		•	•	OTG_HS_ULPI_DIR	ETH_MII_TXD2 ETH_MII_RXD0/ ETH_RMII_RXD0	FMC_SDNE0	•	-	EVENTOUT	ADC123_IN12 ADC12_ IN14
20 PC	4	P1 :	-		-	*	-	-	-		•	-		ETH_RMII_RXD0	-	•	-	EVENTOUT	
21 PC:	5	P1 :	19 -			-	-			-				ETH_MII_RXD1/ ETH_RMII_RXD1	-			EVENTOUT	ADC12_ IN15
22 PC	6	P2 4	47		TIM3_CH1	TIM8_CH1		1252_MCK	-		USART6_TX	-			SDIO_D6	DCMI_D0	LCD_HSYNC	EVENTOUT	
23 PC	8	P2 4	45	_	TIM3_CH3	TIM8_CH3	-		-	_	USART6_CK	-			SDIO_D0	DCMI_D2	-	EVENTOUT	
24 PC!	9	P2 4	46 MCO2		TIM3_CH4	TIM8_CH4	12C3 SDA	I2S CKIN							SDIO D1	DCMI D3		EVENTOUT	
25 PC:		P2 :	20					1253ext_SD	SPI3_MISO	USART3_RX	UART4_RX				SDIO_D3	DCMI_D4		EVENTOUT	
		P2 :						1230KL_3D	3/13_11130	DJAN13_IIX	UNITY_IX				3510_53	DCMI_D4		EVENTOUT	TAMP_1
26 PC:		P2 :	12		-			-	-		*	-		•	-		-		
27 PC:	14	P2 5	9 -		-	*	-	-	-		•	-		•	-	•	-	EVENTOUT	OSC32_IN
28 PC:	15	P2 :	10 -	•		-	-	-	-		•	-			-		-	EVENTOUT	OSC32_OUT
29 PD	0	P2	-		-		-		-	-		CAN1_RX			FMC_D2		-	EVENTOUT	
30 PD	1	P2 :	-		-		-		-	-	-	CAN1_TX			FMC_D3		-	EVENTOUT	
31 PD:	2	P2 :	34		TIM3_ETR			-	-	_	UARTS_RX	-			SDIO_CMD	DCMI_D11	-	EVENTOUT	
32 PD:	13	P2	31					SPI2_SCK/ I2S2_CK		USART2_CTS					FMC CLK	DCMI DS	LCD G7	EVENTOUT	
22 00		P2 :							SAI1_SD_A	USART2_RX					FMC_NWAIT	DCMI_D10	LCD_B2	EVENTOUT	
		P2 :						3 15_W03/125_30	341_30_4							DCWII_D10	cco_oz	EVENTOUT	
34 PD		P2 :			*		*		*	USART2_CK		*			FMC_NE1/ FMC_NCE2				
35 PD:	8			+	<u> </u>	-	-	-	-	USART3_TX		-	-		FMC_D13		-	EVENTOUT	-
36 PD:	19	P1 4	-	-	-	-		-	-	USART3_RX		-			FMC_D14		-	EVENTOUT	-
37 PD		P1 4								USART3_CK					FMC_D15		LCD_B3	EVENTOUT	
38 PD	11	P1 4	-							USART3_CTS					FMC_A16			EVENTOUT	
39 PE	2	P2 :	15 TRACECLK		-	-		SPI4_SCK	SAI1_MCLK_A					ETH_MII_TXD3	FMC_A23			EVENTOUT	
40 PE	4	P2 .	13 TRACED1					SPI4_NSS	SAI1_FS_A						FMC_A20	DCMI_D4	LCD_B0	EVENTOUT	
11 ~		P2 :				TIM9_CH1		SPI4_MISO	SAI1_SCK_A						FMC_A21	DCMI_D6	LCD_G0	EVENTOUT	
1723		P2 :				TIM9_CH2		SPI4_MOSI							FMC_A22		LCD_G1	EVENTOUT	
•4 PE6	ь	P2 :	TRACED3			IIM9_CH2	*	ari4_MUSI	SAI1_SD_A							DCMI_D/	LLU_61		1
43 PE:	7	P1 :	25 -	TIM1_ETR		-		*		-	UART7_Rx		-		FMC_D4			EVENTOUT	
44 PES		P1 :		TIM1_CH1N	-	-		-	-	-	UART7_Tx	-	-		FMC_D5		-	EVENTOUT	
45 PES		P1 :		TIM1_CH1		-		-							FMC_D6		-	EVENTOUT	
46 PE:	10	P1 :	-	TIM1_CH2N	-	-		-							FMC_D7			EVENTOUT	
47 pc	11	P1 :	29	TIM1_CH2				SPI4_NSS							FMC_D8		LCD_G3	EVENTOUT	
48 PE:		P1 :		TIM1_CH3N				SPI4_SCK							FMC_D9		LCD_B4	EVENTOUT	
			~		1														
49 PE:		P1 :	-	TIM1_CH3	*		*	SPI4_MISO	-	*	*	-		*	FMC_D10	*	LCD_DE	EVENTOUT	*
50 PE:				TIM1_CH4	-	-	-	SPI4_MOSI	-	•		-		•	FMC_D11	•	LCD_CLK	EVENTOUT	1
51 PE:		P1 :		TIM1_BKIN	1	L	I.	I-			ŀ	-	-		FMC_D12		LCD_R7	EVENTOUT	