

## 32F429IDISCOVERY Pinout / Free IO

Information taken from STM32F427xx STM32F429xx Datasheet ( DocID024030 Rev 3 ) and UM1670 ( DocID025175 Rev 1 )

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[illegible]

						SDIO_CMD,	
						DCMI_D11,	
						EVENTOUT	
12	PD4	P1		38	118	USART2_RTS,	
						FMC_NOE,	
						EVENTOUT	
13	PD5	P1		37	119	USART2_TX,	
						FMC_NWE,	
						EVENTOUT	
14	PD7	P1		35	123	USART2_CK,	
						FMC_NE1/FMC_NCE2,	
						EVENTOUT	
15	PE2	P1		15	1	TRACECLK,	
						SPI4_SCK,	
						SAI1_MCLK_A,	
						ETH_MII_TXD3,	
						FMC_A23, EVENTOUT	
16	PE3	P1		16	2	TRACED0,	
						SAI1_SD_B, FMC_A19,	
						EVENTOUT	
17	PE4	P1		13	3	TRACED1, SPI4_NSS,	
						SAI1_FS_A, FMC_A20,	
						DCMI_D4, LCD_B0,	
						EVENTOUT	
18	PE5	P1		14	4	TRACED2, TIM9_CH1,	
						SPI4_MISO,	
						SAI1_SCK_A,	
						FMC_A21, DCMI_D6,	
						LCD_G0, EVENTOUT	
19	PE6	P1		11	5	TRACED3, TIM9_CH2,	
						SPI4_MOSI,	
						SAI1_SD_A, FMC_A22,	
						DCMI_D7, LCD_G1,	
						EVENTOUT	
20	PF6		P2	3	18	TIM10_CH1,	ADC3_IN4
						SPI5_NSS,	
						SAI1_SD_B,	
						UART7_Rx,	
						FMC_NIORD,	
						EVENTOUT	
21	PG2		P2	62	87	FMC_A12,	
						EVENTOUT	
22	PG3		P2	61	88	FMC_A13,	
						EVENTOUT	
23	PG9	P1		33	93	SPI6_NSS,	
						USART6_RTS,	
						ETH_PPS_OUT,	
						FMC_SDCLK,	
						EVENTOUT	