

San Jose State University
Department of Computer Engineering

CMPE 140 Lab Report

Assignment 8 Report

Title Pipelined MIPS Processor & I/O Interface

Semester 2017/Fall **Date** 11/14/17

by

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Lab Checkup Record

Week	Performed by (signature)	Checked by (signature)	Tasks Successfully Completed	Tasks Partially Completed*	Tasks Failed or Not Performed*
1					
2					
3					
4					

* Explanation and/or analysis must be given in the report.