

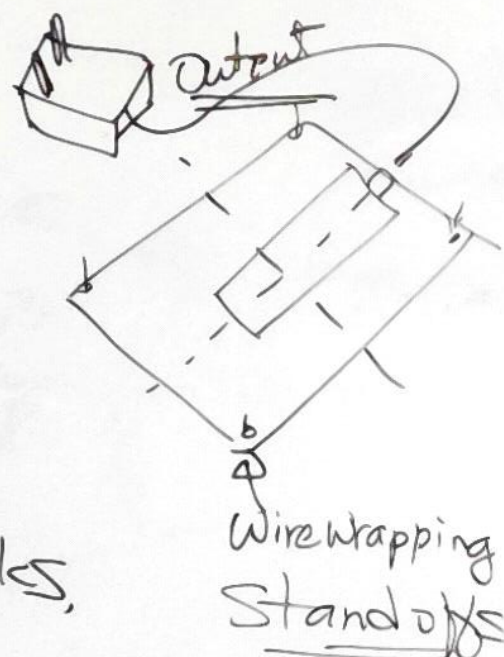
# CMPE127 Microprocessor Systems.

Feb 6th, 2017 I+L.

Quiz: Draw System Block Diagram  
Which Shows (1) CPU Core and  
Jtag unit (2) memory controller Blocks,  
(3) Peripheral Controllers.

Today's Topics: 1° Layout Design → Lab  
2° CPU Architecture / Memory Map → I.D.E. / Lab 1  
Homework: NXP website → AS Developer  
By Next Monday. Xpresso.

Example: Layout Design.



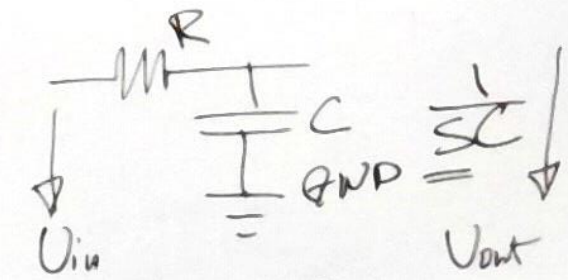
H.S.C.

SMD  
Surface Mount Device

PWR Ckt Design.

Table 1 PWR Ckt

U1	Wall mount PWR Adaptor 7.5 VDC 1500-2000mA
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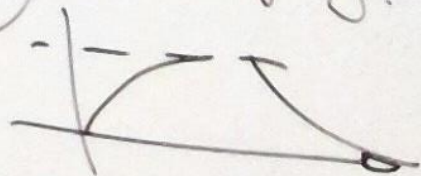


$$U_{out}(S) = \frac{U_{in}}{R + \frac{1}{sC}}$$

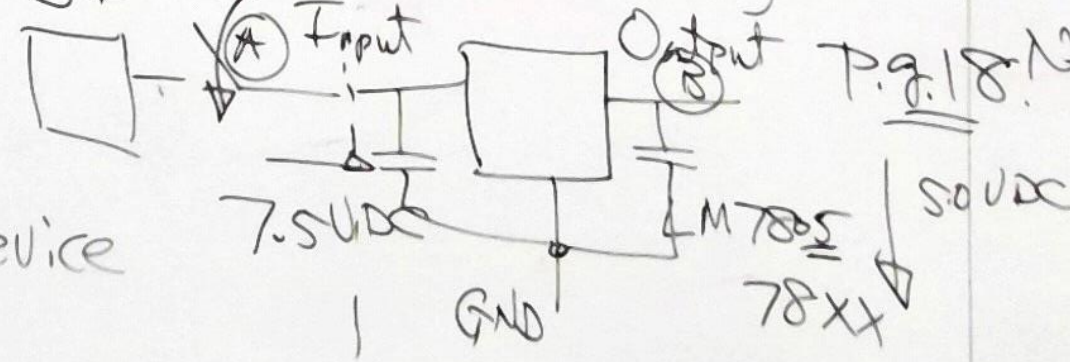
$$= \frac{U_{in}}{1 + sCR} \tau \text{ Time Constant}$$

$$V_{cc}(1 - e^{-\frac{t}{\tau}}) \text{ Charging.}$$

$$V_{cc}e^{-\frac{t}{\tau}} \text{ Discharging.}$$



Central Expwy + Corin  
SW1 (South West Corner)



P.g. 18. Note: 1° Two Caps Needed  
to form L.P.F. TO Remove