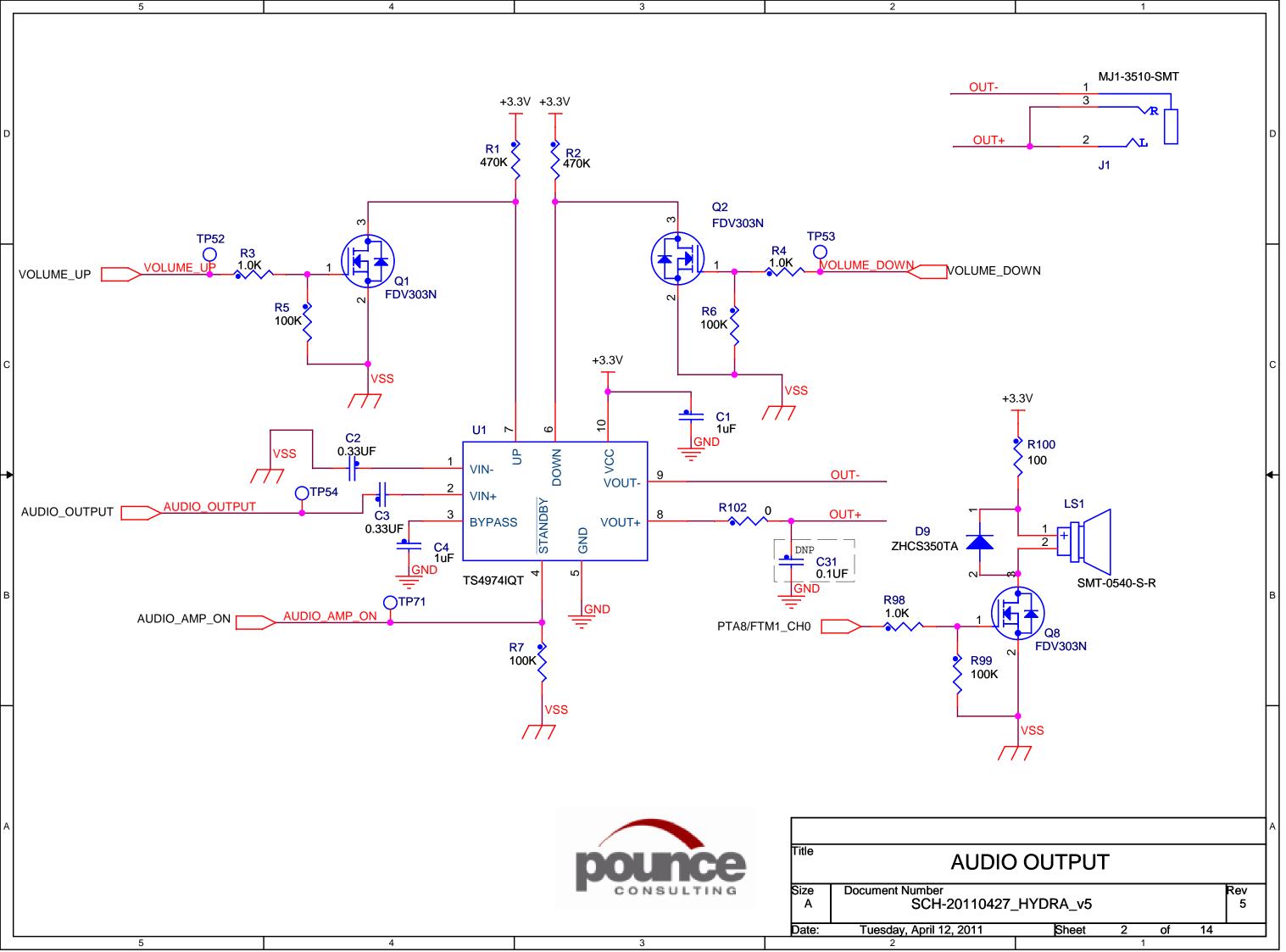
COMMUNICATION HEADERS PTE7/UART_RTS PTE8/UART_TX PTE9/UART_RX PTE10/UART_CTS INFRARED S MICRO SD CARD PTE27/SDCARD_SW_PTE6/SDCARD_ON
PTE3/SDHC0_CMD
PTE2/SDHC0_DCLK 20_D3 20_D2 SDA/SI SCL/SI PTA16/SPI0_SOUT PTA17/SPI0_SIN PTA14/SPI0_PCS0 PTA15/SPI0_SCK COMMUNICATION HEADERS TOUCH SENSING 27/SDCARD_SW_DET PTE6/SDCARD_ON PTE3/SDHC0_CMD PTE2/SDHC0_DCLK PTE26/IR_TX 8888 VCC_USB_JLINK VCC_USB_JLINK PTE4/SDHC0_[PTE5/SDHC0_[SDA/SDHC0_[SCL/SDHC0_[PTA4/TSI0_CH5 PTB0/TSI0_CH0 VCC_USB_K40 USB_DP_K40 USB_DN_K40 VCC_USB_K40 USB_DP_K40 USB_DN_K40 PTA24/ELECT_ PTA25/ELECT_ PTA26/ELECT_ PTA27/ELECT_ PTA24/ELECT_1 PTA25/ELECT_2 PTA26/ELECT_3 PTA27/ELECT_4 PTE0/12C1_ PTE1/12C1_ +5V_USB TWRPI SOCKET AUDIO OUTPUT PTA8 PTA8/FTM1 CH0 PTA13/TWRPI_GPIO3/CNIP2_IN0 PTA10/TWRPI_GPI00 PTA11/TWRPI_GPI01 PTA12/TWRPI_GPI02 AUDIO_OUTPUT AUDIO_AMP_ON AUDIO_OUTPUT AUDIO_AMP_ON PTA10/TWRPI_GPIO0 PTA11/TWRPI_GPIO1 PTA12/TWRPI_GPIO2 PTA0/JTAG_TCK/TSI0_CH1 PTA1/JTAG_TDI/TSI0_CH2 PTA2/JTAG_TDO/TSI0_CH3 PTA3/JTAG_TMS/TSI0_CH4 RESET_TWRPI PTA8
PTA9
PTA9/FTM1_CH0
PTA9/FTM1_CH1 MICROPHONE PTE24/CAN1_TX PTE25/CAN1_RX +5V_U 양된 +5V USB PTA14/SPI0_PCS0 PTA8/FTM1 PTA15/SPI0_SCK PTA16/SPI0_SOUT PTA7/MICROPHONE SIGNAL ■ PTA7/MICROPHONE SIGNAL PTE0/I2C1_SDA/SDHC0_D1
PTE1/I2C1_SCL/SDHC0_D0 MCU J-LINK PTA8/FTM1_CH0 ◆ PTA9/FTM1_CH1 ◆ PTE24/CAN1_TX PTE25/CAN1_RX TCK/TSI0_C TDD/TSI0_C TDO/TSI0_C PTE7/UART_R PTE8/UART_ PTE9/UART_ PTE10/UART_C PTE0/I2C1_SDA/SDHC0_D1 +5V_USB RESET PTA0/JTAG_T PTA1/JTAG_T PTA2/JTAG_T PTA3/JTAG_T PTE1/I2C1_SCL/SDHC0_D0 JTAG_VSS
USB_DN_JLINK
USB_DP_JLINK
USB_DP_JLINK TOWER PORT

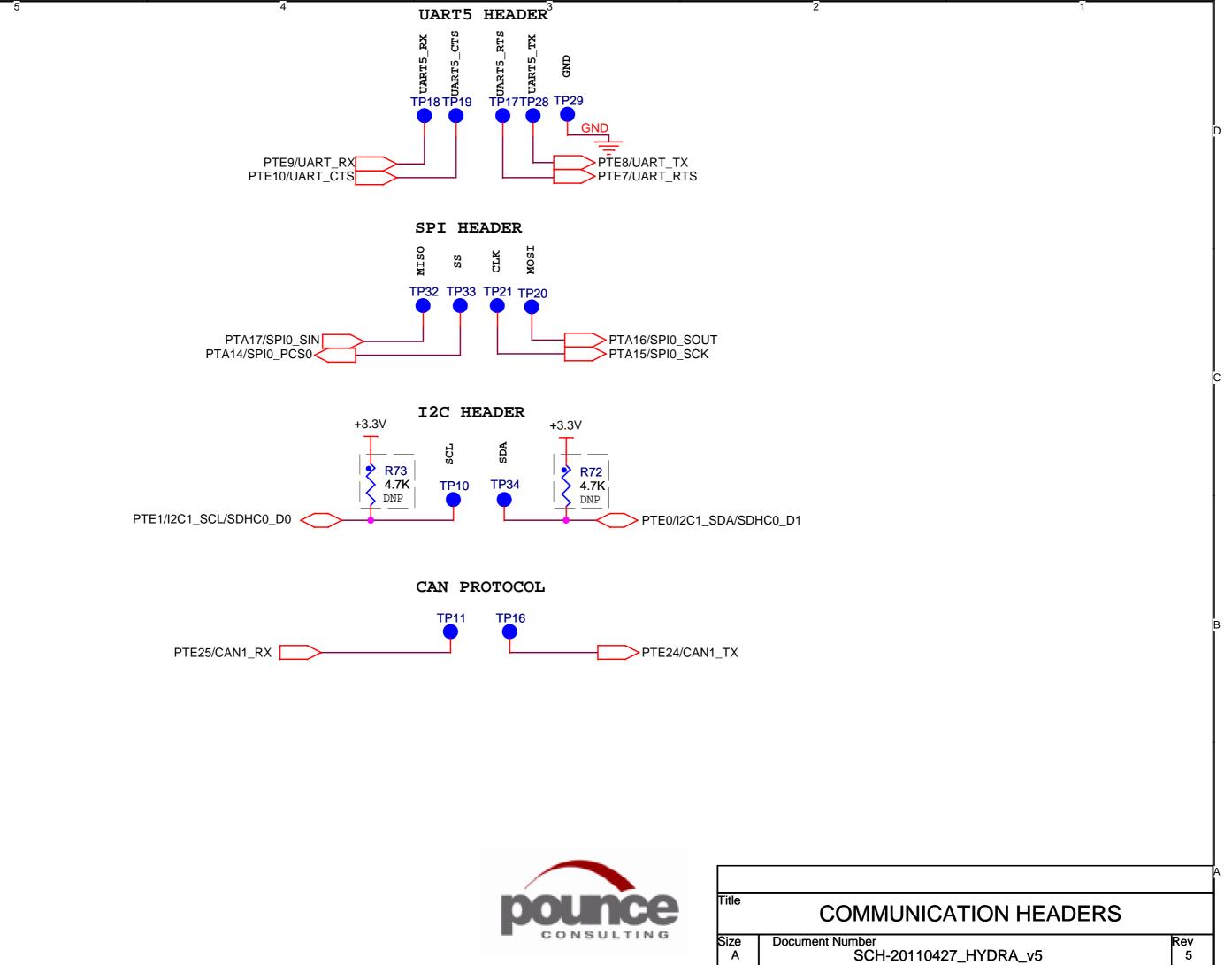
Power & Ground Nets

NET	VOLTAGE	DESCRIPTION
BAT+	3.0V Rated	Primary input power. Discharged Battery positive electrode.
+3.3V	3.3V	Battery voltage in full charge after protection diode (D3) and switch transistor (Q6)
+3.3V	3.3V	Output of voltage regulator using USB power input
+5V_USB	5V	USB power Input
+3.3VDD	3.3V	Digital power. Filtered from +3.3V
VREFH	3.3V	Upper reference voltage for ADC on the MCU. Filtered from +3.3VDD.
+3.3V_SWITCH	3.3V	Input power to switch ON/OFF, output switching regulator
+3.3VDDMCU	3.3V	Main MCU K40 voltage
GND	OV	Digital Ground.
VSS	0V	VSS power for MCU and analog circuits. Filtered from GND.
VREFL	OV	Lower reference voltage for ADC on the MCU. Filtered from VSS.
JTAG_VSS	0V	VSS power for JTAG connector to program external MCU devices, switched by VSS.
EXT_VDD	3.3V	External input power. Only for programming external JTAG devices.
+3.3VDD_JLINK	3.3V	Digital power. Filtered from +3.3V. Power supply J-LINK (JM128)



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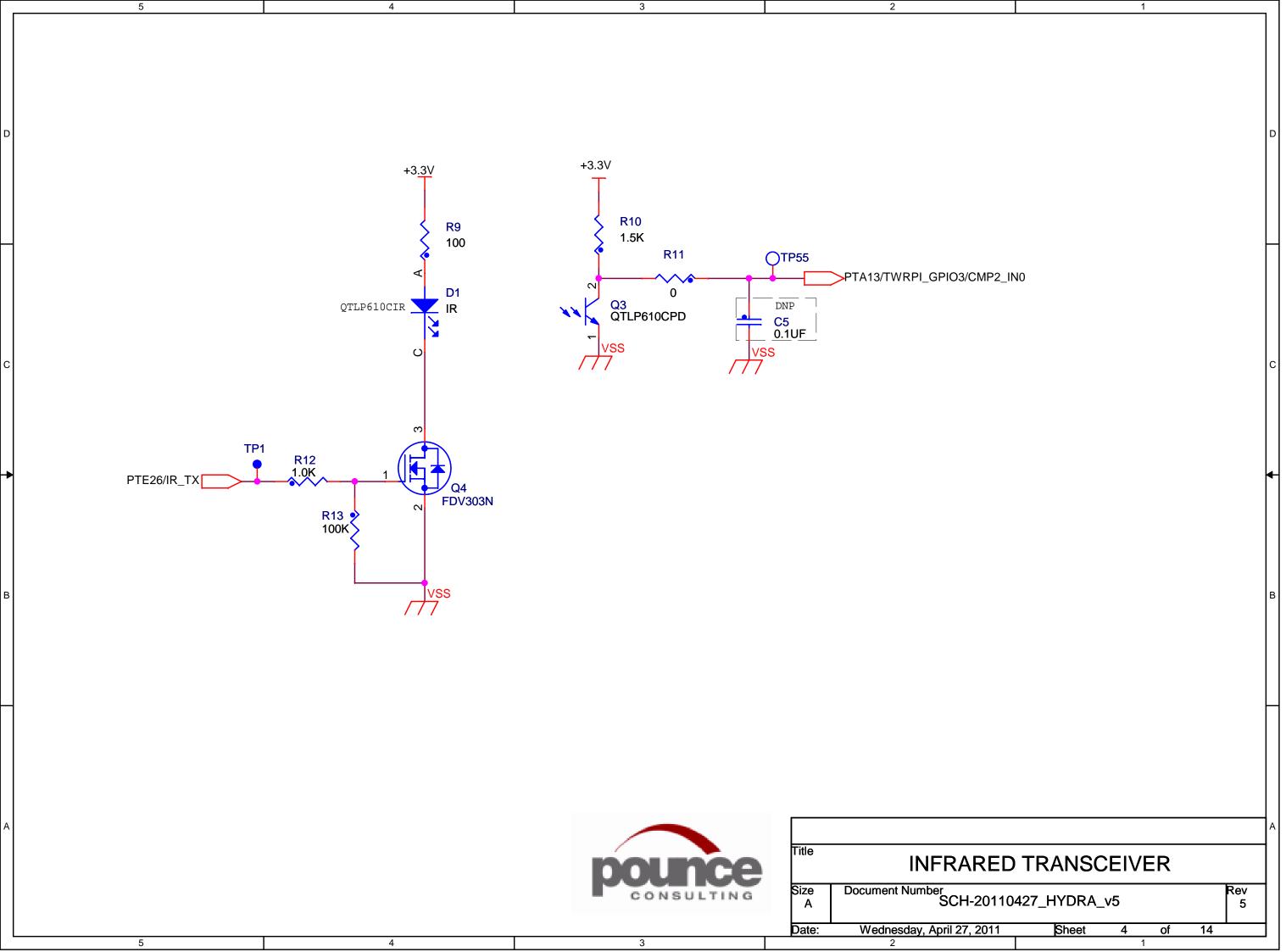
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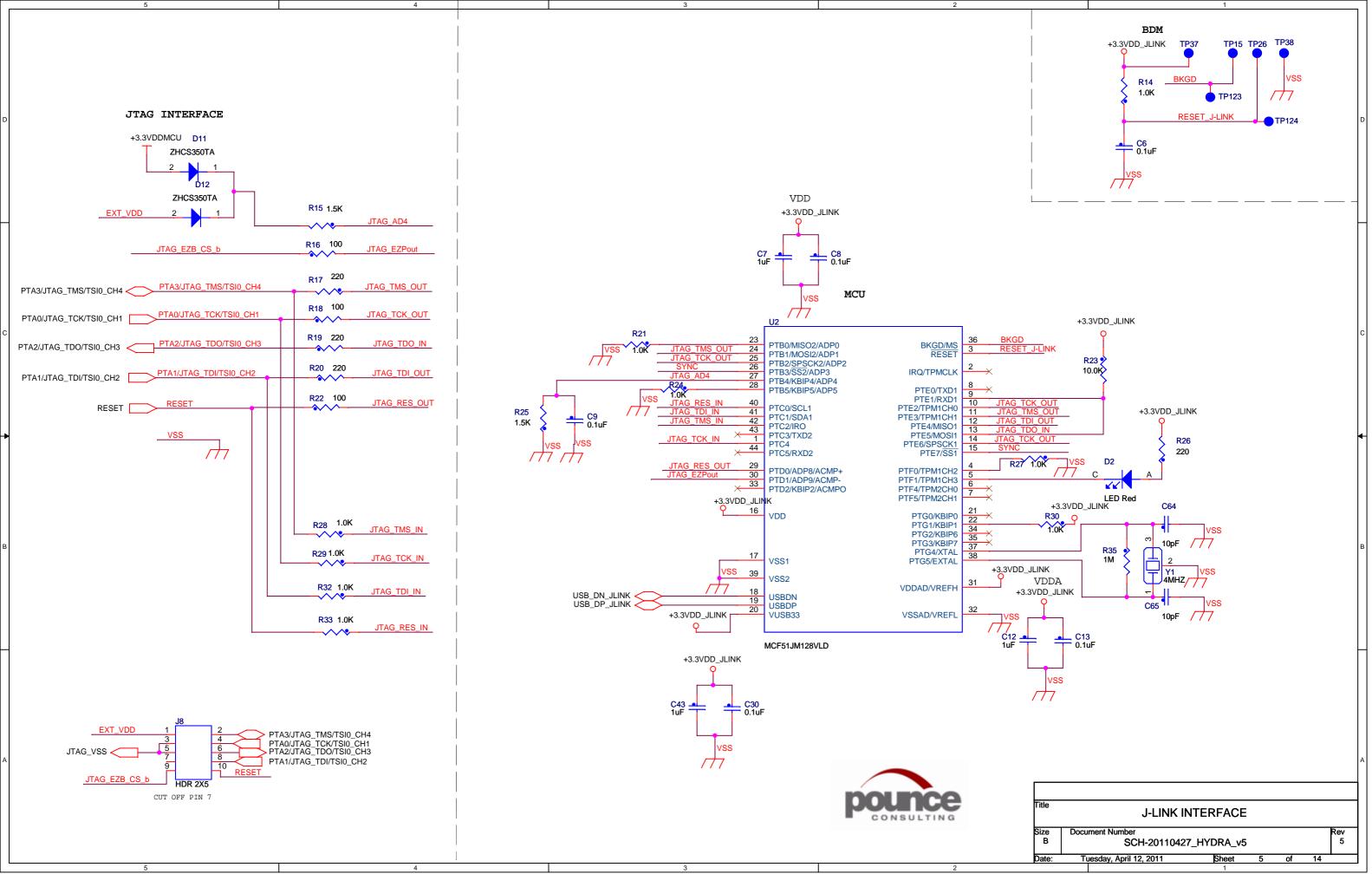
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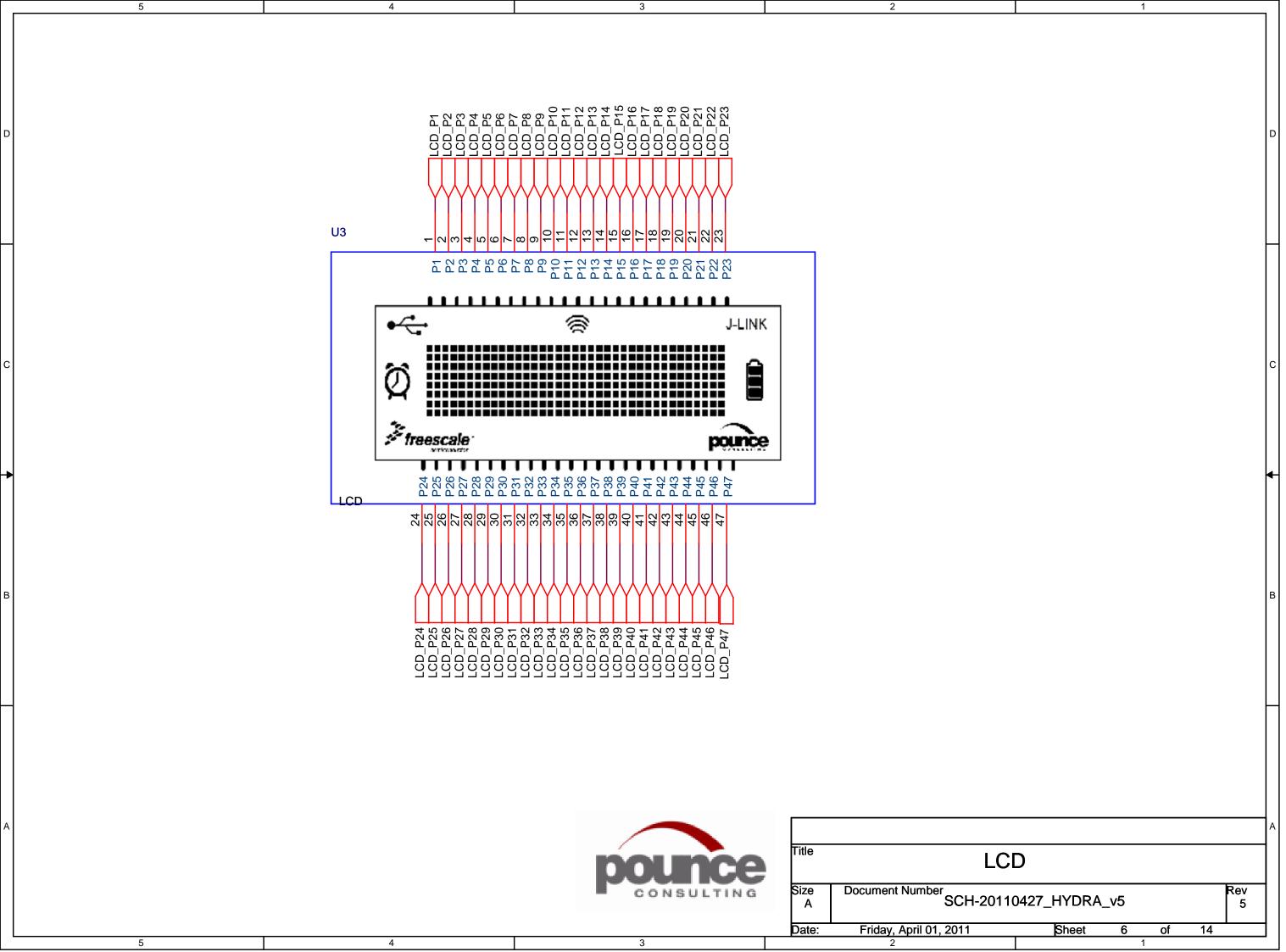
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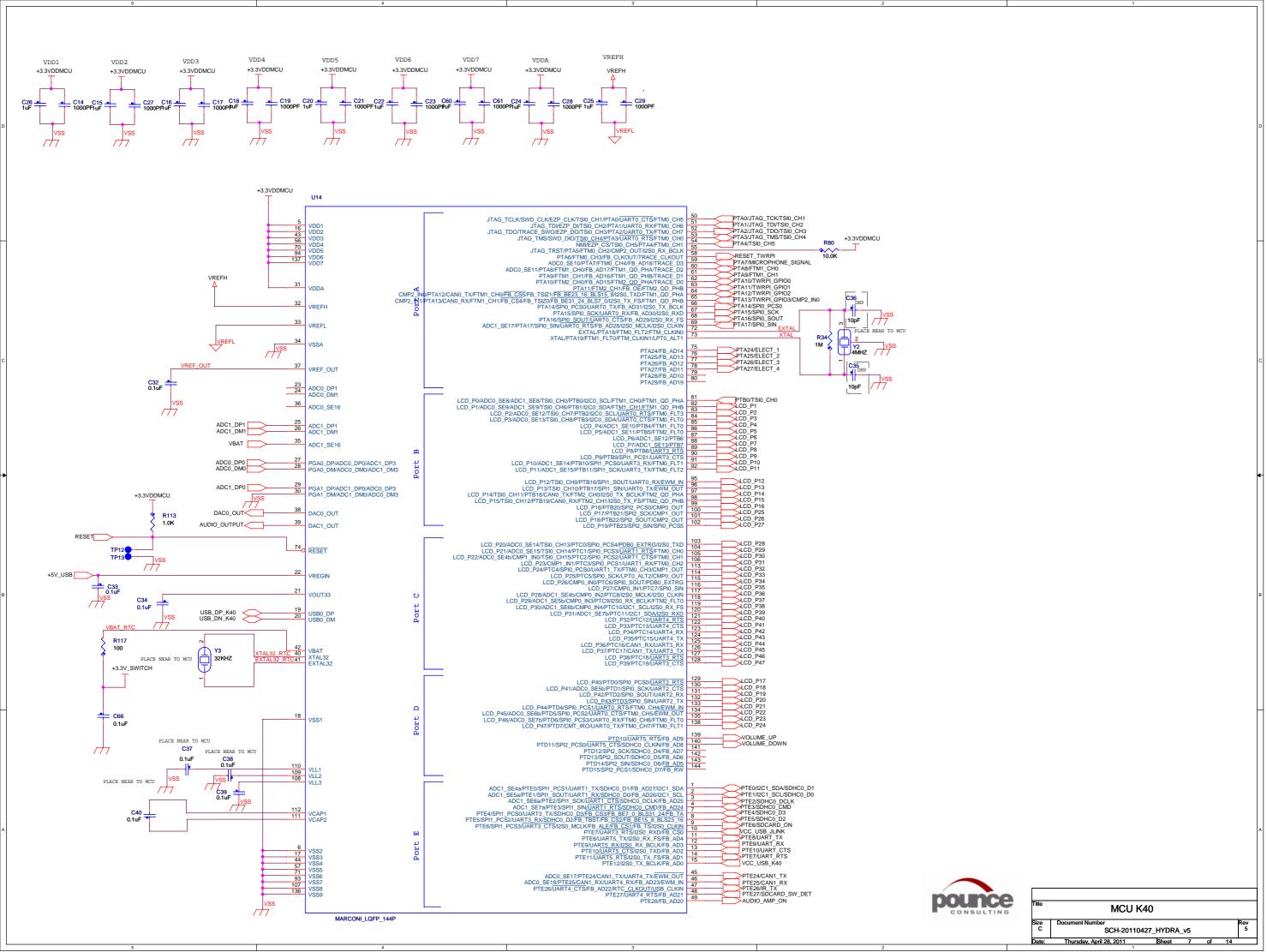
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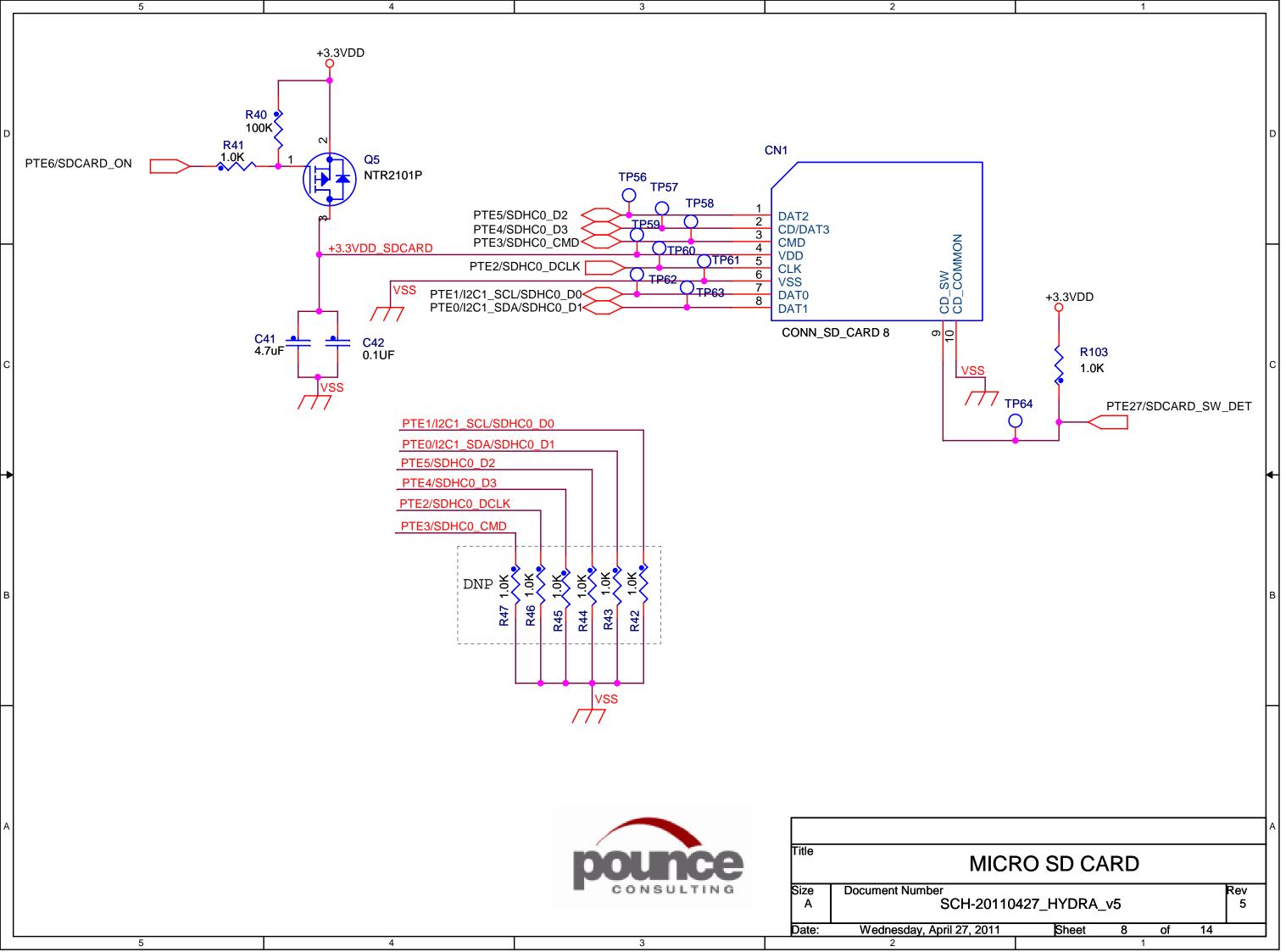
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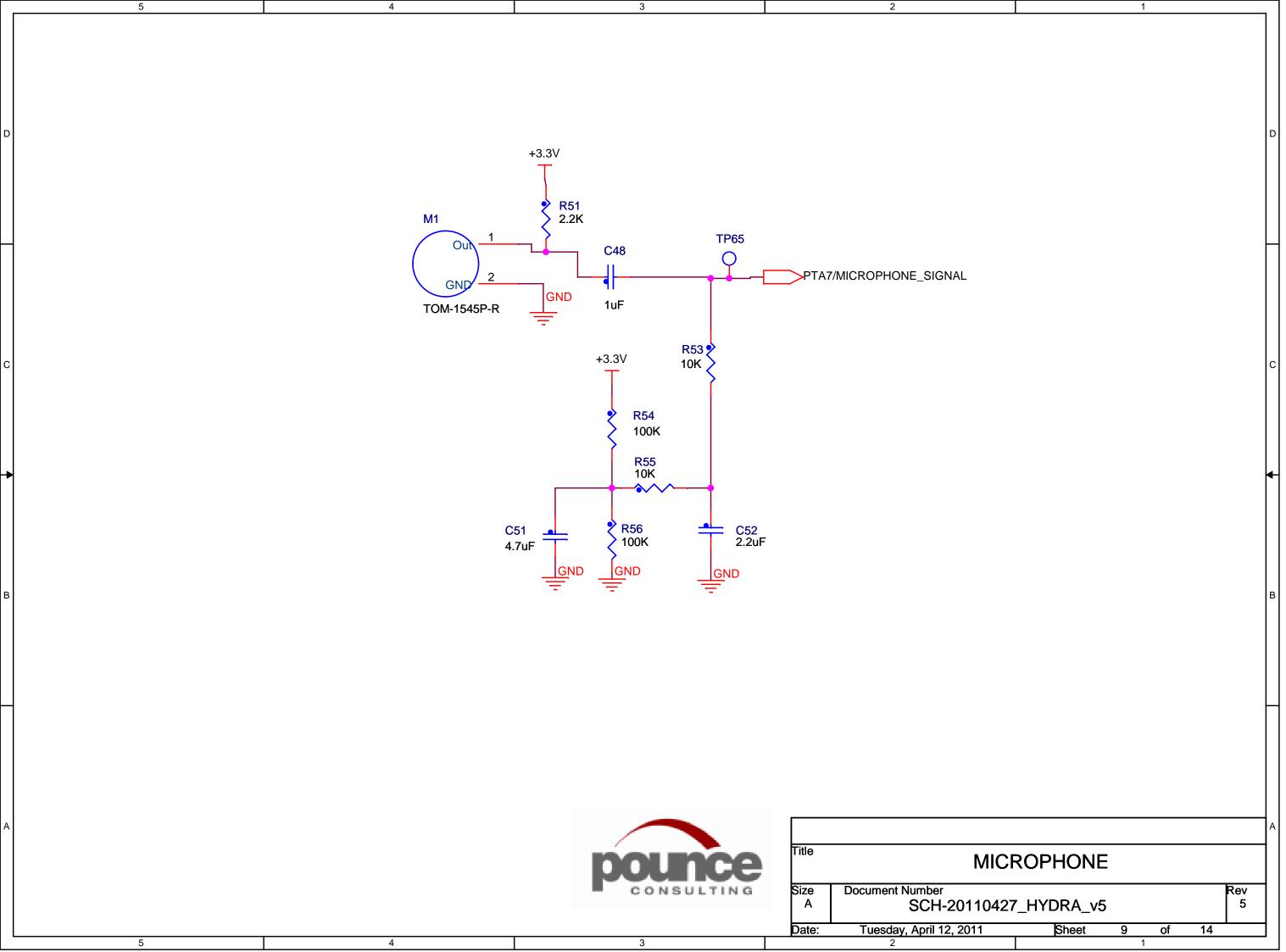


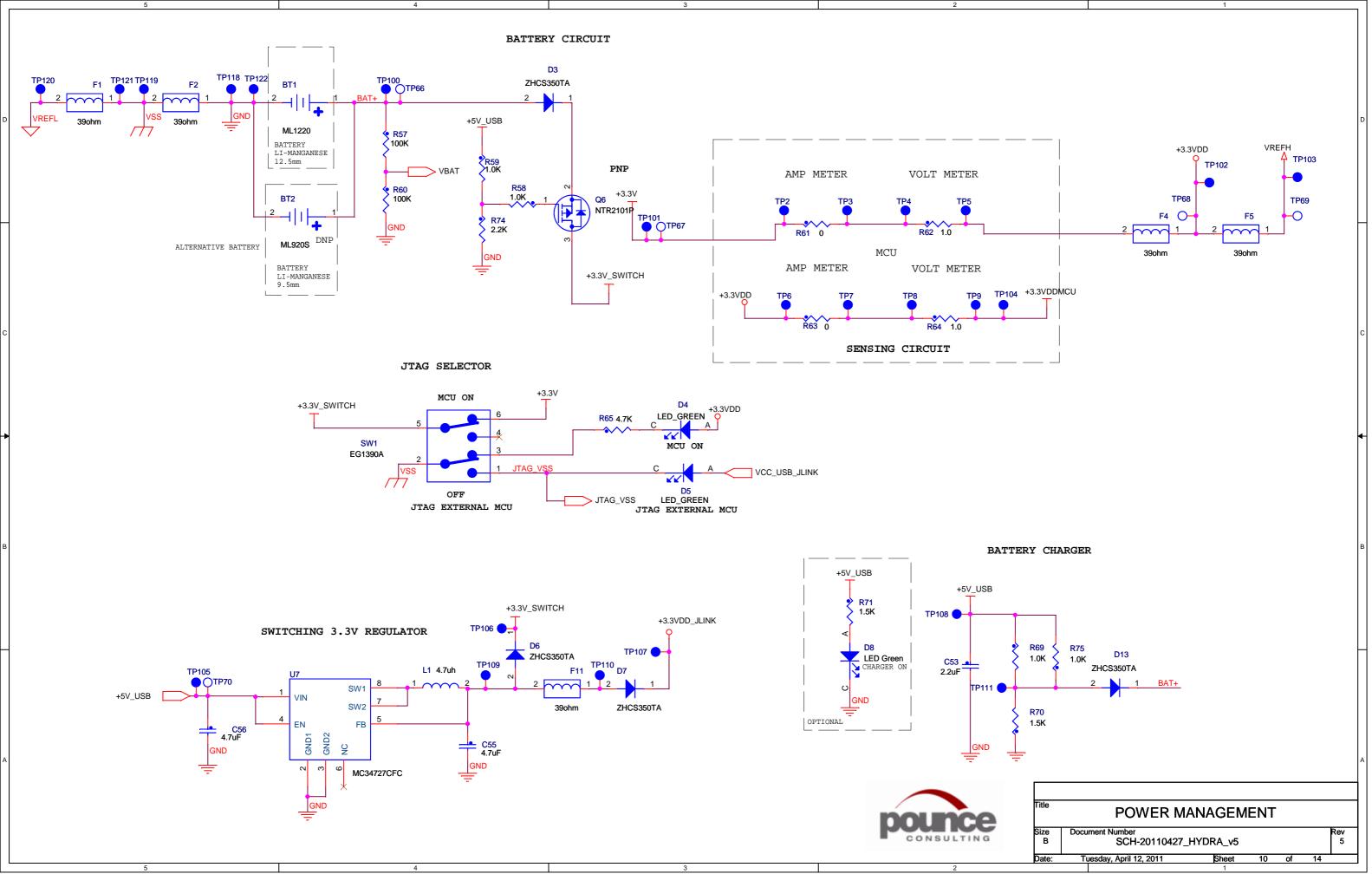


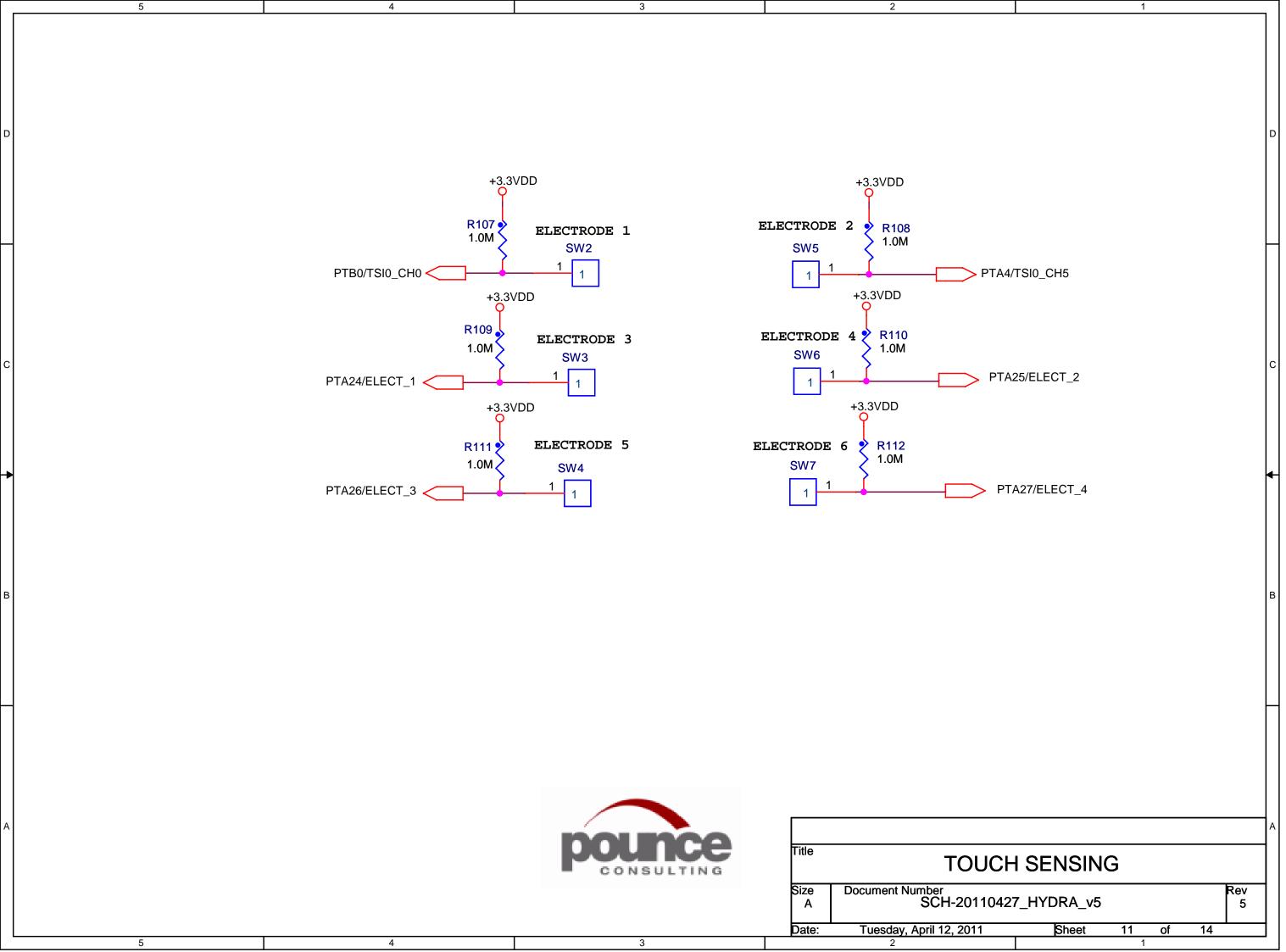


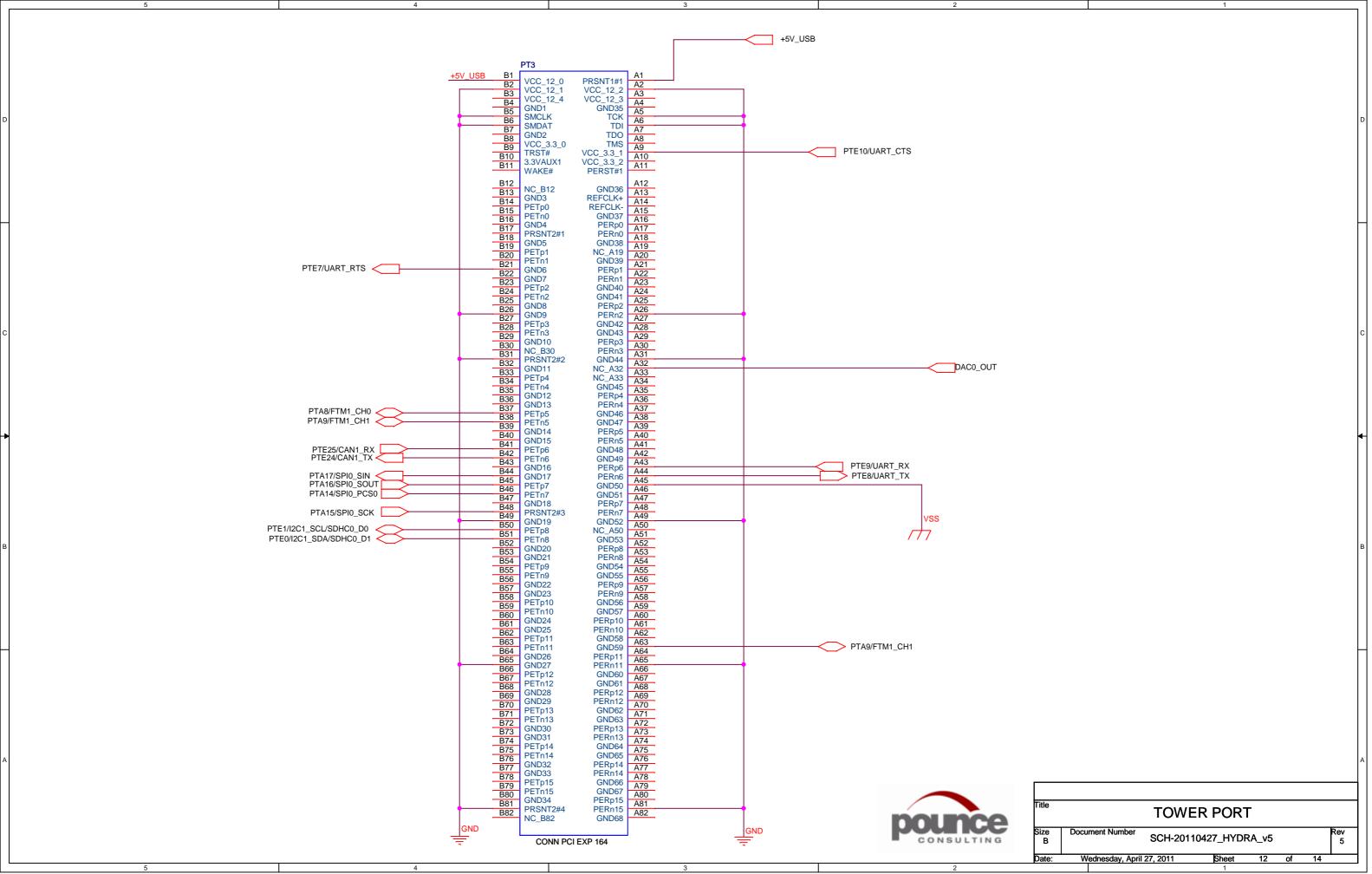


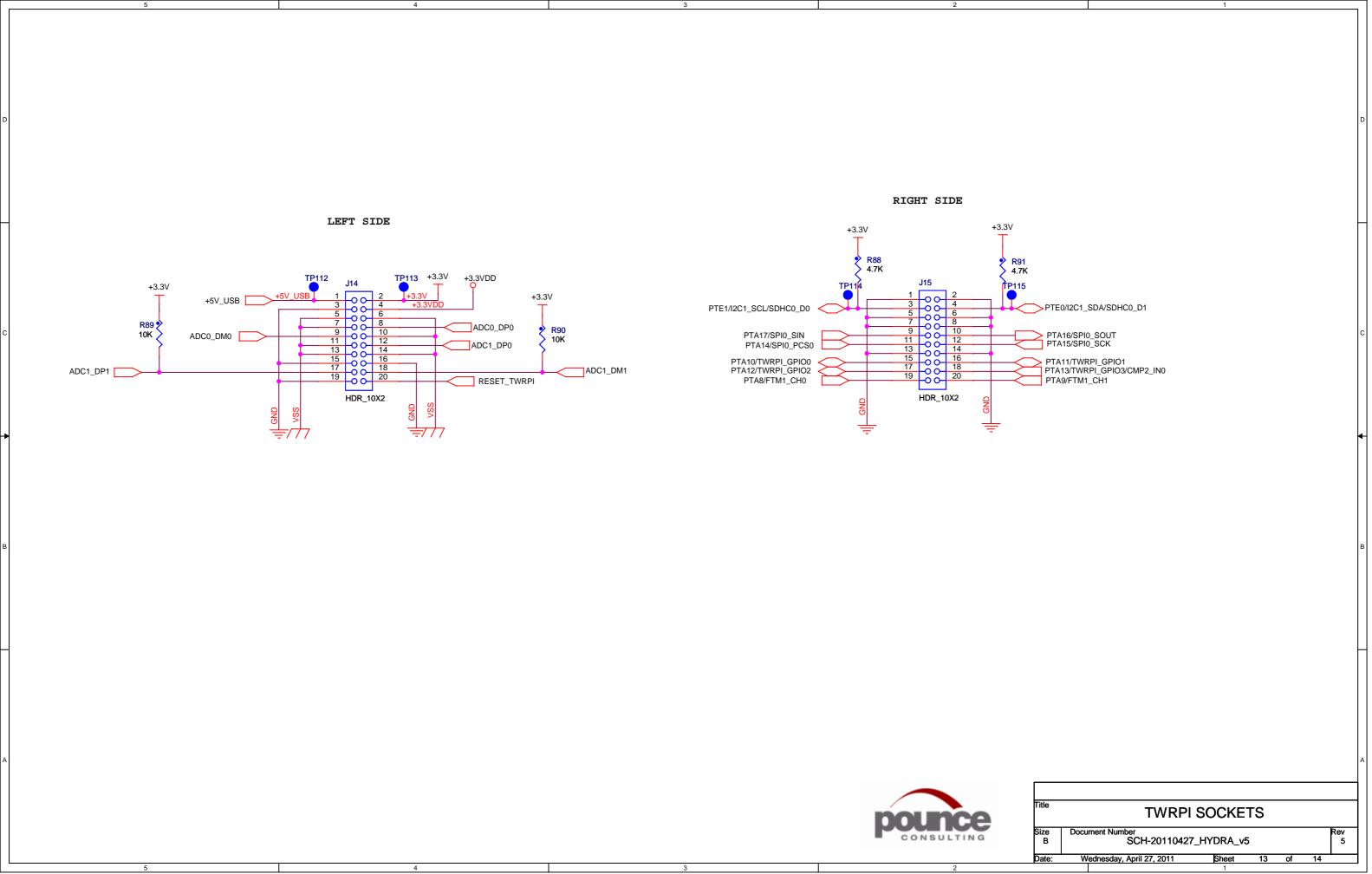


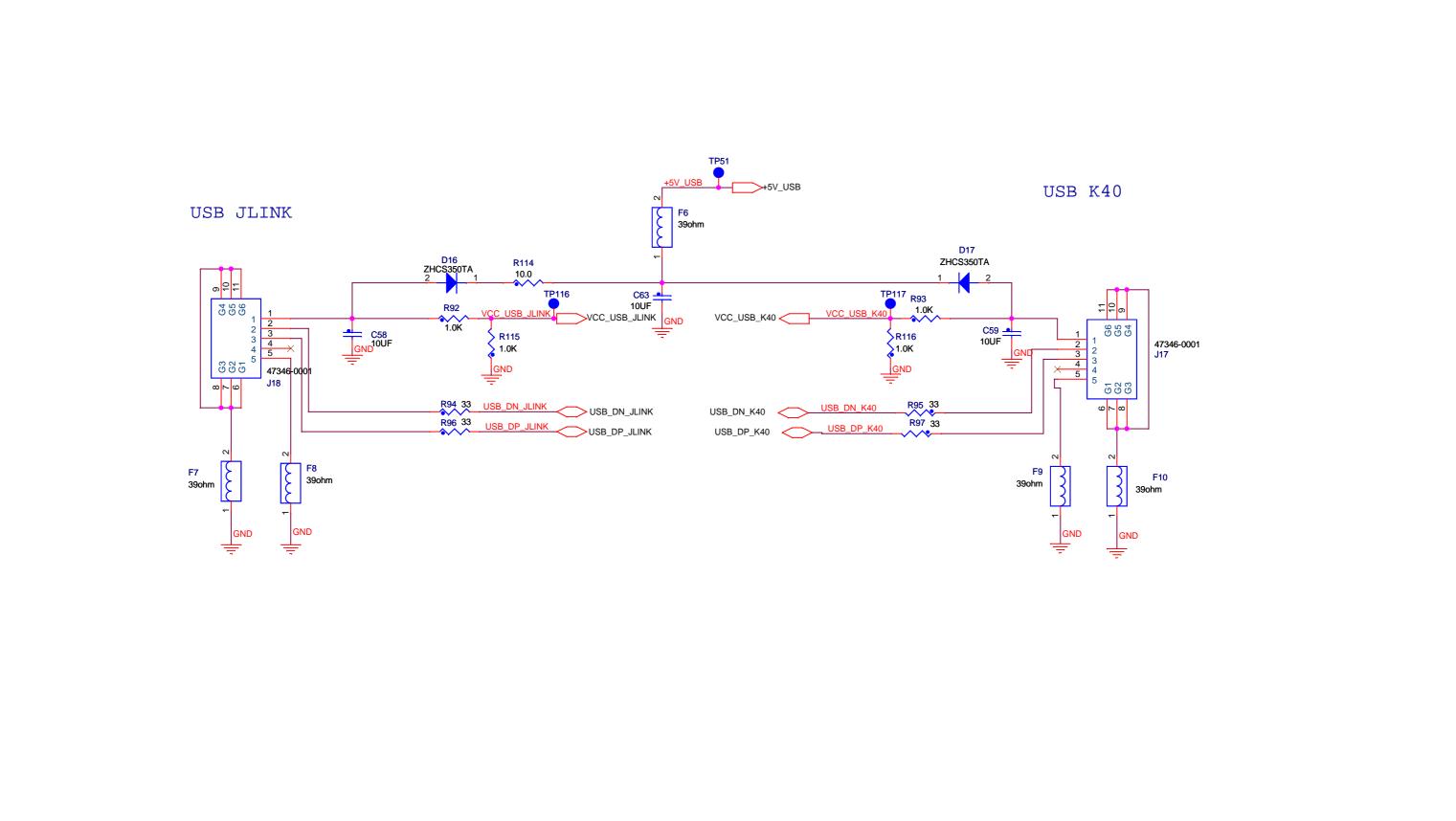














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