1. design a timer.

The output of the timer contains 4-bit hh, 5-bit mm, 6-bit ss, and 1-bit pm.

The inputs of the timer contains 1-bit clk, 1-bit reset, 1-bit load, 4-bit i\_hh, 6-bit i\_mm, 6-bit i\_ss, 1-bit i\_pm. When clk arrives, the timer is increased by 1 second.

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| Timer |  |
| Testfixture |  |
| wave |  |
| run |  |