Samuel Andrew Bondoc

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Campus
TBD (relocation as necessary)

Home
31421 Seminole St.
Temecula, CA 92591

EDUCATION

University of California, Irvine — Entrance: *August 2018*
- **Bachelor of Science in Computer Engineering** — Expected Graduation Date: *June 2021*

SKILLS

Coding – **C | C++ | Python | SQL | JavaScript | Verilog 8051 | MIPS | HTML | CSS**
Tools – **GitHub | Xilinx Vivado | Google Apps Script MySQL | SolidWorks | Cadence Virtuoso**
Interfaces – **Windows | Linux | Raspberry Pi | Arduino Node.js | Koa.js**
Design – **Paint.NET | Canva | Premiere Pro After Effects**

EXPERIENCE

Engineering Course Tutor (UCI Office of Access and Inclusion) *August 2020 – Present*

- Instructed college students on circuit network analysis as well as procedural and assembly programming paradigms. **[C | 8051 | MIPS]**

Media Coordinator (IEEE Student Branch at UCI) *November 2019 – Present*

- Established online presence for the IEEE at UCI club through graphic design as well as newsletter, social media, and form management, increasing engagement by 40%. **[Paint.NET | Premiere Pro | After Effects | Google Apps Script]**

Cybersecurity Workshop Tech Mentor (UCI Office of Access and Inclusion) *May 2021*

- Facilitated a virtual 3-weekend workshop with over 60 participants from colleges across the U.S. in setting up a Linux-based DNS server for query blacklisting. **[Raspberry Pi | Linux]**

Summer Program Assistant (UCI Office of Access and Inclusion) *April 2019 – September 2019*

- Mentored over 100 elementary school to community college-educated students over the course of 8 weeks in app development, software/hardware integration, and mechanical fabrication. **[SolidWorks | GitHub]**

PROJECTS

IoT Irrigation System

- Designed irrigation prototype with DHT-11 humidity/temperature sensor, HW-416 PIR sensor, and I2R LCD with distribution protocol efficiency augmented by CIMIS database retrieval. **[Raspberry Pi | Python]**

FPGA-Compatible MIPS Processor

- Created HDL model for single-cycle processor based on MIPS32 architecture including pipelining and hazard protection functionality with performance analysis on simulated Kintex-7 FPGA. **[Xilinx Vivado | Verilog]**

Mass Data Management System

- Implemented spreadsheet organization script with anti-spam filter utilizing image matching and RegEx generation for an anonymous broadcasting form receiving over 20 submissions per day. **[Google Apps Script | JavaScript]**