Samuel Andrew Bondoc

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Campus

TBD (relocatable)

Home 31421 Seminole St. Temecula, CA 92591

Entrance: August 2018

June 2021

Graduation:

EDUCATION

University of California, Irvine

Bachelor of Science in Computer Engineering

SKILLS

Coding - C | C++ | Python | SQL | JavaScript | Verilog HTML | CSS | Java | 8051 | MIPS | RISC-V

Tools - GitHub | Xilinx Vivado | Google Apps Script
MySQL | SolidWorks | GDB | Valgrind

Interfaces – Git | Windows | Linux | Raspberry Pi Arduino | Node.js | Koa.js

Design – Paint.NET | Canva | Adobe Premiere Pro Adobe After Effects

EXPERIENCE

Engineering Course Tutor

August 2020 - Present

UCI Office of Access and Inclusion

- Taught programming paradigms to 4 students. [C | 8051 | MIPS | RISC-V]
 - Developed memory management visualizations for 1 student. [Paint.NET | C]
 - Instructed 1 student on circuit analysis techniques.

Media Coordinator

November 2019 - Present

IEEE Student Branch at UC Irvine

- Established online presence for the club, increasing engagement by over 40%.
- Designed graphics for, composed, and edited weekly newsletter. [Paint.NET]
- Created and published visual content for online advertising on 4 platforms. [Premiere Pro | After Effects]
- Defined standardized protocols for executive board's new file system with API-style documentation.
- Coordinated with 5 other board members to manage correspondence with over 30 industry tour participants.
- Supervised 2 embedded systems workshops with 25 students each. [Arduino]

Cybersecurity Workshop Tech Mentor

May 2021

UCI Office of Access and Inclusion

- Facilitated virtual 3-weekend seminar with over 60 participants from colleges across the U.S.
- Diagnosed DHCP setup anomalies for query-filtering DNS server. [Raspberyy Pi | Linux]
- Guided beginner Python users through coding a Caesar cypher. [Python]

Summer Program Assistant

April 2019 – September 2019

UCI Office of Access and Inclusion

- Mentored over 80 students for 8 weeks in software/hardware integration. [Python | SolidWorks]
- Formulated JavaScript curriculum for advanced participants. [GitHub | JavaScript | Premiere Pro]

PROJECTS

IoT Irrigation System

- Designed irrigation prototype with DHT-11 sensor, HW-416 PIR sensor, and I2R LCD. [Raspberry Pi | Python]
- Augmented distribution efficiency protocol with CIMIS database retrieval using FTP.

FPGA-Compatible MIPS Processor

- Incrementally implemented core, pipelining, and hazard protection functionality. [Verilog]
- Tested and analyzed power consumption on simulated Kintex-7 FPGA. [Xilinx Vivado]