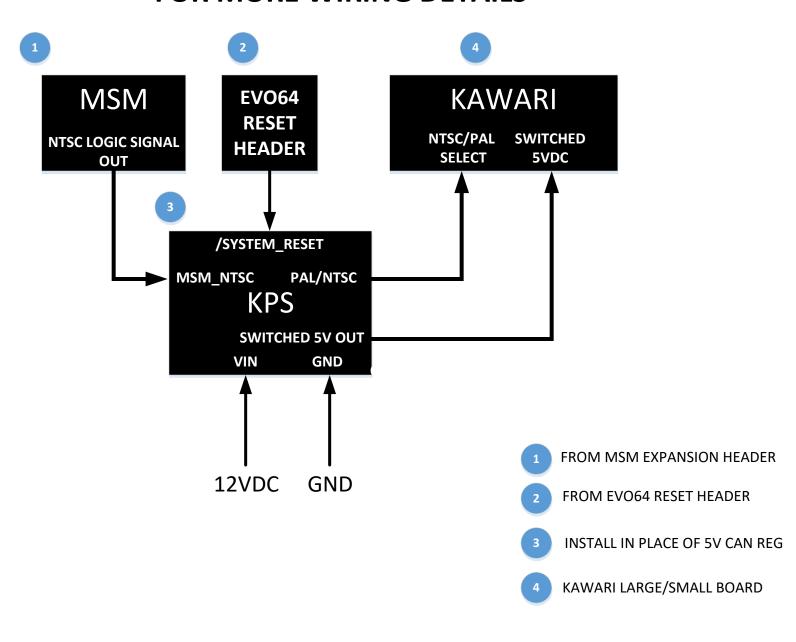
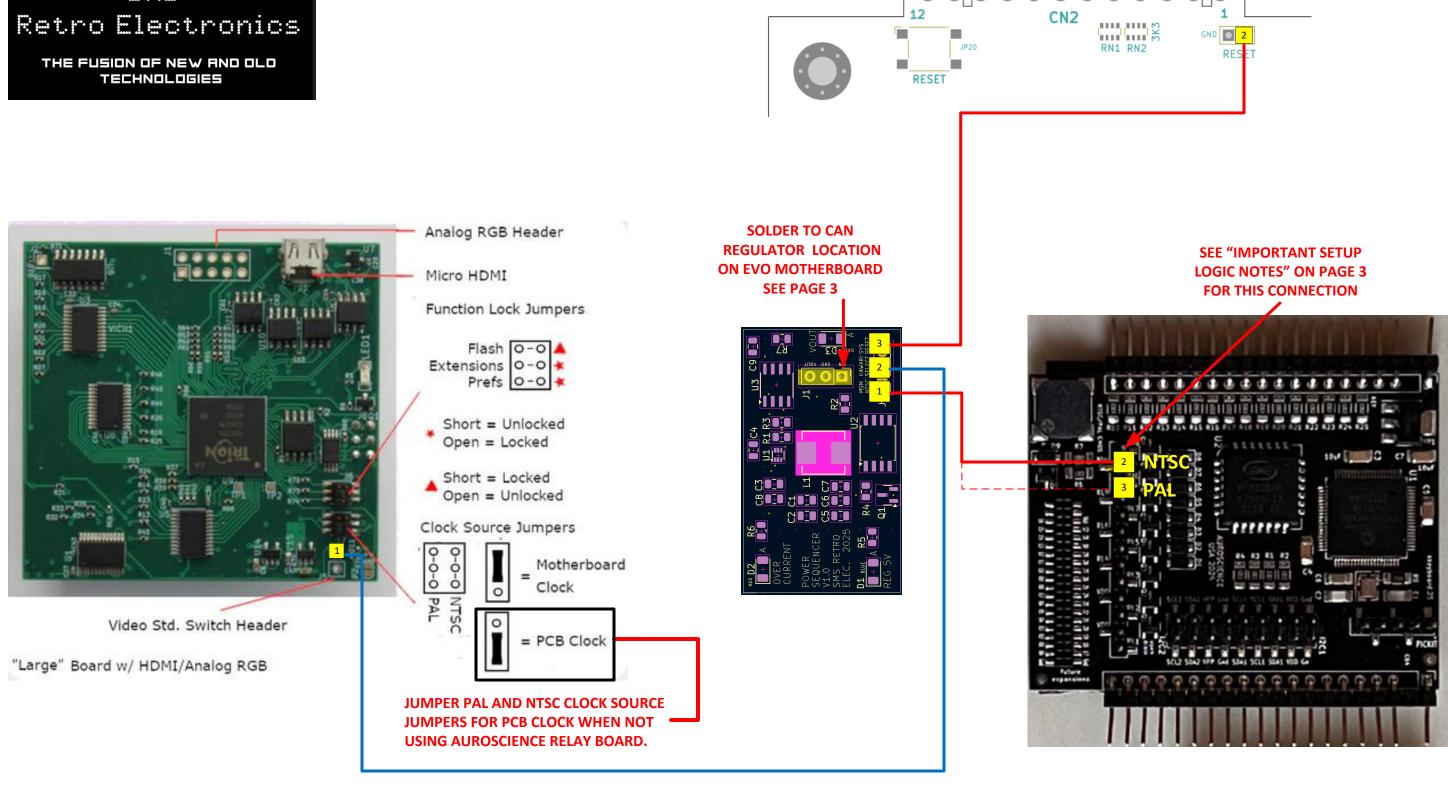


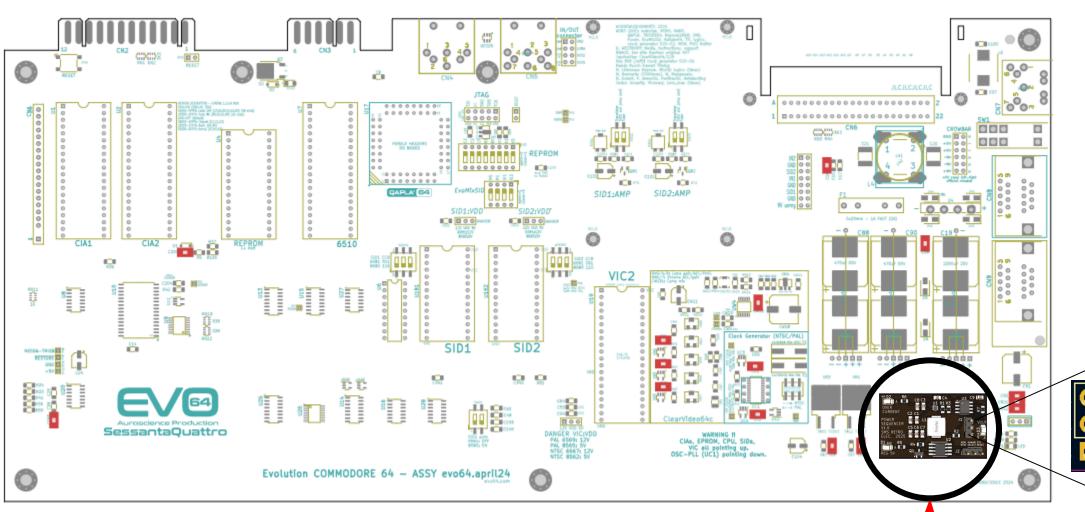
(KPS) KAWARI POWER SEQUENCER INTERCONNECTION (SEE PAGE TWO FOR MORE WIRING DETAILS







MODULE DRAWINGS ARE NOT TO SCLAE FOR CLARITY





KPS INSTALLATION

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IMPORTANT SETUP LOGIC NOTES

The 'switch' header on the Kawari PCB will toggle the chip model between the saved standard (switch open) and the opposite standard (switch closed). Therefore, it is important to wire the right MSM logic output to the KPS J2 PIN 1 which is dependent on your saved standard. See table below.

What's Saved	Swith OPEN	Switch CLOSED	
6567R8 NTSC	6567R8 NTSC	6569R5 PAL-B	USE OU1
6567R56A NTSC	656756A NTSC	6569R1 PAL-B	WIT
6569R5 PAL-B	6569R5 PAL-B	6567R8 NTSC	USE
6569R1 PAL-B	6569R1 PAL-B	6567R56A NTSC	OUT WIT

USE PAL LOGIC
OUTPUT ON MSM
WITH 6567Rx
USE NTSC LOGIC
OUTPUT ON MSM
WITH 6569Rx

SOLDER TO CAN REGULATOR LOCATION ON EVO MOTHERBOARD

