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apachevanilla2

Intel Core i7-8850H testing with a Dell 0WCMKT (1.7.3 BIOS) and Intel UHD 630 3GB on Fedora 32 via the Phoronix Test Suite.



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Test Systems:

Intel Core i7-8850H - Intel UHD 630 3GB - Dell 0WCMKT

Processor: Intel Core i7-8850H @ 4.30GHz (6 Cores / 12 Threads), Motherboard: Dell 0WCMKT (1.7.3 BIOS), Chipset: Intel Cannon Lake PCH, Memory: 16384MB, Disk: PM981 NVMe Samsung 512GB, Graphics: Intel UHD 630 3GB (1150MHz), Audio: Realtek ALC3246, Network: Intel I219-LM + Qualcomm Atheros QCA6174 802.11ac

OS: Fedora 32, Kernel: 5.7.7nocamflow0.7.0+ (x86_64), Desktop: GNOME Shell 3.36.7, Display Server: X Server 1.20.9 + Wayland, Display Driver: modesetting 1.20.9, OpenGL: 4.6 Mesa 20.2.2, Compiler: GCC 10.2.1 20201016 + Clang 10.0.1, File-System: ext4, Screen Resolution: 1920x1080

Environment Notes: MFLAGS= MAKEFLAGS=

Compiler Notes: --build=x86_64-redhat-linux --disable-libunwind-exceptions --enable-__cxa_atexit --enable-bootstrap --enable-cet --enable-checking=release --enable-gnu-indirect-function --enable-gnu-indirect-function

Processor Notes: Scaling Governor: intel_pstate powersave

Security Notes: SELinux + itlb_multihit: KVM: Mitigation of Split huge pages + l1tf: Mitigation of PTE Inversion; VMX: conditional cache flushes SMT vulnerable + mds: Mitigation of Clear buffers; SMT vulnerable + meltdown: Mitigation of PTI + spec_store_bypass: Mitigation of SSB disabled via protl and seccomp + spectre_v1: Mitigation of usercopy/swapgs barriers and __user pointer sanitization + spectre_v2: Mitigation of Full generic retpoline IBPB: conditional IBRS_FW STIBP: conditional RSB filling + srbds: Mitigation of Microcode + tsx_async_abort: Mitigation of Clear buffers; SMT vulnerable

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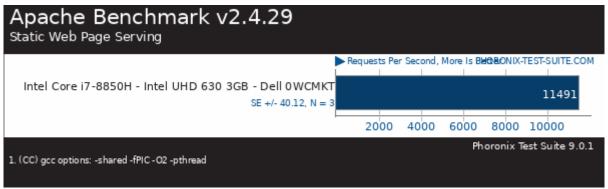
Apache Benchmark - S.W.P.S (Reqs/sec) 11491

Standard Deviation 0.6%

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