

Long Short Term Memory

RNN \rightarrow Long Term Dependency \rightarrow Vanishing Gradient Problem

① RNN \rightarrow Problem

② Why LSTM

③ How LSTM

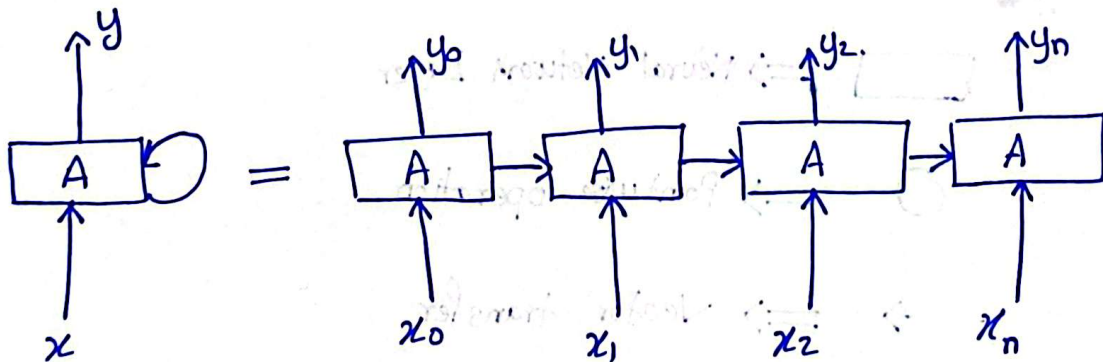
a. Long term memory

b. Short term memory

④ LSTM Architecture

⑤ Working of LSTM

\rightarrow Problems with RNN



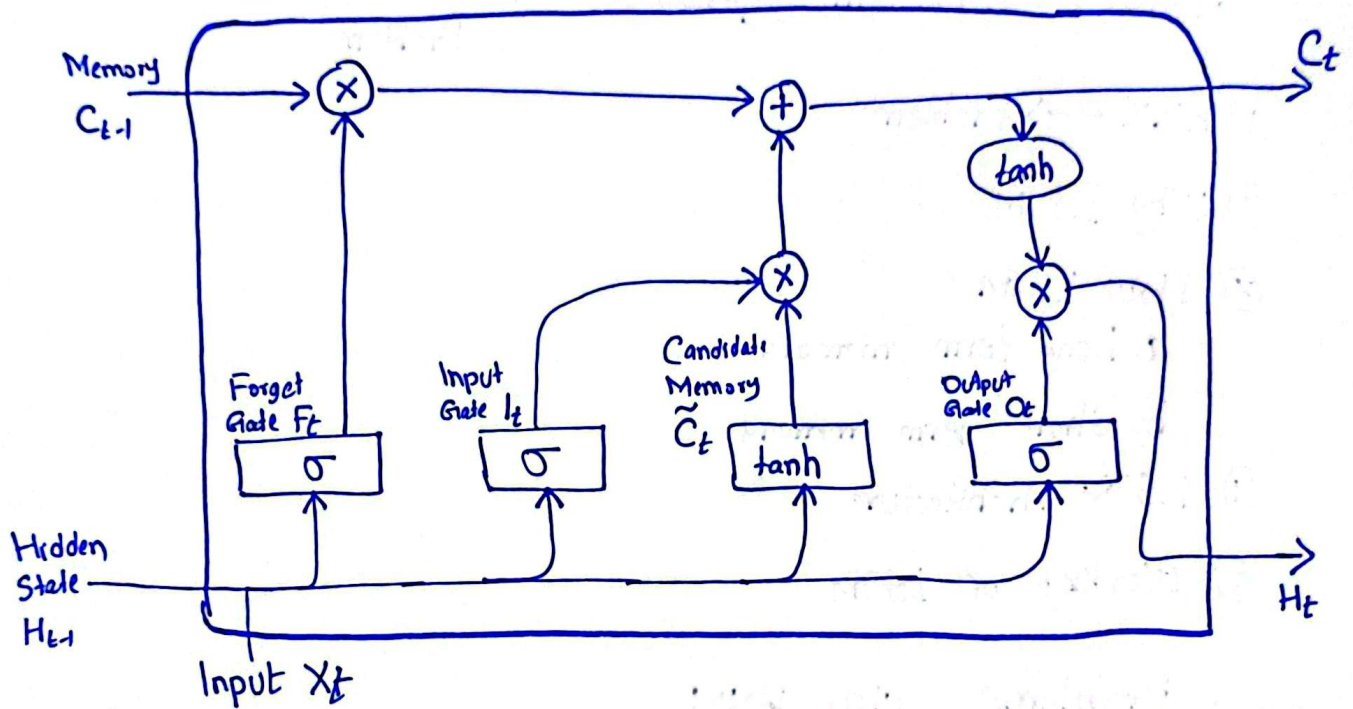
Task:

① The color of sky is blue \Rightarrow No Vanishing Gradient

② Much of the ML research \rightarrow early days of deep learning \Rightarrow context gap is very high

\Downarrow
Long term dependency \Rightarrow Vanishing Gradient

→ LSTM Architecture



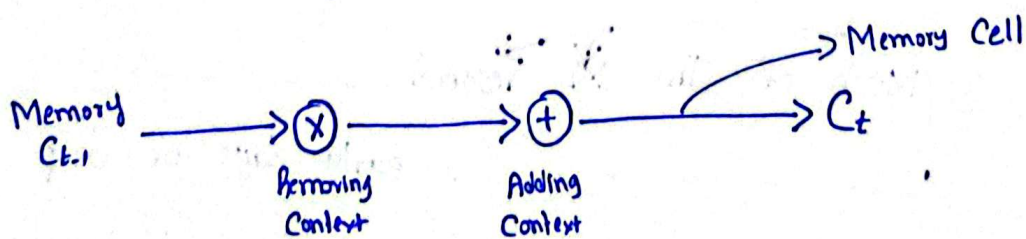
\Rightarrow Neural Network Layer

\Rightarrow Point wise operation

\Rightarrow Vector transfer

\Rightarrow Copy

\Rightarrow Concatenate

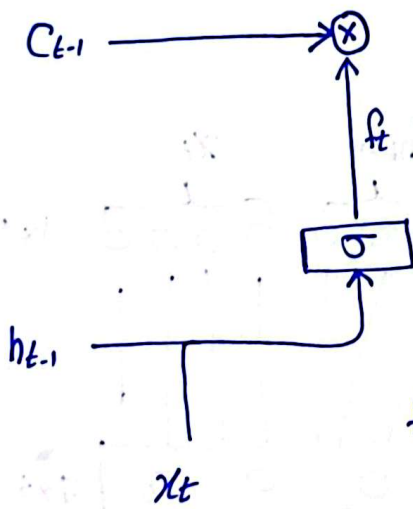


→ Forget Gate

> Forget Gate

h_{t-1} = hidden state of previous time stamp

x_t = word passed as input in the current time stamp



$$x_t [0 \ 2 \ 4 \ 1]$$

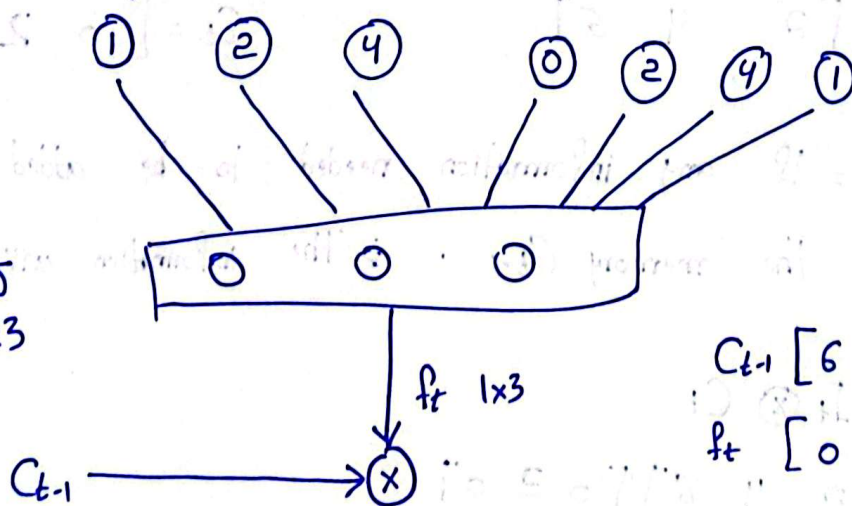
$$h_{t-1} [1 \ 2 \ 4]$$

$$C_{t-1} [4 \ 2 \ 1]$$

$$f_t = \sigma(W[h_{t-1}x_t] + b_f)$$

$h_{t-1} \ x_t$
(1x7)

σ
7x3



$$C_{t-1} [6 \ 8 \ 9]$$

$$f_t [0 \ 0 \ 0]$$

$$[6 \ 8 \ 9] \otimes [1 \ 1 \ 1]$$

$$= [6 \ 8 \ 9]$$

Not removing any content

$$[6 \ 8 \ 9] [0.5, 1, 0.5]$$

$$= [3 \ 8 \ 4.5]$$

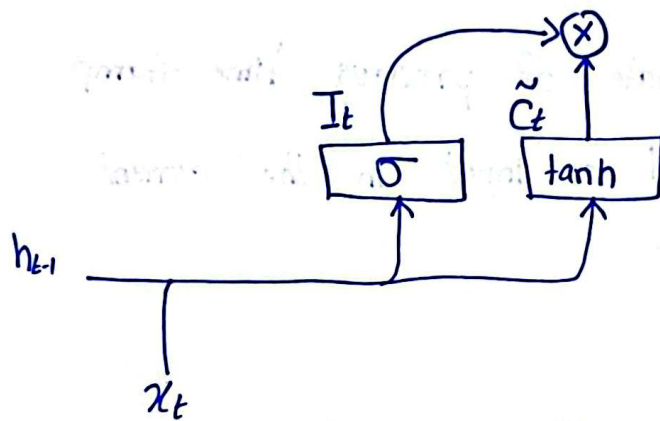
Removing some context

$$C_{t-1} \otimes f_t$$

$$[6 \ 8 \ 9] \otimes [0 \ 0 \ 0]$$

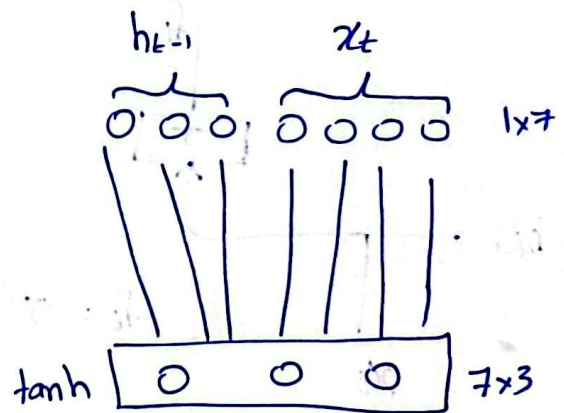
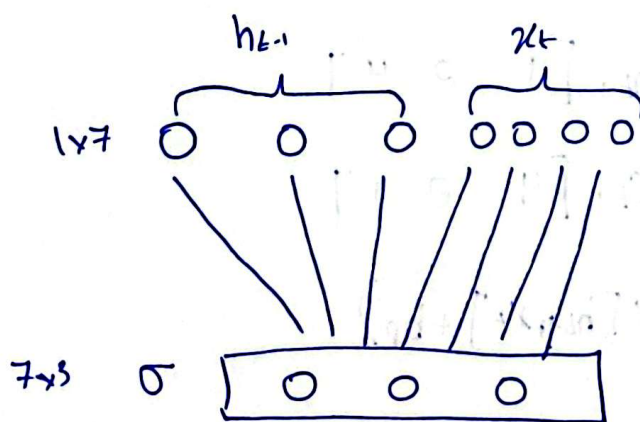
$$= [0 \ 0 \ 0] \text{ Removing all previous context}$$

> Input Gate and Candidate Memory



$$I_t = \sigma(W_i[h_{t-1}, x_t] + b_i)$$

$$\tilde{C}_t = \tanh(W_c[h_{t-1}, x_t] + b_c)$$



$$I_t = \begin{bmatrix} 2 & 4 & 6 \end{bmatrix}$$

$$C_t = \begin{bmatrix} 0 & 2 & 0 \end{bmatrix}$$

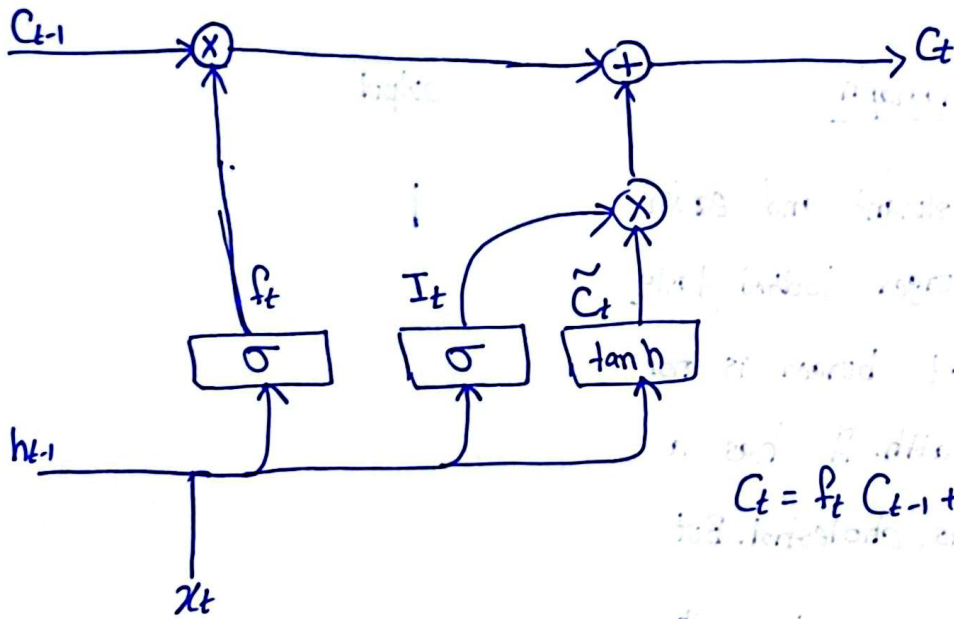
Context = If any information needed to be added in the memory $C_{t-1} \rightarrow$ The information will be added

$$I_t \otimes C_t$$

$$\begin{bmatrix} 2 & 4 & 6 \end{bmatrix} \begin{bmatrix} 0 & 2 & 0 \end{bmatrix}$$

$$\begin{bmatrix} 0 & 8 & 0 \end{bmatrix} \Rightarrow \text{new information is added}$$

> Forget Gate + Input Gate and Candidate Memory

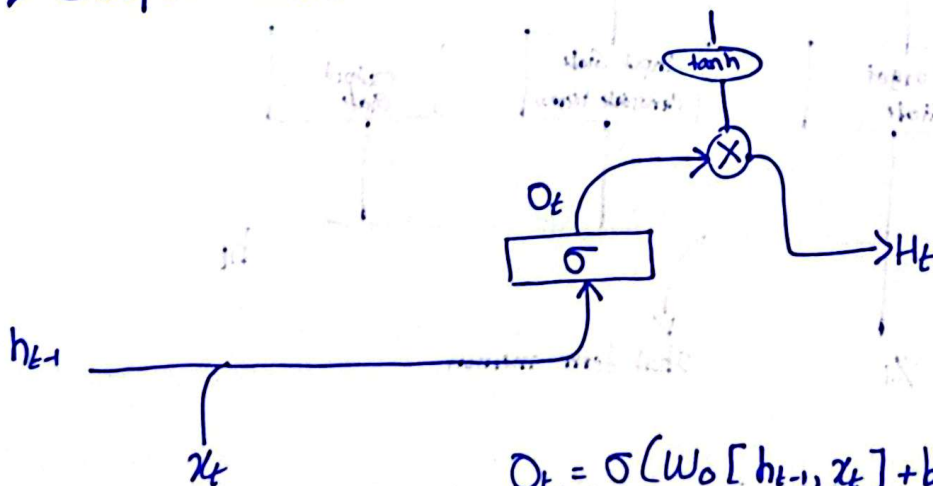


$$C_t = f_t C_{t-1} + I_t \tilde{C}_t$$

$f_t C_{t-1} \Rightarrow$ Removing or Forgetting Some memory

$I_t \tilde{C}_t \Rightarrow$ Adding some memory through candidate memory

> Output Gate



$$O_t = \sigma(W_o[h_{t-1}, x_t] + b_o)$$

$$H_t = O_t * \tanh(C_t)$$