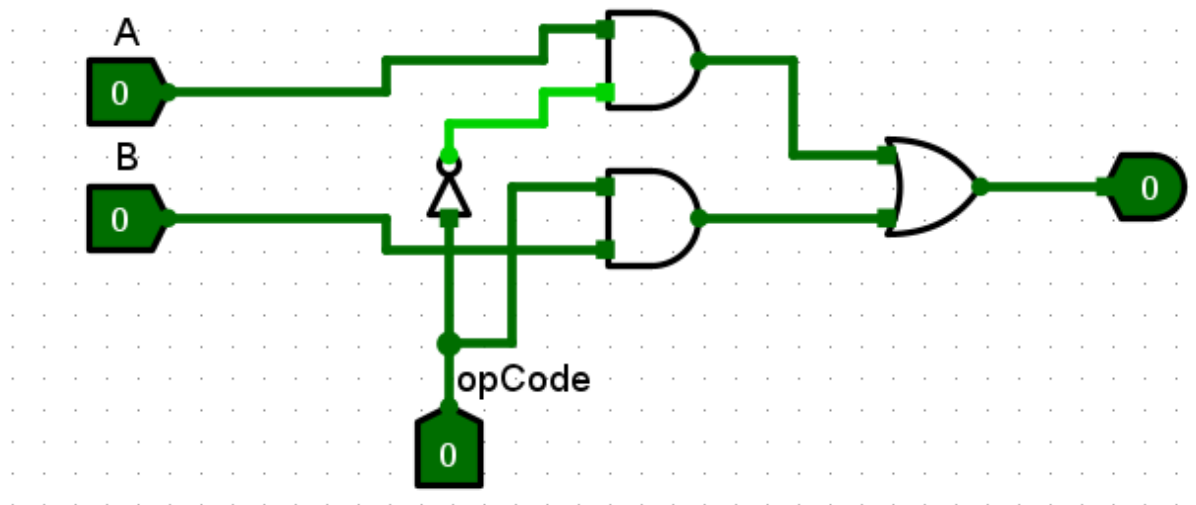
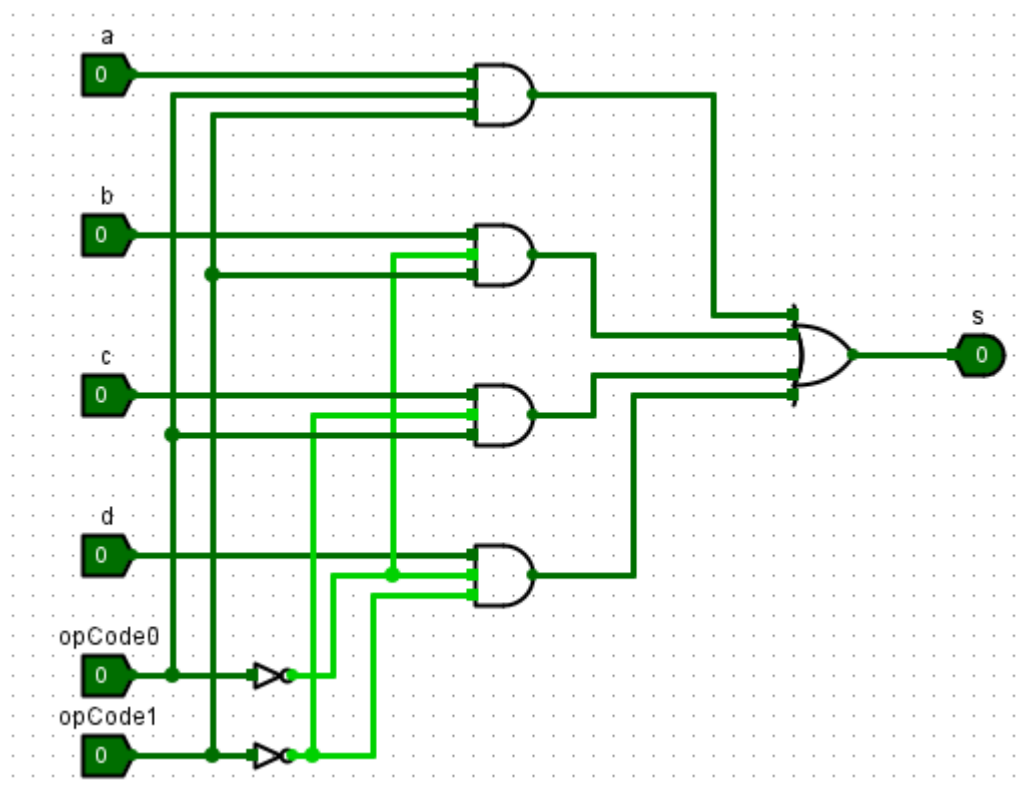


Exercício Prático 2 - AC - II

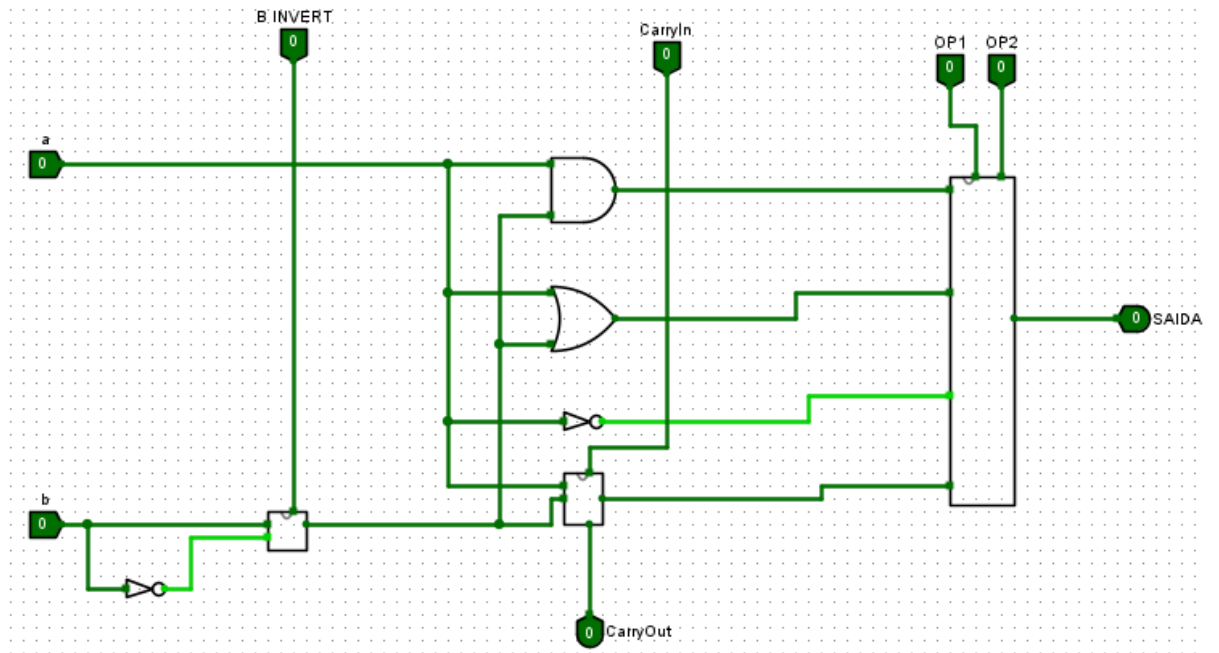
- Mux de 1 bit:



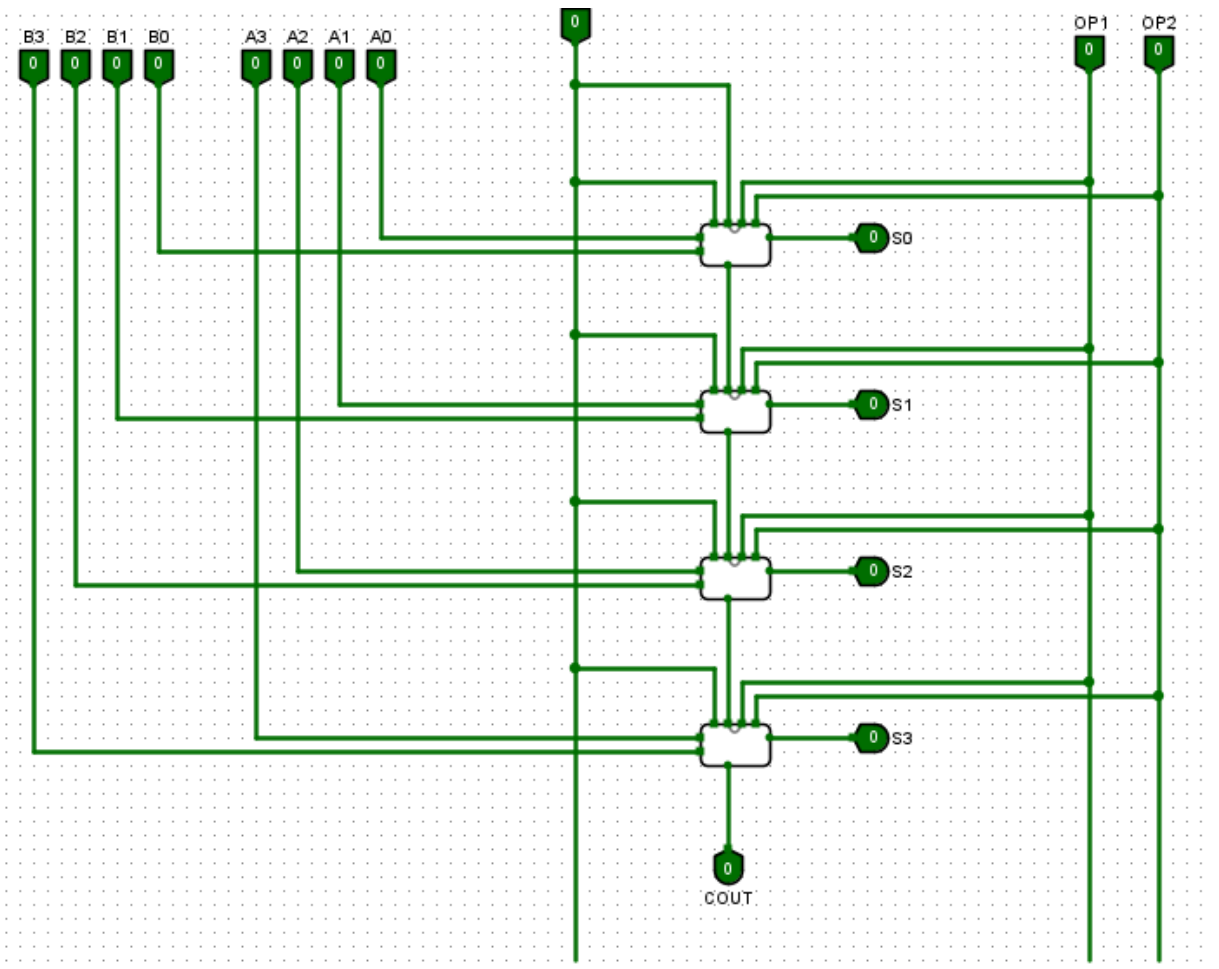
- Mux de 4 bits:



- Ula de 1 bit:



- Ula de 4 bits:

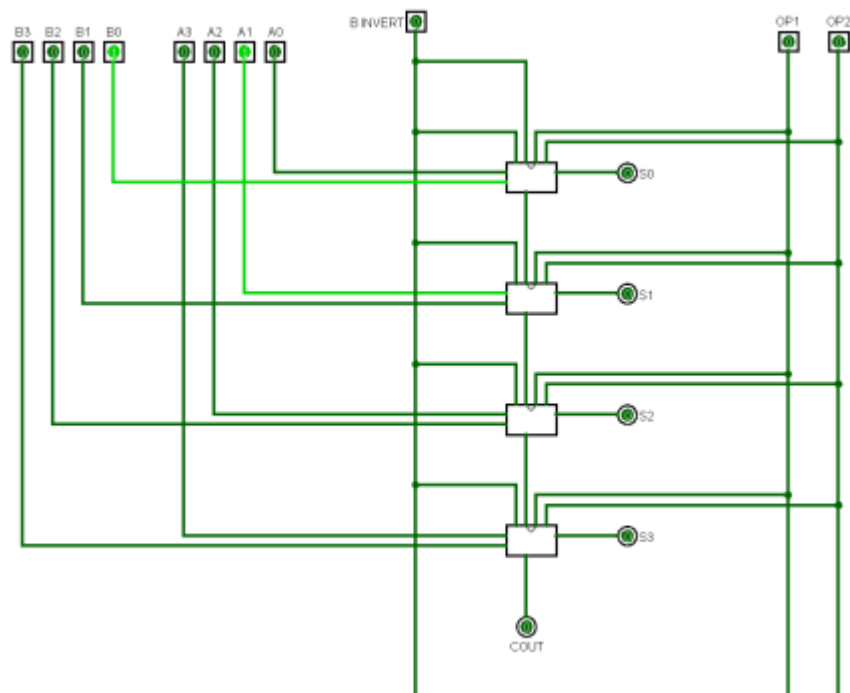


- Tabela de resultados:

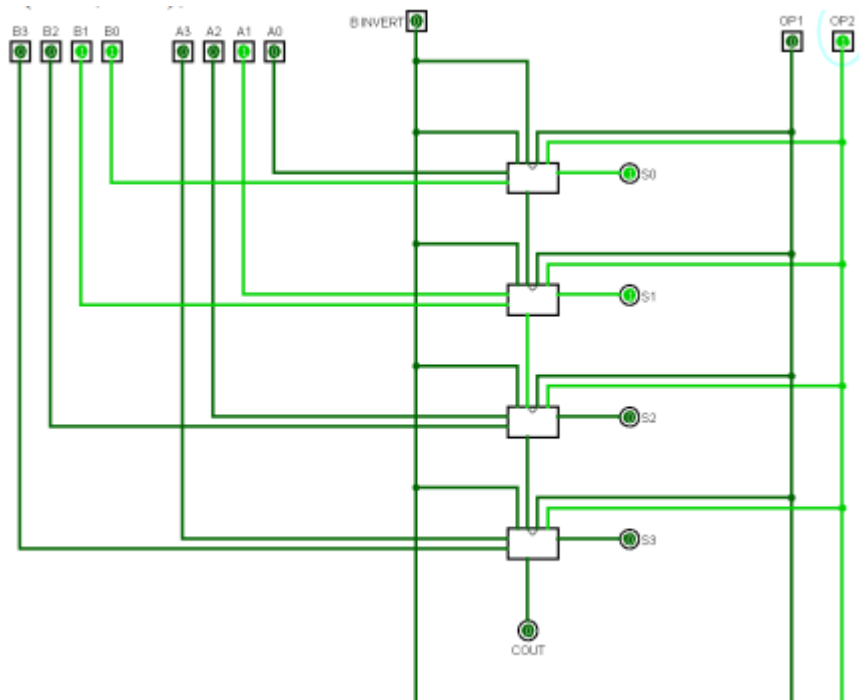
Instrução realizada	Binário (A,B,Op.code)	Valor em Hexa (0x ...)	Resultado em binário
AND(A,B)	0010 0001 00	(0000 1000 0100) = 0x084	0000
OR(A,B)	0010 0011 01	(0000 1000 1101) = 0x08d	0011
SOMA(A,B)	0010 0011 11	(0000 1000 1111) = 0x08f	0101
NOT(A)	1100 0011 10	(0011 0000 1110) = 0x30e	0011
AND(B,A)	1100 1101 00	(0011 0011 0100) = 0x334	1100

- Testes da ula de 4 bits:

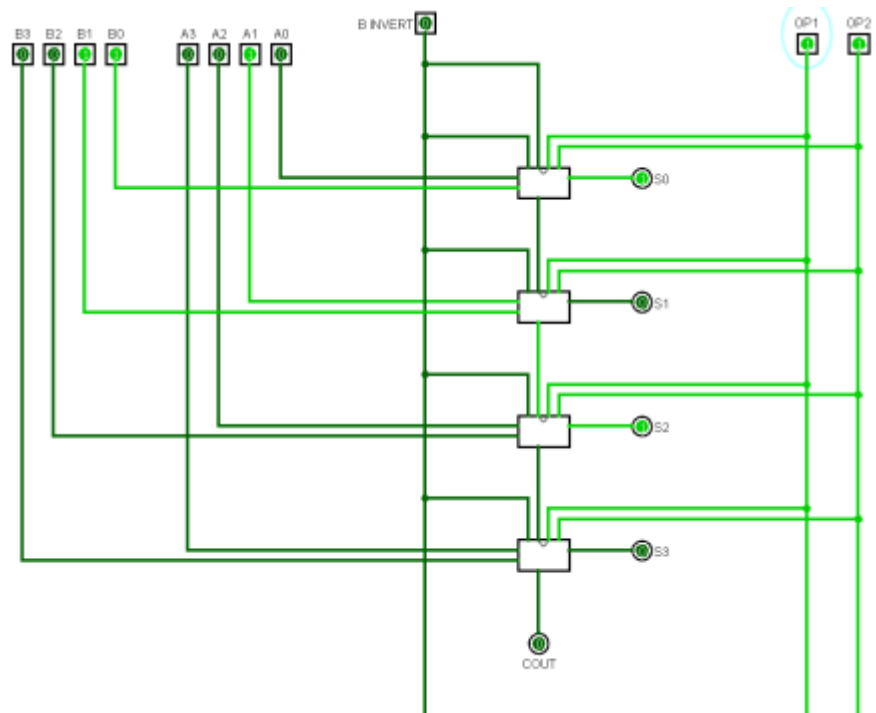
AND:



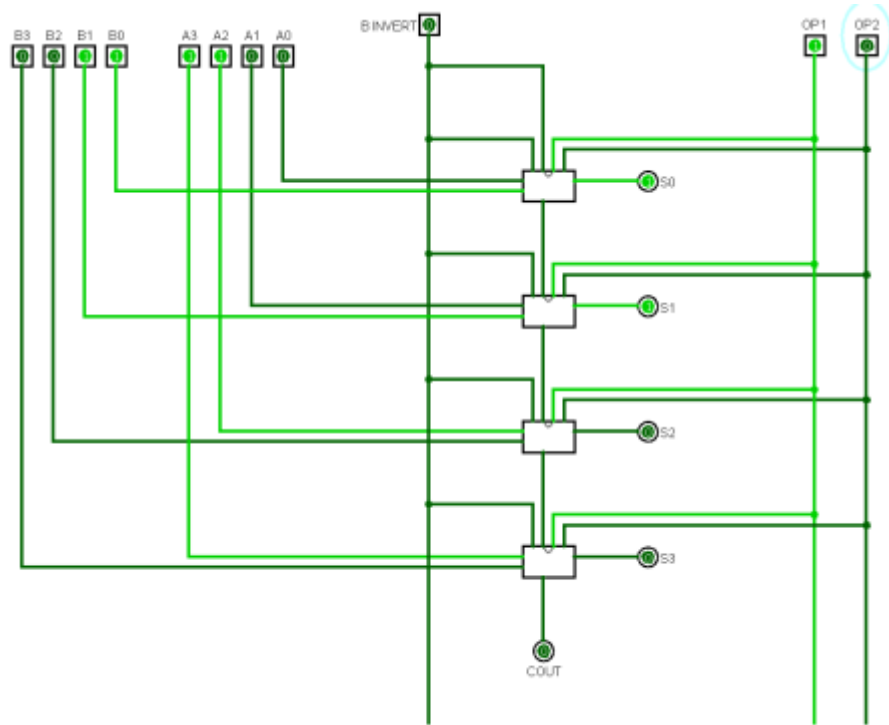
OR:



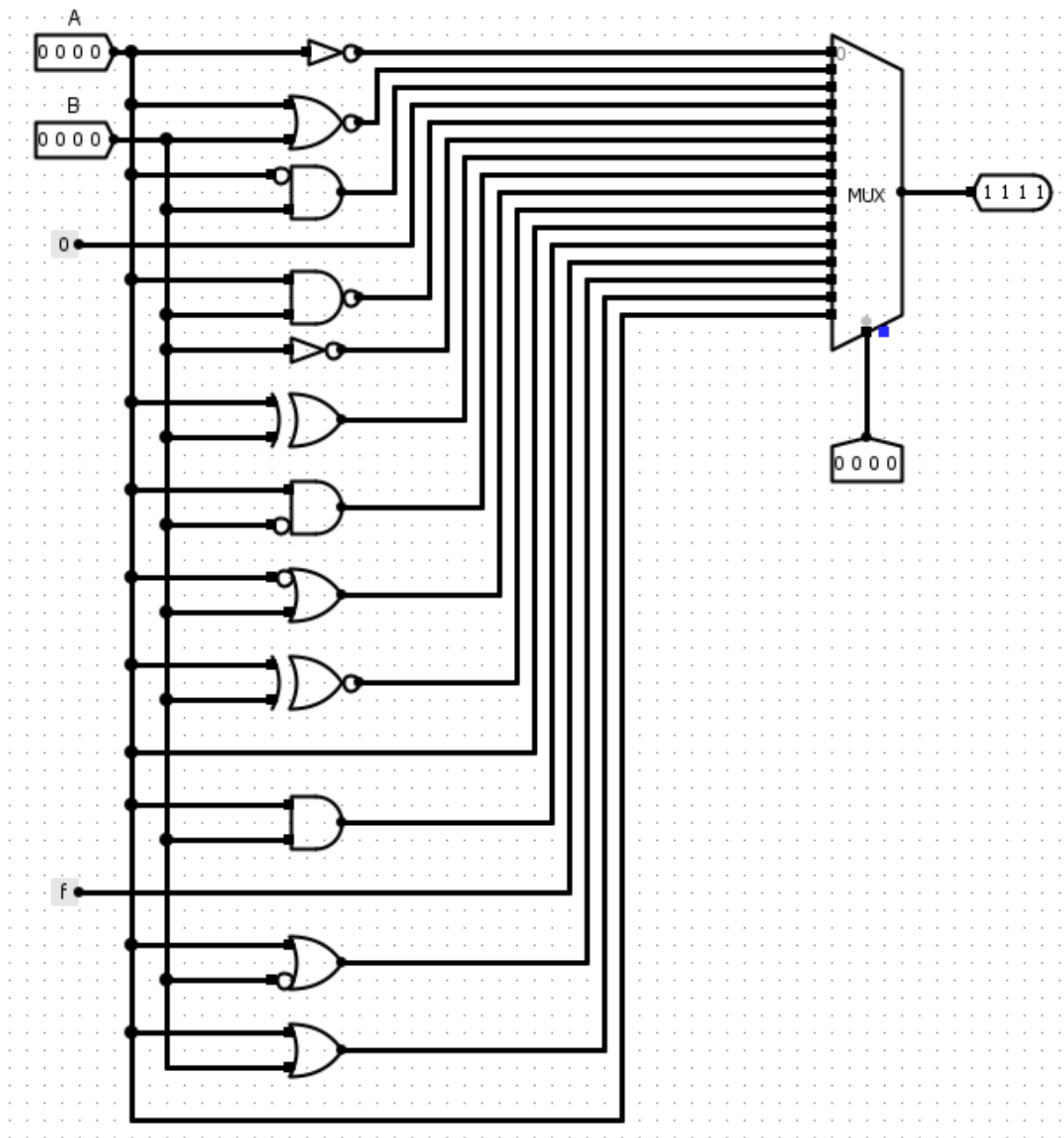
SOMA:



NOT:



Circuito 74181

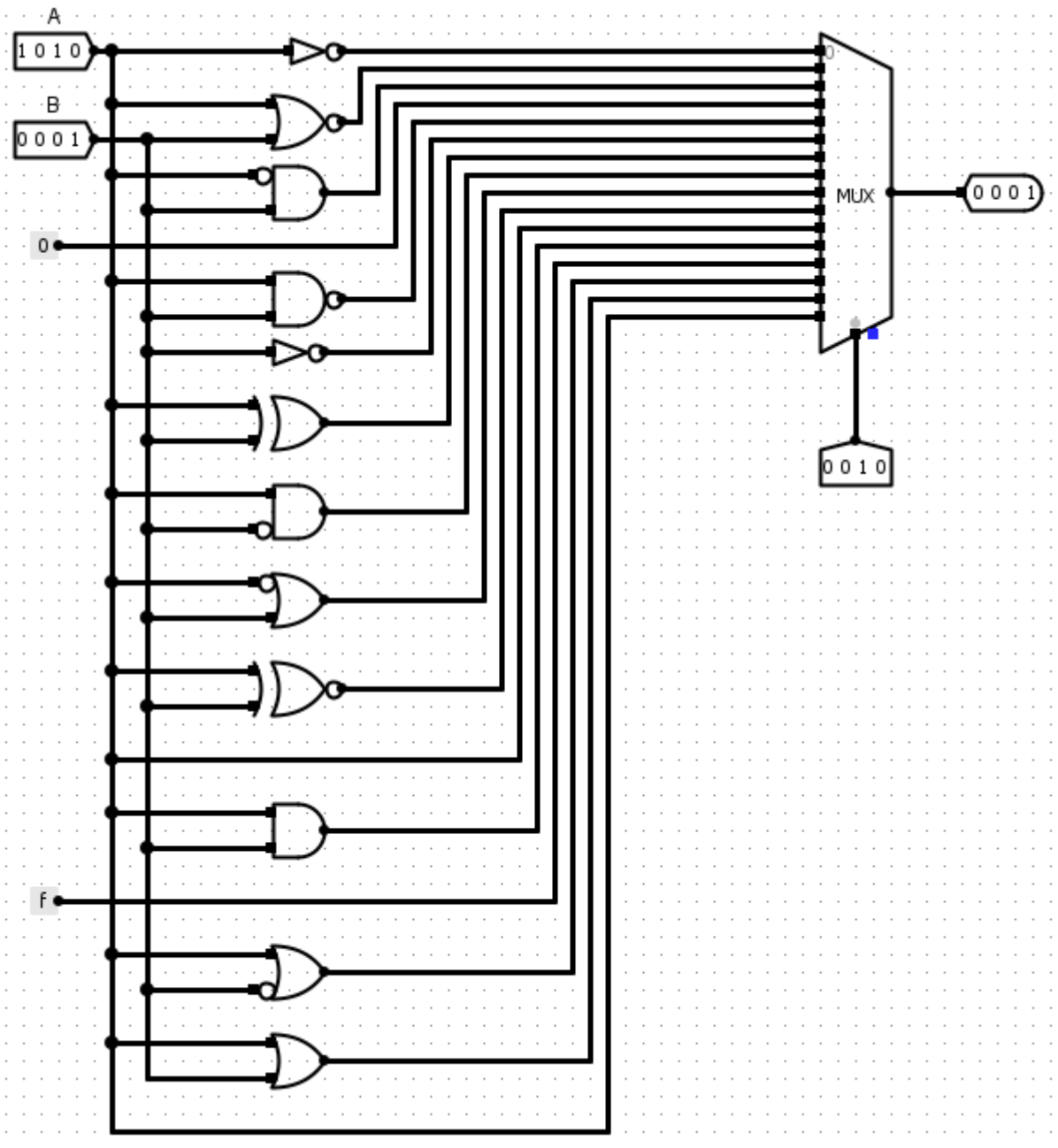


- Tabela:

Instruções	Binário	Resultado da operação
450	0100 0101 0000	1010
CB1	1100 1011 0001	0111
A32	1010 0011 0010	0001
C43	1100 0100 0011	0000
124	0001 0010 0100	1110
785	0111 1000 0101	1000
9B6	1001 1011 0110	0010
CD7	1100 1101 0111	0000
FE8	1111 1110 1000	0001
649	0110 0100 1001	0010
D9A	1101 1001 1010	1001
FCB	1111 1100 1011	1100
63C	0110 0011 1100	1111
98D	1001 1000 1101	1111
76E	0111 0110 1110	0111
23F	0010 0011 1111	0010

- Teste do circuito 74181:

A32:



- Se o objetivo fosse realmente testar esta ULA, quantas linhas a nossa tabela verdade deveria ter, ou seja na verdade a tabela que você preencheu deveria ter quantas linhas?

R.: A tabela verdade deveria ter 4096 linhas, 2^{12} , já que existem 12 portas de entrada no circuito 74181.