Lab #0: Introduction to Electronic Design Automation (EDA) I

Name_____

All Schematics and Screenshots must be uploaded in your CMPE 310 BOX I created for you.
AND PRESENTED to TA before leaving lab.

Pay careful attention to Capture CIS Tutorial. The objective of this project is to walk you through the process of creating schematic design include placing and connecting part, running Design Rules Check, and generating netlist to give you a sense of what we'll be doing in our hardware project.

We keep this schematic design so simple so that by following the tutorial, you'll get a sense of what's involved in working with Capture CIS.

You should complete this lab individually. When you have completed the assignment, raise your hand to get TA's attention, so that you can present your schematic layout to the TA and get your lab signed off before leaving class.

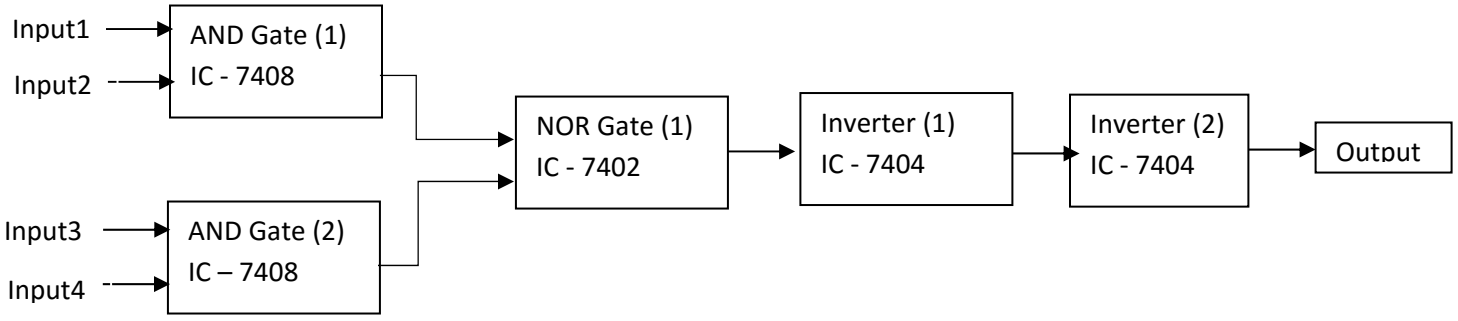
Reading/reference material

- Capture CIS tutorial

Concepts

Illustrate the importance developing skills in using an EDA tool to create schematic design for PCB.

Problem: Following is the circuit block diagram below, create a basic AOI (AND-OR-Inverter) design using the Capture CIS Tool.



All Schematic design and Screenshot must be uploaded in your CMPE 310 BOX I created for you**.