8086 Microprocessor Design Project

CMPE 310

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Contents

1 Introduction

This document provides detailed instructions to develop an 8086 microprocessor board using Cadence® OrCAD® Capture software. Included are the schematics of individual IC components and their description. Details of the ICs include decoding, programming specifications, and descriptions of IC pinouts.

1.1 Purpose

As per the project description, this document is to serve as the only documentation of the operational and functional specifications of the Intel 8086. The documentation is to be thorough and concise to provide information to design a similar board.

1.2 Scope and Organization of Document

This report will elaborate on the integrated circuit (IC) chips used in designing the board, along with brief high level overviews of their pinouts, the different connections

2 8086 Microprocessor

The 8086 microprocessor is an enhanced version of the 8085 microprocessor developed by Intel in 1978. It is a 16-bit microprocessor, with 20 address lines and 16 data lines to provide up to 1 MB of physical memory.

2.1 Description

2.2 Address and Data Buses

2.3 Control Bus

3 Decoding

3.1 Programming Logic Device - 16L8

3.2 Programming the PLD

4 Clock Generator - 8284

4.1 Description

5 Memory Architecture

5.1 Static Random Access Memory - CY7C199

5.2 Interfacing Memory Banks with the Microprocessor

5.3 Addressing

5.4 CMOS Flash Memory - 28F010

5.5 Flash Memory Implementation

5.6 Addressing Flash Memory